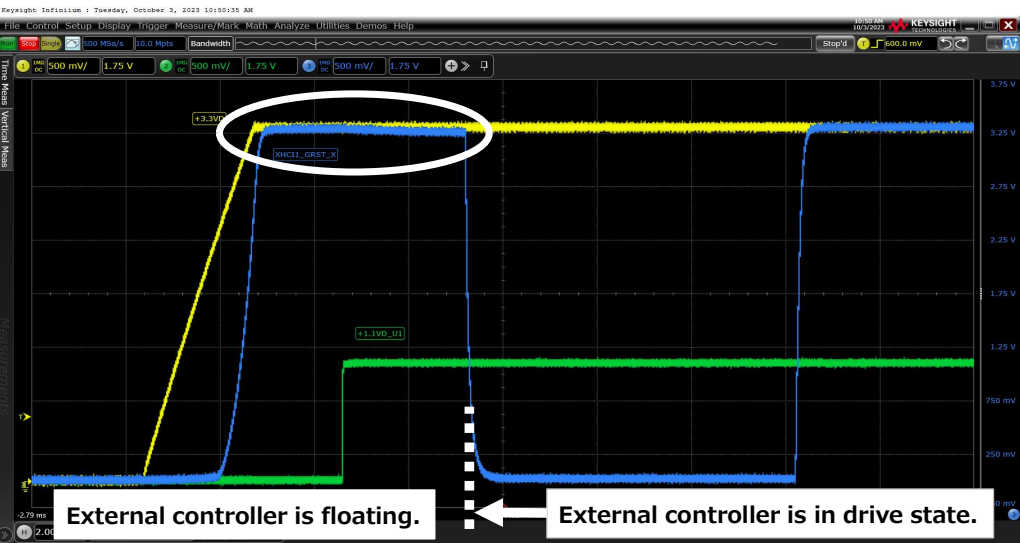
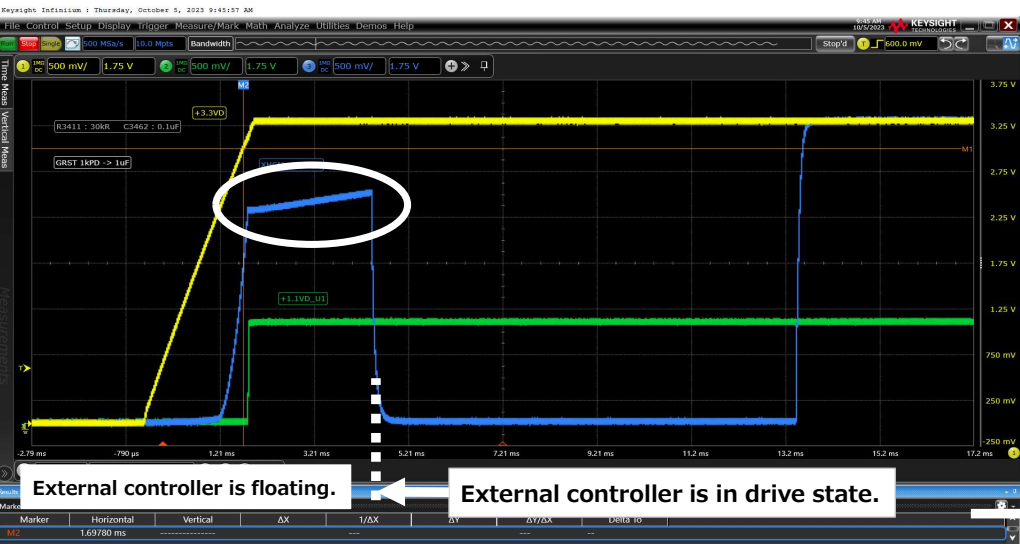
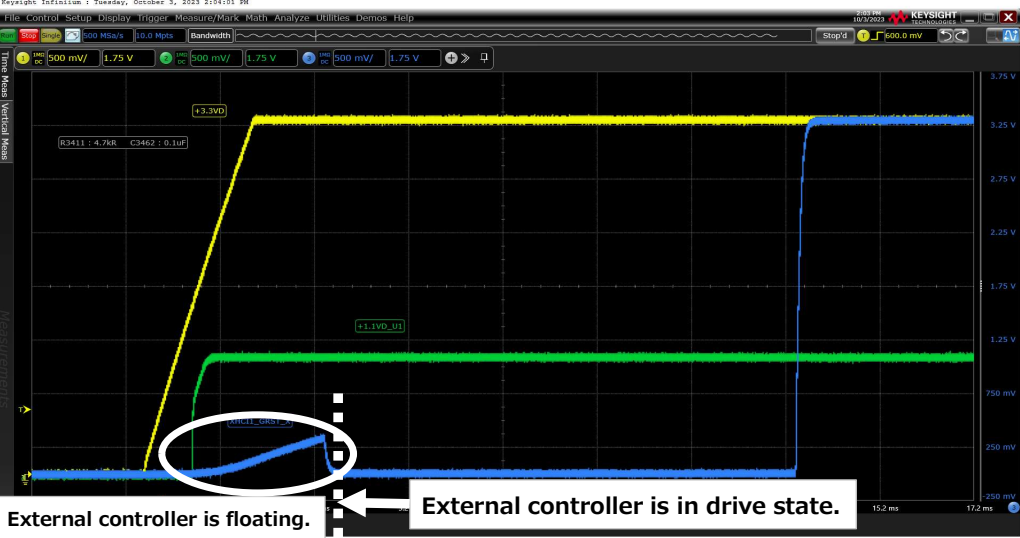


**CH1 : VDD33 (+3.3VD)**  
**CH2 : VDD11 (+1.1VD\_U1)**  
**CH3 : XHC11\_GRST\_X**

Power supply input to VDD33 pin of TUSB7320.  
Power supply input to VDD11 pin of TUSB7320.  
Signal input to the GRST pin of the TUSB7320.



**Connection diagram**



GRST is controlled by an external controller.  
The external controller shall be floating until the power is turned on.  
**Changed parts : 1kΩ→1μF**

**GRST rises differently depending on the 1.1V rise timing.**