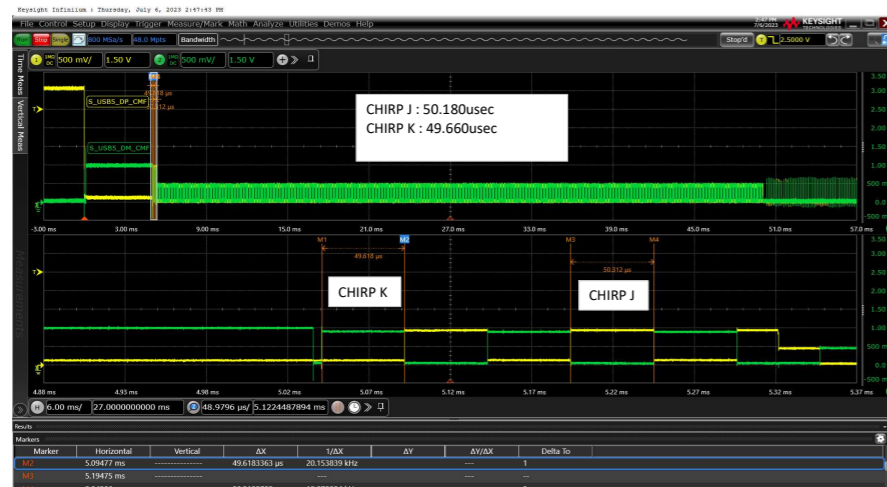


## Successful boot

Signal description

**Ch1 : D+** Output signal from USB\_DP\_DN2 ( B12) pin of TUSB7320.

**Ch2 : D-** Output signal from USB\_DM\_DN2 ( A13) pin of TUSB7320.

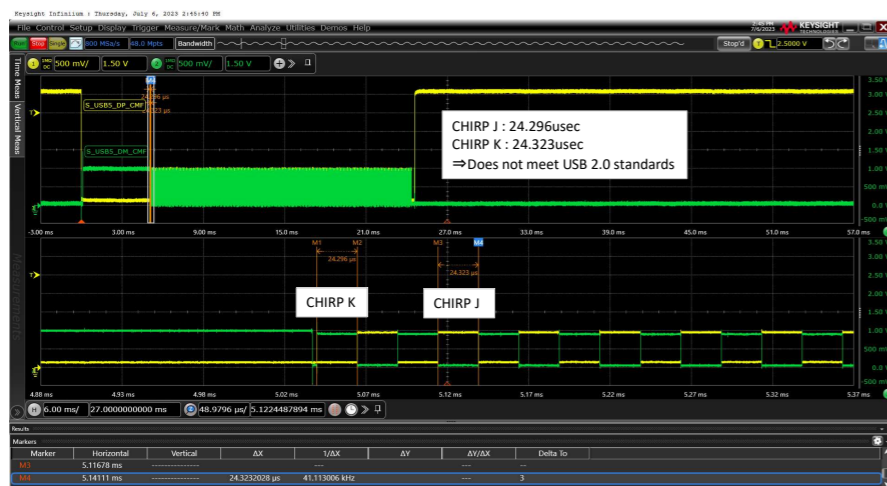


## Failure boot

Signal description

**Ch1 : D+** Output signal from USB\_DP\_DN2 ( B12) pin of TUSB7320.

**Ch2 : D-** Output signal from USB\_DM\_DN2 ( A13) pin of TUSB7320.



## measuring point

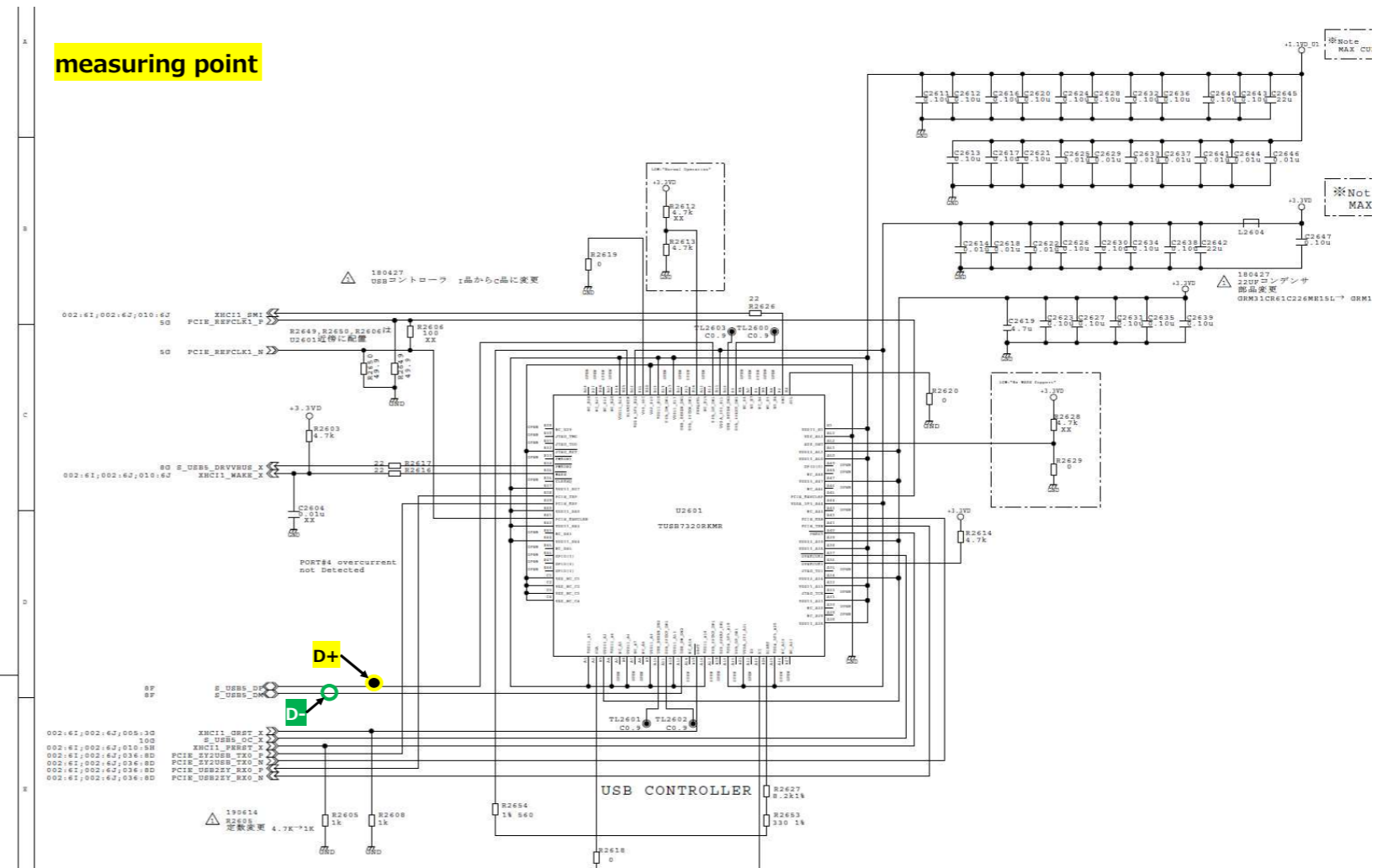


Table 7-13. Hub Event Timings (Continued)

Event Description	Symbol	Conditions	Min	Max	Unit
Inter-packet delay for device/root hub response w/detachable cable for high-speed	THSRSPID1	Section 7.1.18.2		192	bit times
<b>Reset Handshake Protocol:</b>					
Time for which a Chirp J or Chirp K must be continuously detected (filtered) by hub or device during Reset handshake	TFILT	Section 7.1.7.5	2.5		$\mu$ s
Time after end of device Chirp K by which hub must start driving first Chirp K in the hub's chirp sequence	TWTDCH	Section 7.1.7.5		100	$\mu$ s
Time for which each individual Chirp J or Chirp K in the chirp sequence is driven downstream by hub during reset	TDCHBIT	Section 7.1.7.5	40	60	$\mu$ s
Time before end of reset by which a hub must end its downstream chirp sequence	TDCHSE0	Section 7.1.7.5	100	500	$\mu$ s

Reference destination :  
<https://www.usb.org/document-library/usb-20-specification>  
 Universal Serial Bus Specification Revision 2.0