



Table 6. Configuration Select (MODE_SEL)

MD	Mod Num (Pdb/Vdd)	Mod Val (Y)	Required Resistor R ₁ (kΩ, 1% tol)	Required Resistor R ₂ (kΩ, 1% tol)	REFPAT	EXWID	DIR_L	DIR_H	LDR
1	0	0	0P0N	40.2	L	L	L	L	L
2	0.006	0.007	29.4	4.02	L	L	H	L	L
3	0.104	0.140	25.5	4.99	H	L	L	L	L
4	0.223	0.277	26.7	7.88	H	L	H	L	L
5	0.268	0.343	25.5	10.2	L	L	L	H	H
6	0.305	1.205	22.6	13.0	L	L	H	H	H
7	0.446	1.472	26.5	16.5	H	L	L	H	H
8	0.541	1.798	16.2	19.1	H	L	H	H	H
9	0.629	2.075	12.4	21.0	L	H	L	L	L

Table 7. Serial Control Bus Addresses for ICs

IC	IDEAL RATIO (Pdb/Vdd)	IDEAL Val (Y)	SUGGESTED RESISTOR R ₁ (kΩ, 1% tol)	SUGGESTED RESISTOR R ₂ (kΩ, 1% tol)	ADDRESS F ₁	ADDRESS F ₂
1	0	0	0P0N	40.2	0x0	0x0
2	0.002	0.006	25.6	6.78	0x3	0x6
3	0.040	1.117	21.6	11.3	0x4	0x6
4	0.080	1.292	20.0	19.7	0x5	0x6
5	0.419	1.413	19.7	14.9	0x7	0x0
6	0.476	1.676	17.4	16.8	0x7	0x6
7	0.517	1.797	16.4	16.5	0x9	0x0
8	0.601	1.846	16.0	16.1	0x9	0x7
9	0.693	1.987	13.7	21.9	0x9	0x4
10	0.783	2.058	9.99	30.1	0x9	0x9