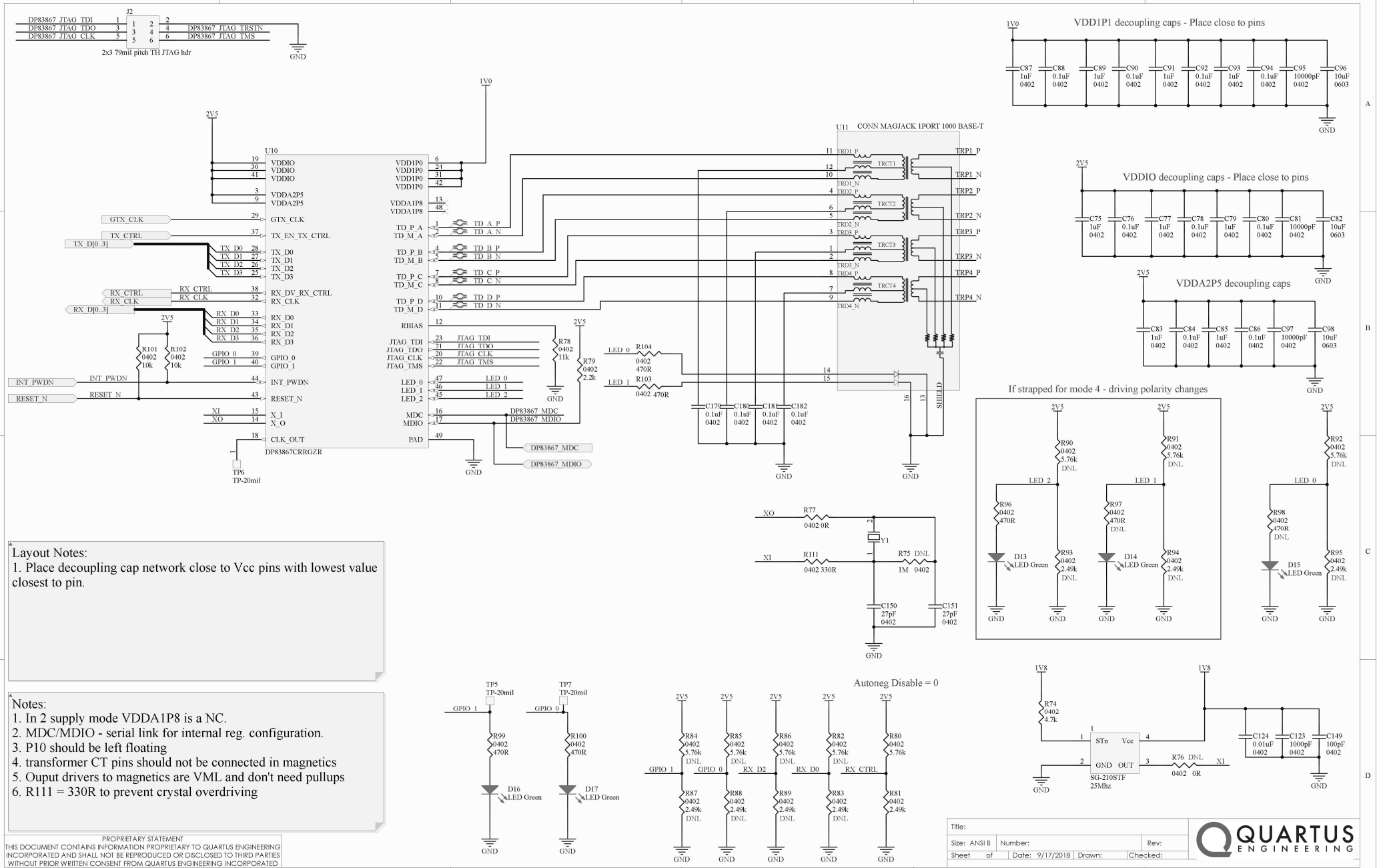

Layout Notes:
1. Place decoupling cap network close to Vcc pins with lowest value closest to pin.

Notes:
1. In 2 supply mode VDDA1P8 is a NC.
2. MDC/MDIO - serial link for internal reg. configuration.
3. P10 should be left floating
4. transformer CT pins should not be connected in magnetics
5. Ouput drivers to magnetics are VML and don't need pullups
6. R111 = 330R to prevent crystal overdriving

PROPRIETARY STATEMENT
THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO QUARTUS ENGINEERING INCORPORATED AND SHALL NOT BE REPRODUCED OR DISCLOSED TO THIRD PARTIES WITHOUT PRIOR WRITTEN CONSENT FROM QUARTUS ENGINEERING INCORPORATED

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