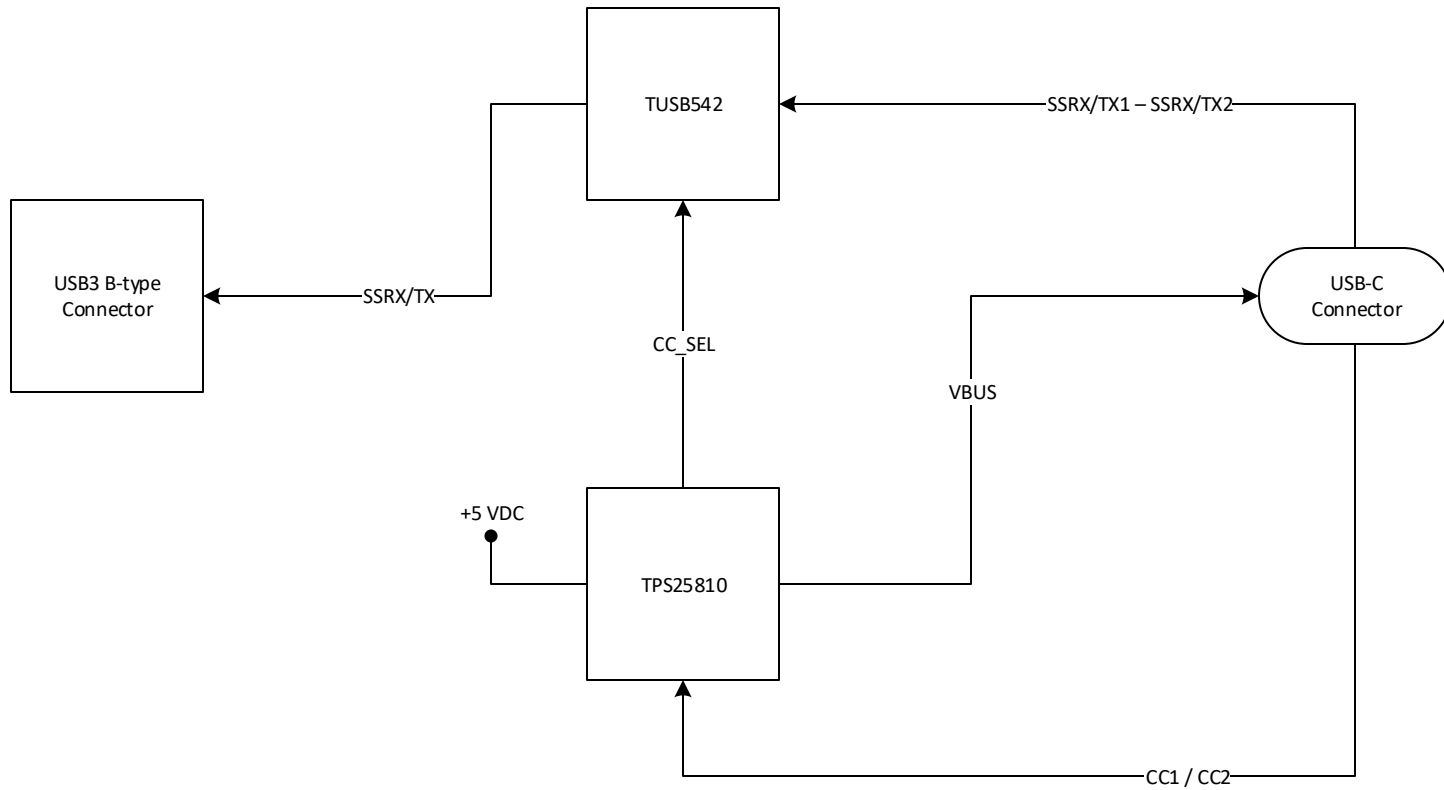


SIMPLIFIED USB CIRCUIT BLOCK DIAGRAM



TEST PLATFORM DIAGRAM

