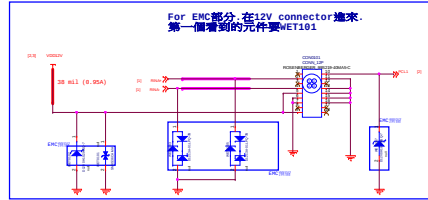
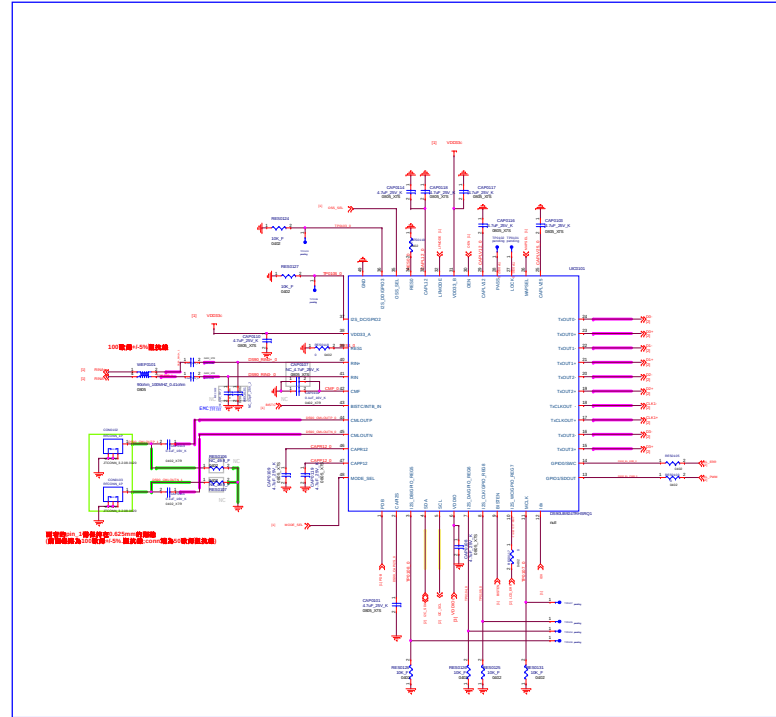


Line	Note
100 ohm line	
Differential 100 ohm line	
I/O Signal	Equal length (GND protect on 4 side)
Control / data signal	GND protect on 2 side
Clock Sensitive signal	GND protect on 4 side
Power plane	

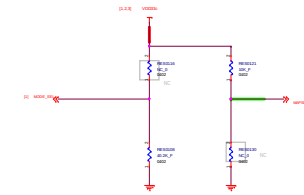
HSD CONN



Controller

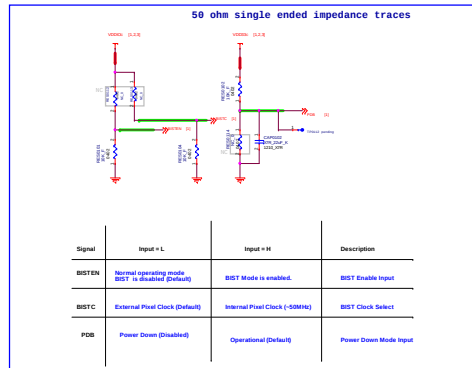


MODE_SEL/MAPSEL Control

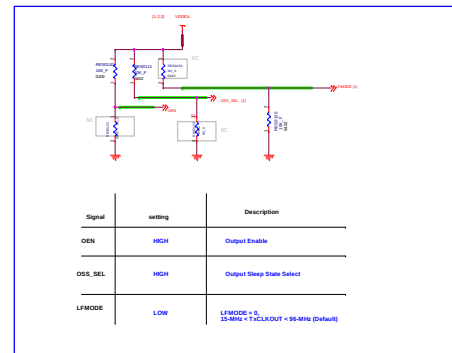


Signal	setting	Description
MODE_SEL	REPEAT OPEN REPEAT 48.240	REPEAT 48.240 L
MAPSEL	HIGH	MSBs on T4OUT3x

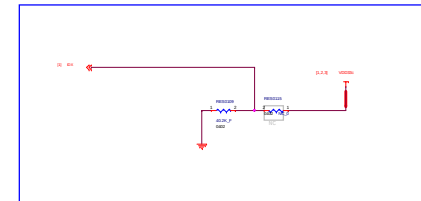
PDB/BISTC/BISTEN Control



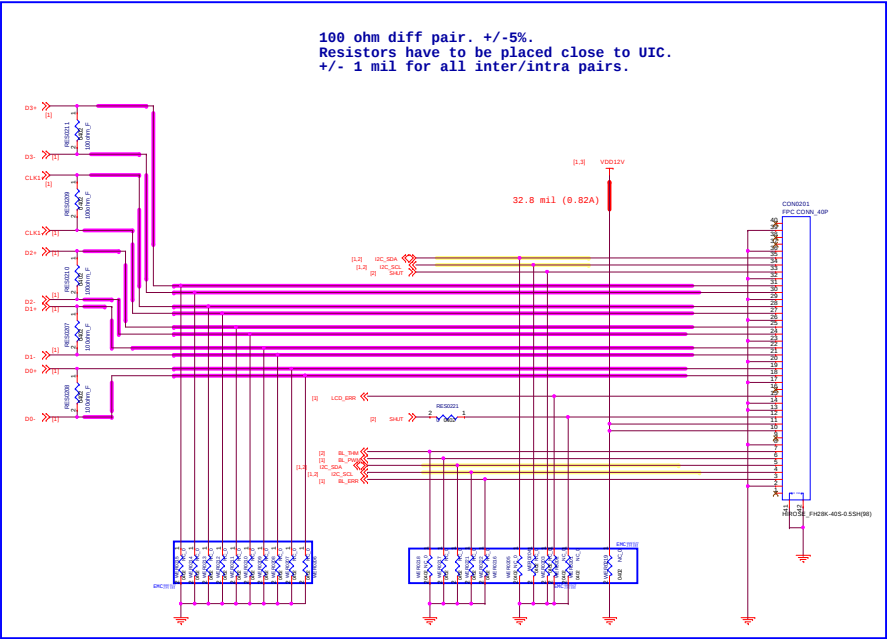
LFMODE/OSS_SEL/OEN Control



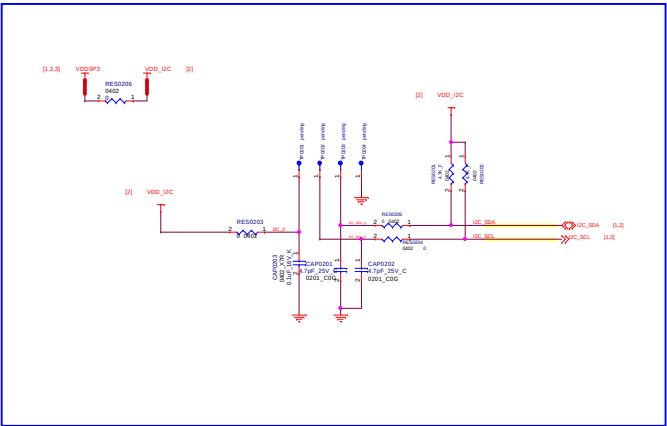
IDX Control



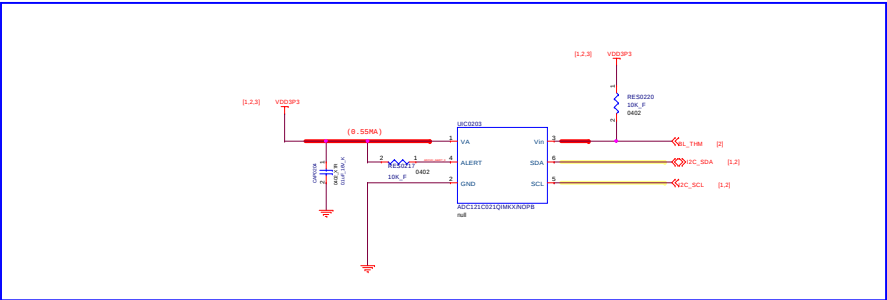
FPC CONN



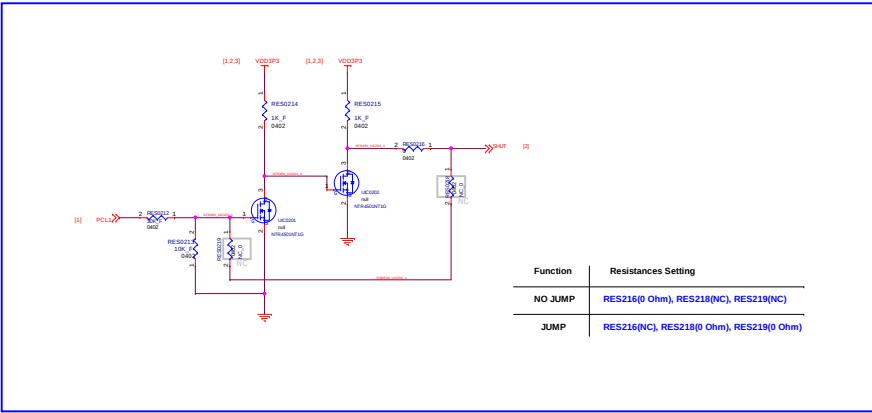
External I2C connection



ADC converter to I2C



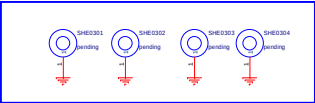
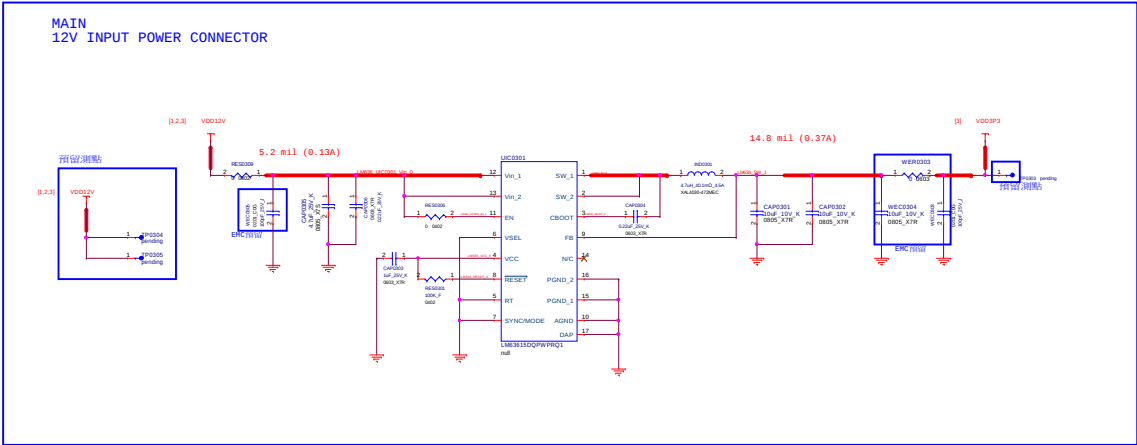
N-MOFET_Confirm PCL1



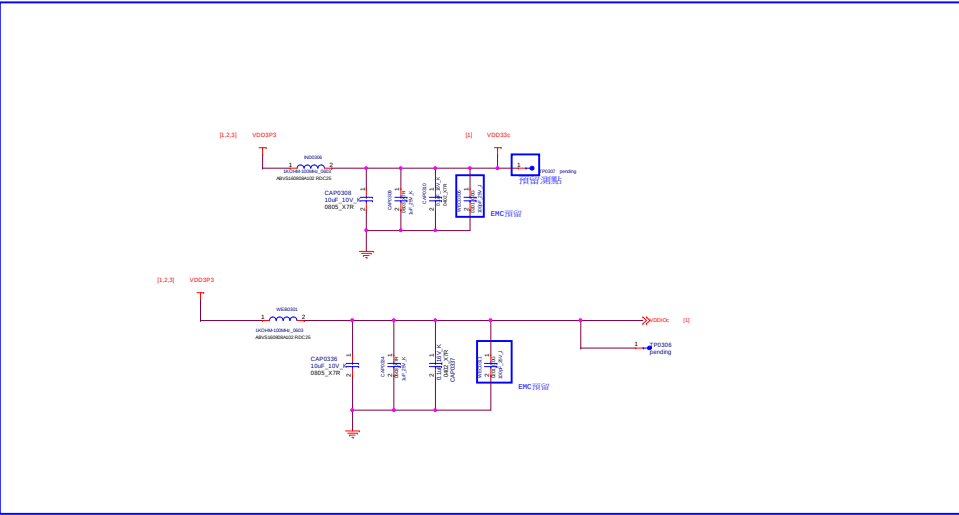
Line	Note
RF 50 ohm line	
Differential 100 ohm line	
I/O Signal	Equal length (GND protect on 4 side)
Control / data signal	GND protect on 2 side
Clock Sensitive signal	GND protect on 4 side
Power plane	

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Main power



需靠近至UIC101, 並放置在Bottom layer



Line	Note
RF 50 ohm line	
Differential 100 ohm line	
I/O Signal	Equal length (GND protect on 4 side)
Control / data signal	GND protect on 2 side
Clock Sensitive signal	GND protect on 4 side
Power plane	