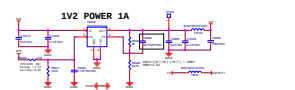
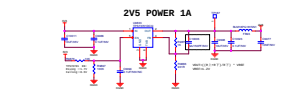
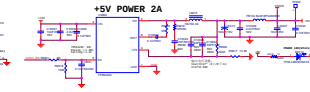
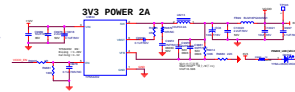
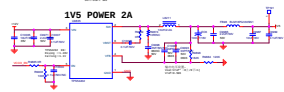
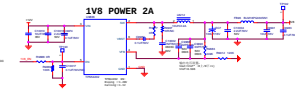
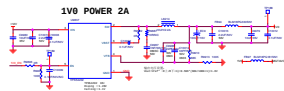
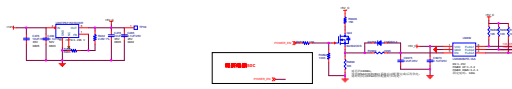


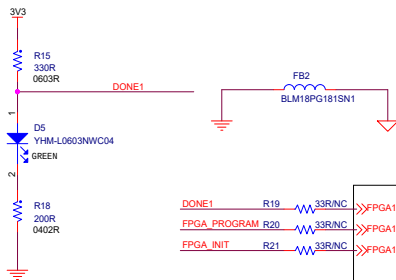
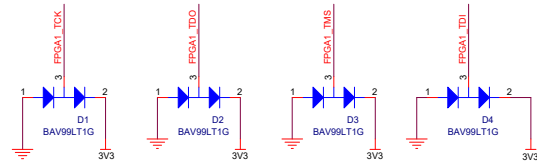
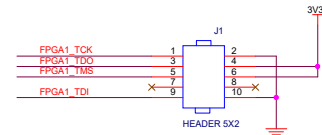
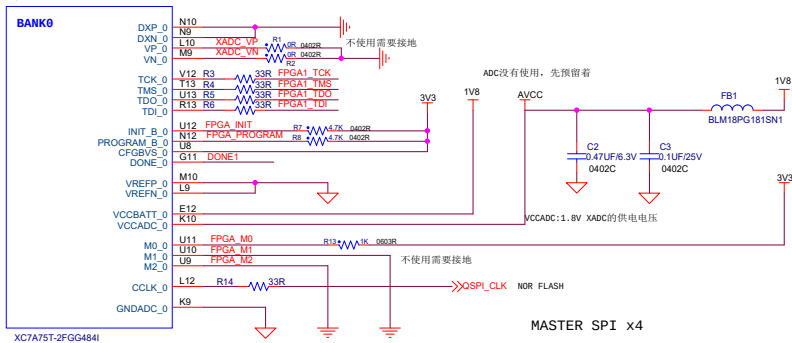
+5V_C POWER 50mA 给使能电路供电



BANK0

JTAG Connector

U1-1 BANK0 3.3V



DONE1 R19 33R/NC >>FPGA1_DONE
FPGA_PROGRAM R20 33R/NC >>FPGA1_PROGRAM
FPGA_INIT R21 33R/NC >>FPGA1_INIT

需要连接SOC

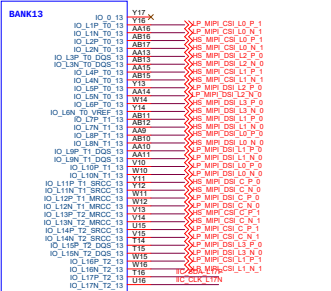
SOC控制FPGA1的状态与检测

OK

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B		CN_E15-2000_MAINBOARD	
Date		Saturday, May 27, 2023	
		Sheet 2 of 27	

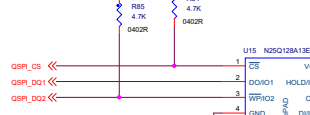
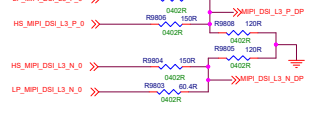
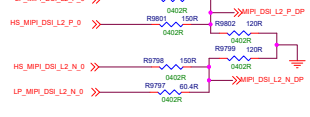
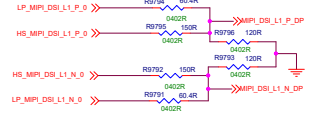
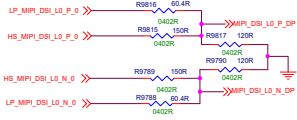
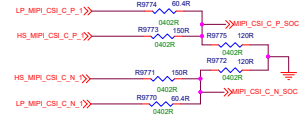
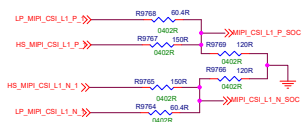
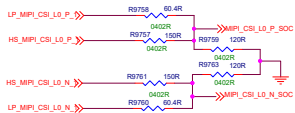
BNAK9.....3V3
BANK13.....1V8
BANK14.....3V3
BANK15.....2V5
BANK16.....2V5
BANK34 35.....1V5

MIPI_DSI_输出给显示器
MIPI_CSI_输出给SOC



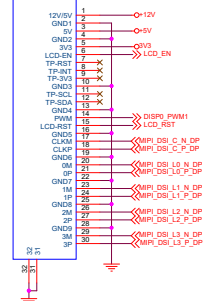
Y07AT5T.25GG48M

XC7A35T无BANK13
XC7A100T/XC7A200T有BANK13

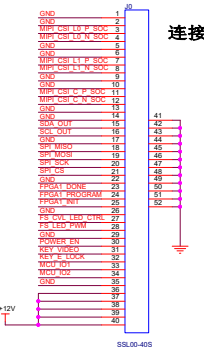


J9911
FPC30P-005

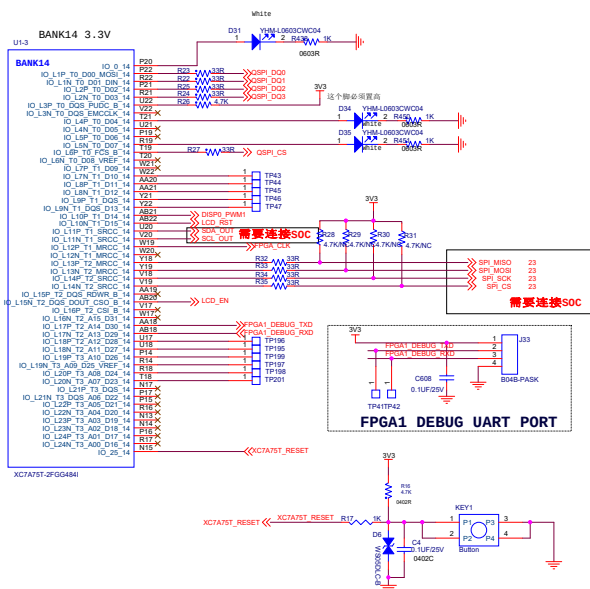
用于调试输出MIPI



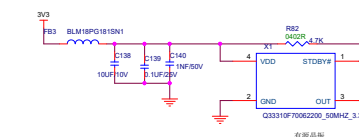
40PIN CONNECTOR



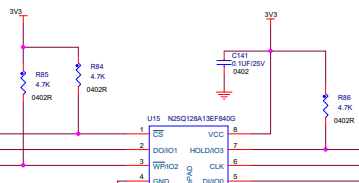
MIPI DSI 输出给SOC



CLOCK



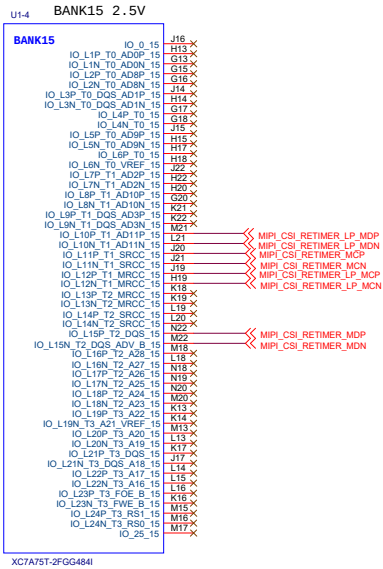
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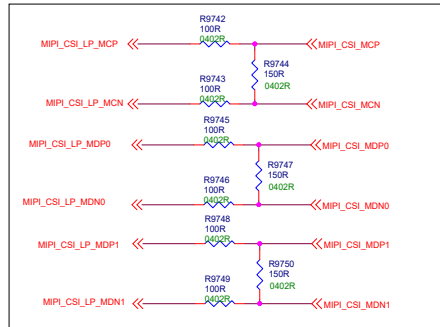
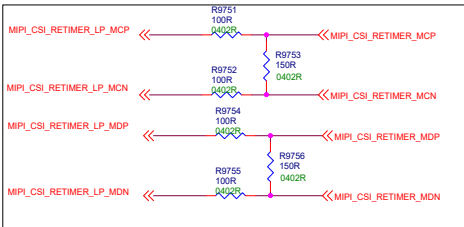
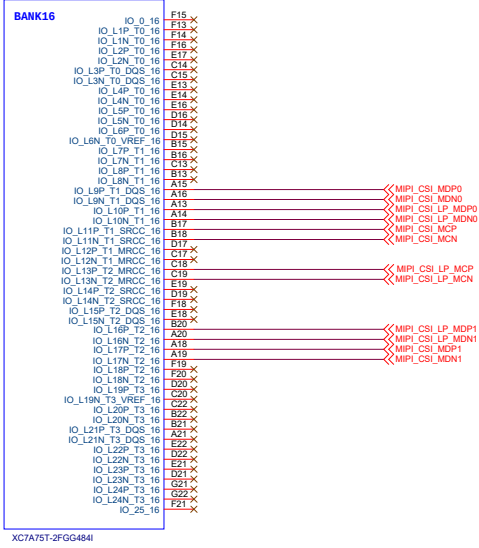
SPI_FLASH
容量: 16M字节

MIPI_CSI_1路0AH428

MIPI_CSI_1路RETIMER



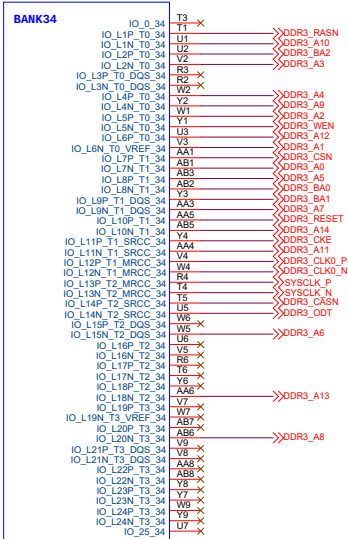
U1-5 BANK16 2.5V



OK

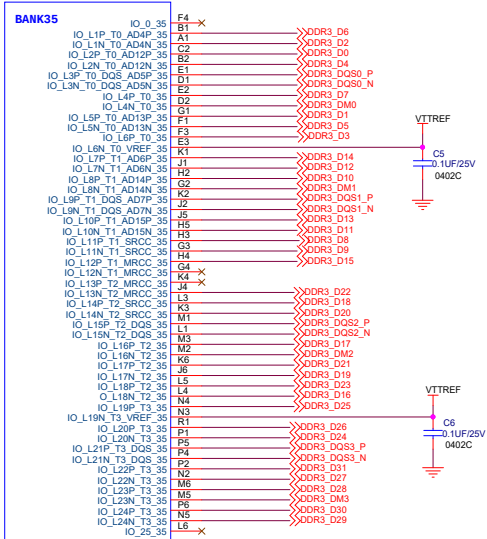
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Date:	Friday, May 26, 2023	Sheet	4 of 27	

U1-6 BANK34 1.5V



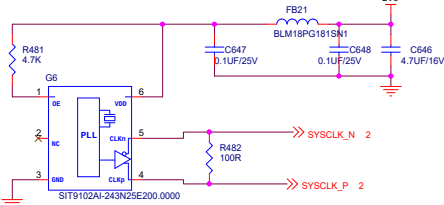
XC7A75T-2FGG484I

U1-7 BANK35 1.5V



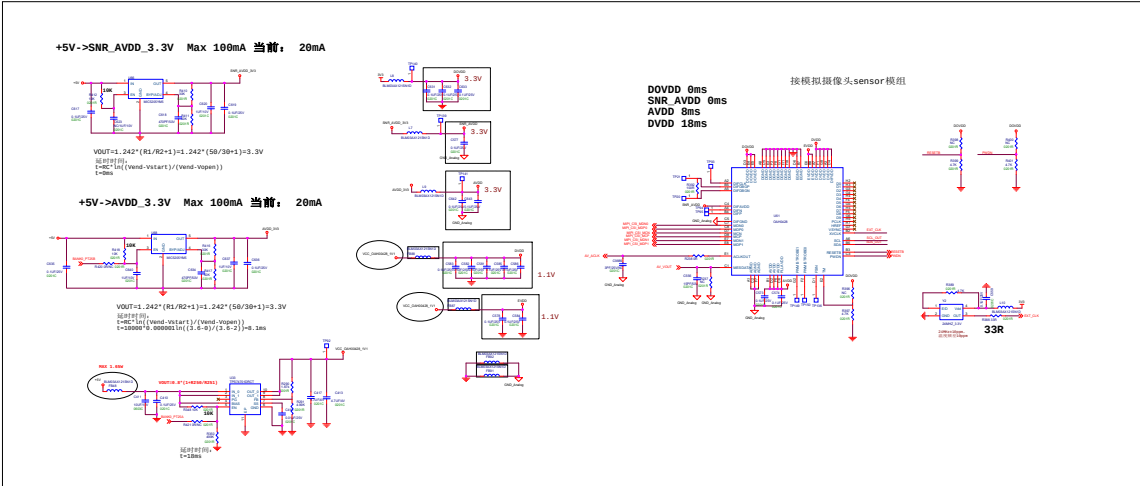
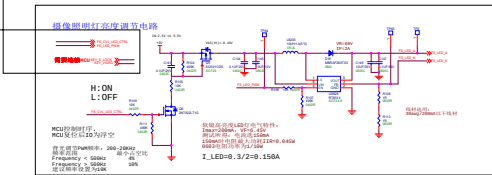
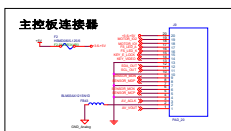
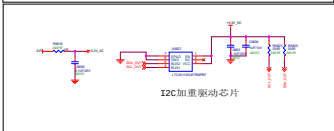
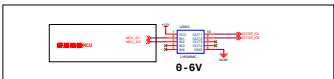
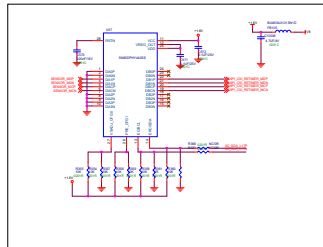
XC7A75T-2FGG484I

DDR REF CLOCK=200MHz 50PPM



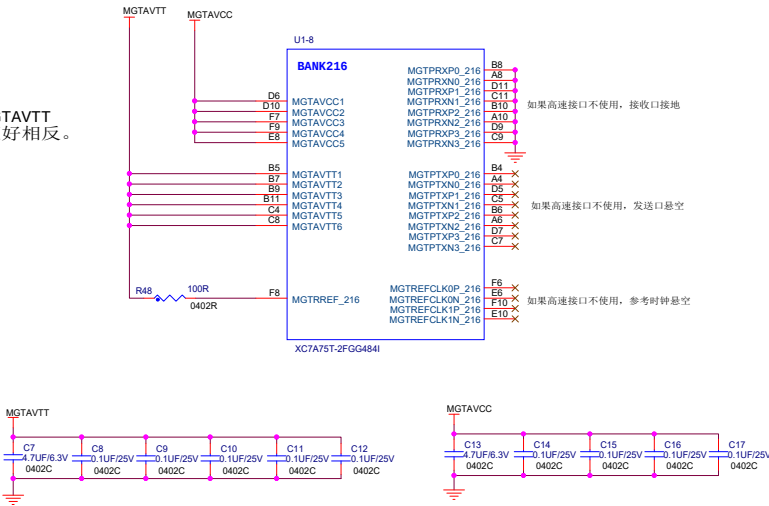
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File		<Title>
Size		Document Number
B		<Doc>
Date		Friday, May 26, 2023
Sheet		5 of 27



MGTAVCC:1.0V GTP收发器核心电压
MGTAVTT:1.2V GTP收发器终端匹配电压

GTP收发器的上电顺序为VCCINT、MGTAVCC、MGTAVTT
或者MGTAVCC、VCCINT、MGTAVTT。断电顺序正好相反。



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File		<Title>	
Size	B	Document Number	Rev
Date:		<Doc>	
Friday, May 26, 2023		Sheet	6 of 27

