

Drawn by  
Jae Mun Jung, Vicent Climent, Javier Julve  
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Document name 21SMR006\_IF\_Video.SchDoc  
Title Danwon ECU  
21SMR006  
IF Video

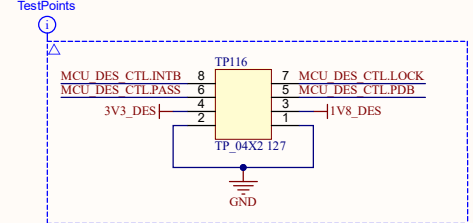
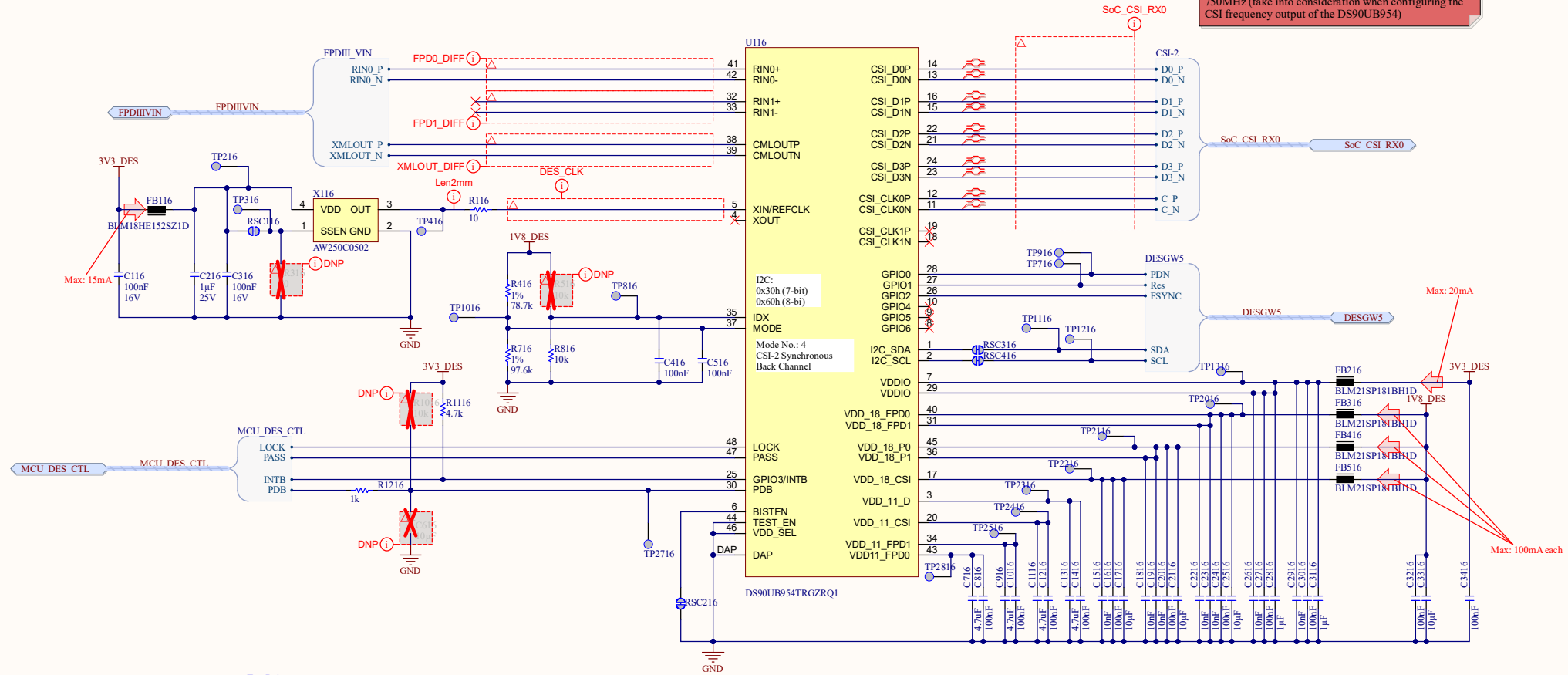
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Not approved  
Approve Date Not approved

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21SMR006v01  
Revision 2063  
Date 22.08.2022  
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TP1816  
TP1916  
TP2616

Free Test Points. Can be used for test-point signals which currently are in the top layer.

GW5 AN5001 V1.7 28 section 5.3: MIPI Interface:  
MIPI Interface of the GW5 can go up to 1.5Gbps/lane  
-> This is understood as maximum CSI\_CLK of 750MHz (take into consideration when configuring the CSI frequency output of the DS90UB954)



SNLS570B -AUGUST  
2017-REVISED DECEMBER 2018:

3V3 (VDDIO):  
Vin: 3.0 3.6 [V]  
Current max: 15 [mA]  
Max ripple: 100 [mVp-p]

IV8 (I):  
Vin: 1.71 1.89 [V]  
Current max: 200mA  
Max ripple: 50 [mVp-p]



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