

NOTES:

1. SILKSCREEN ALL JUMPERS AND HEADERS.
2. PLACE ALL PARTS ON A 0 OR 90 DEGREE ORIENTATION.
3. HIGH SPEED SERIAL DATA SHOULD BE ROUTED AS SINGLE-ENDED 50 OHM TRANSMISSION LINES. ROUTING DISTANCE SHOULD BE 3 INCHES OR LESS.
4. USE FR4-370 MATERIAL.
5. PLACE TI LOGO IN TOP SIDE METAL

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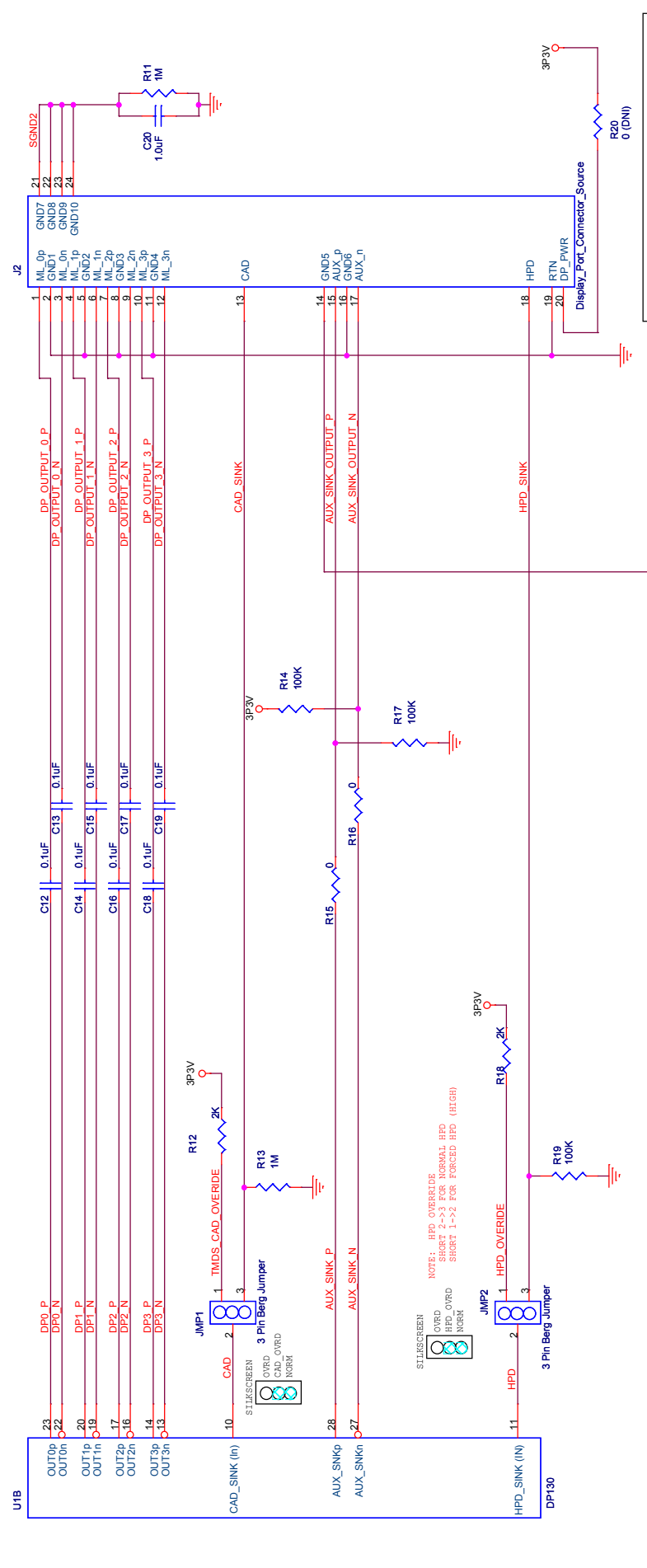
DP130 DATA SHEET REVISION: 0.98
 DATA SHEET LAST UPDATED ON: 03/30/10

REVISIONS		
ECR	ECR NUMBER	DATE
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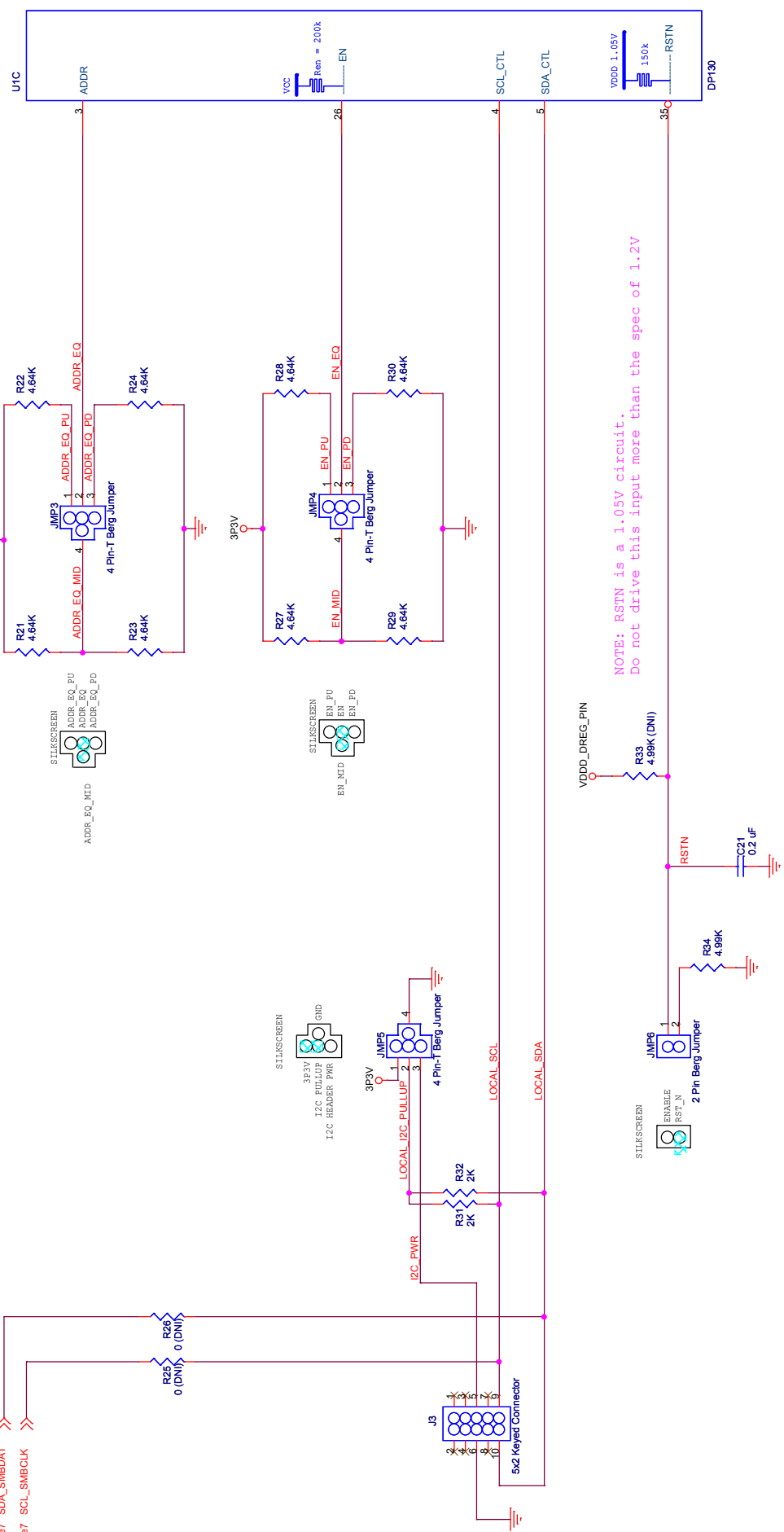
TEXAS INSTRUMENTS

ENGINEER		DATE	
K SHAW		05/08/10	
LAYOUT		DATE	
Steve Gregory		05/09/10	
RELEASED		DATE	
SCHEMATIC TITLE		PAGE TITLE	
DP130 EVM			
SIZE	DOCUMENT NUMBER	REV	SHEET
B	6516396	A	1 of 8

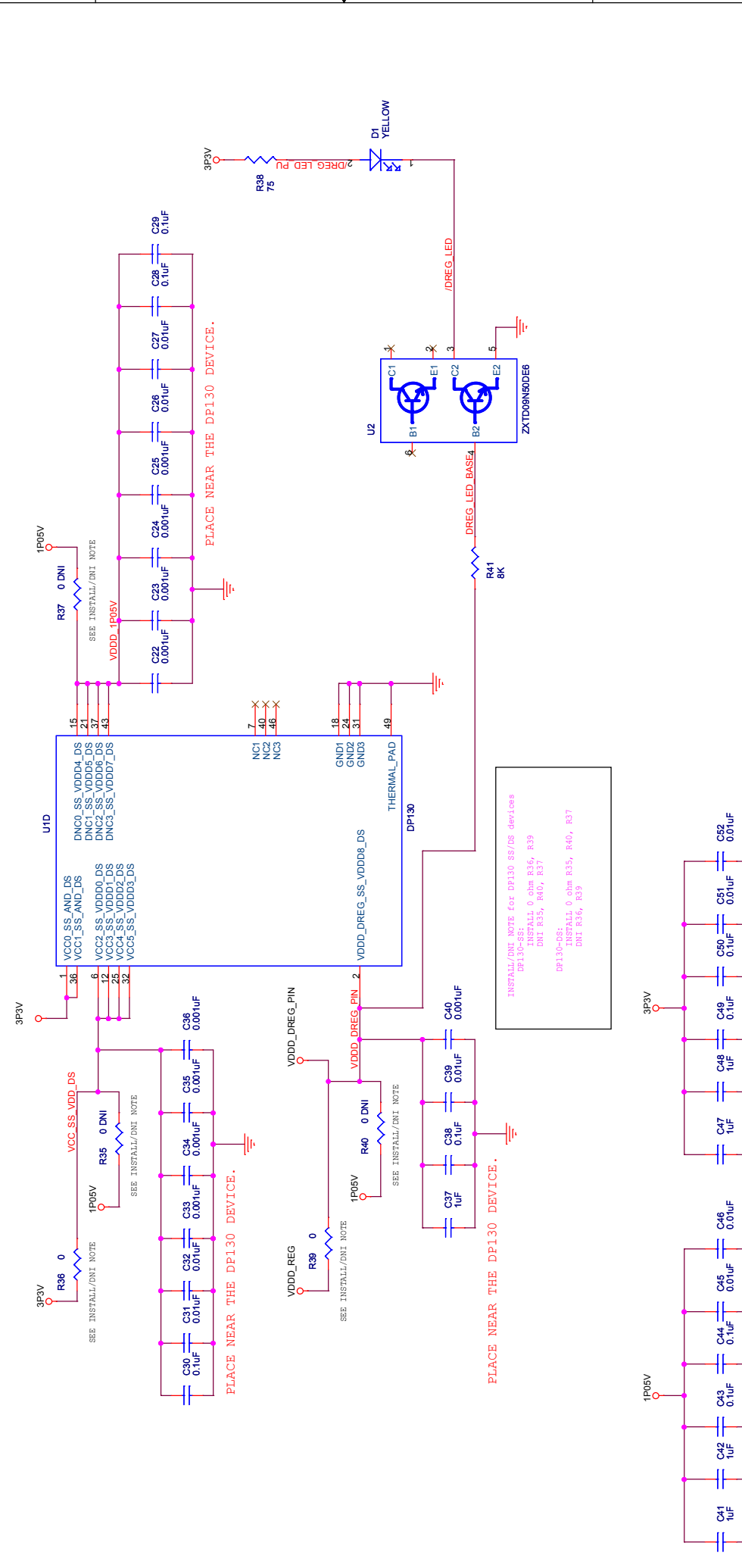


see Page2 CEC >>>
 R20 can be installed to provide DP source power for dongle support

see Page7 SDA_SMBDAT
see Page7 SCL_SMBCLK



NOTE: RSTN is a 1.05V circuit.
Do not drive this input more than the spec of 1.2V

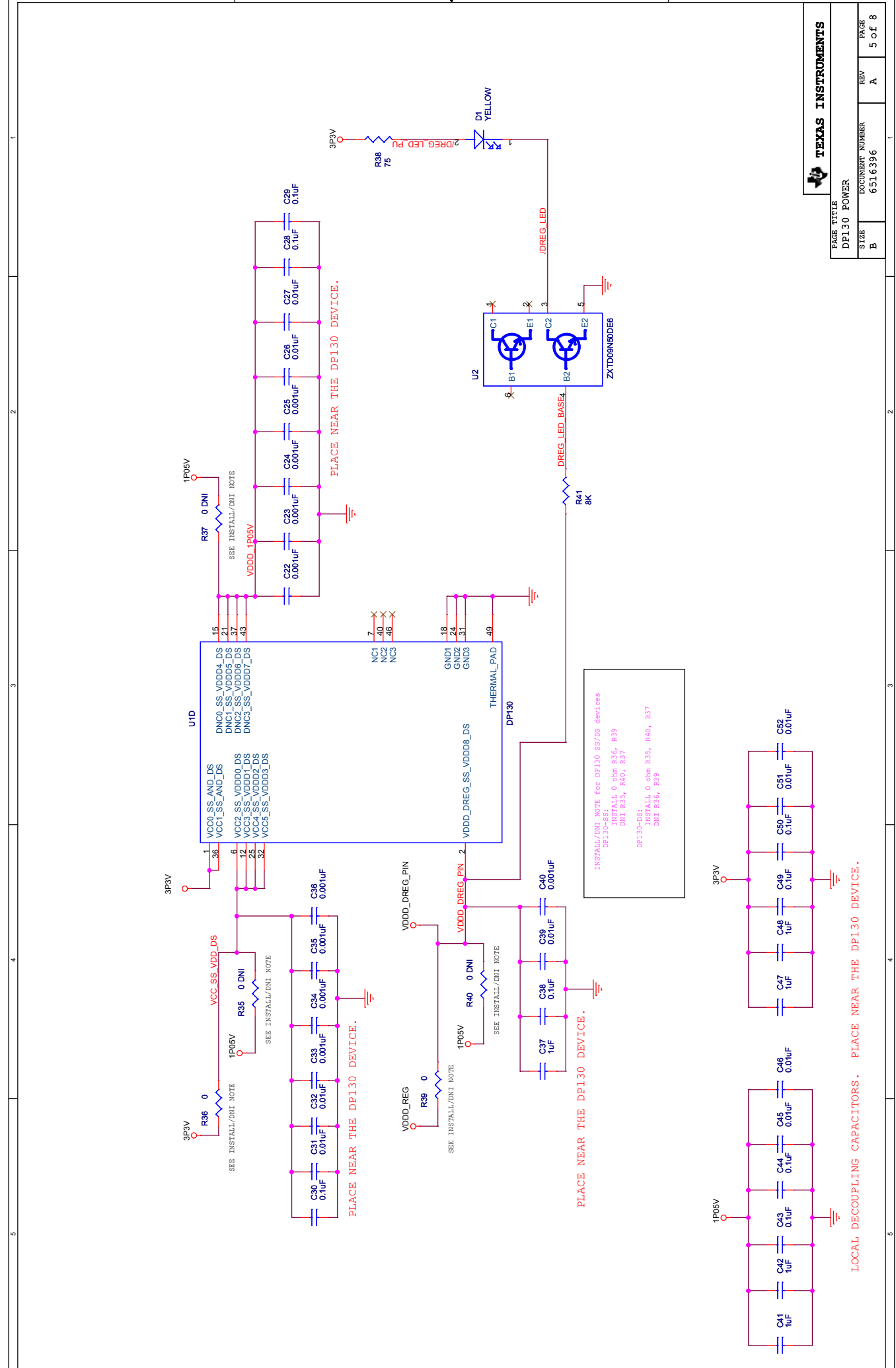


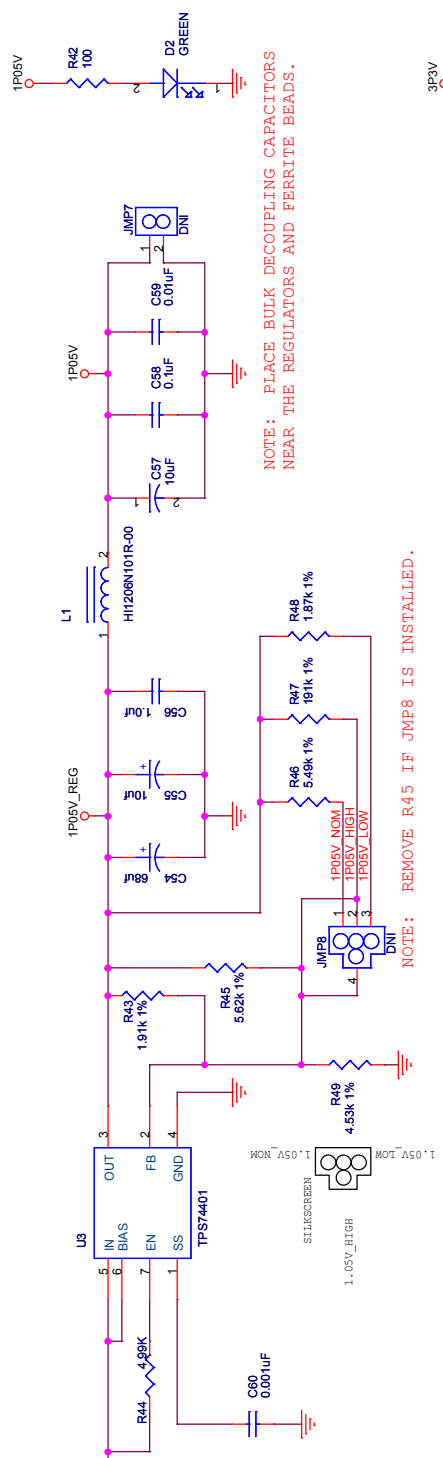
LOCAL DECOUPLING CAPACITORS. PLACE NEAR THE DP130 DEVICE.

PLACE NEAR THE DP130 DEVICE.

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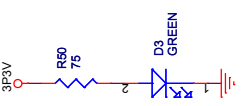
PLACE NEAR THE DP130 DEVICE.





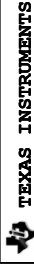
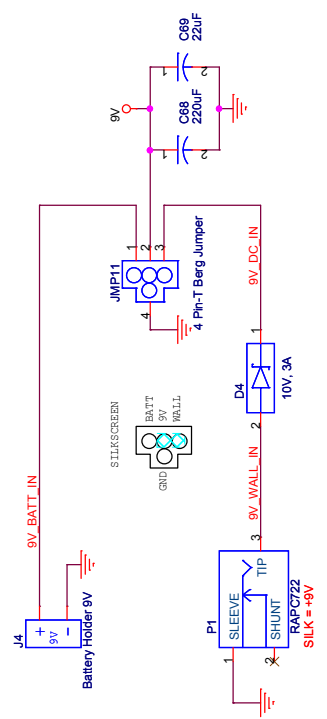
NOTE: PLACE BULK DECOUPLING CAPACITORS NEAR THE REGULATORS AND FERRITE BEADS.

NOTE: REMOVE R45 IF JMP8 IS INSTALLED.

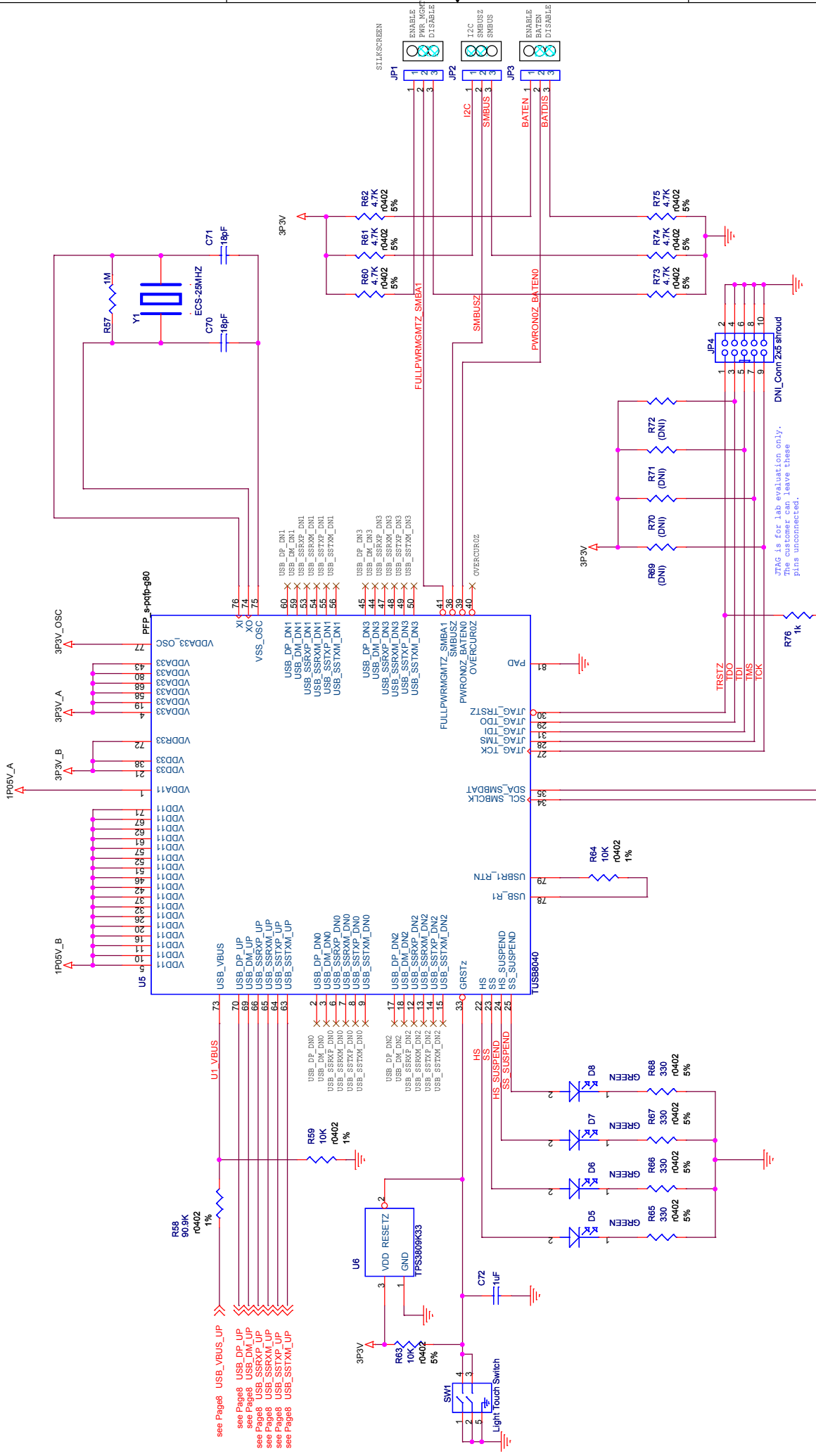


NOTE: PLACE BULK DECOUPLING CAPACITORS NEAR THE REGULATORS AND FERRITE BEADS.

NOTE: REMOVE R52 IF JMP10 IS INSTALLED.

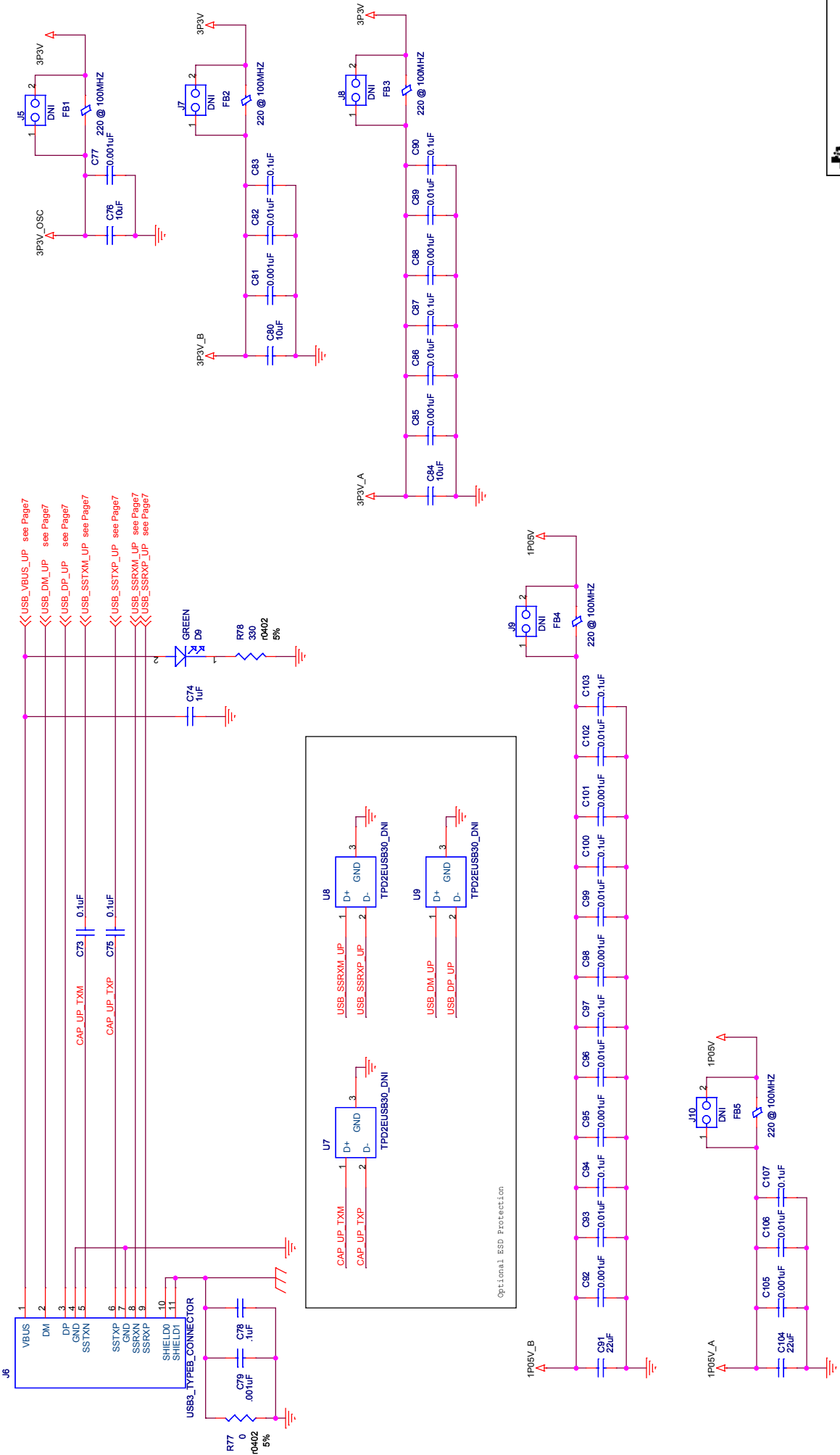


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REV	A
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JTAG is for lab evaluation only. If not needed, please leave these pins unconnected.

see Page4 SCL_SMBCLK
 see Page4 SDA_SMBDAT



USB_VBUS_UP see Page7
 USB_DM_UP see Page7
 USB_DP_UP see Page7
 USB_SSTXM_UP see Page7
 USB_SSTXP_UP see Page7
 USB_SSRXM_UP see Page7
 USB_SSRXP_UP see Page7

CAP_UP_TXM C73 0.1uF
 CAP_UP_TXP C75 0.1uF

U7 TP02EUSB30_DNI
 U8 TP02EUSB30_DNI
 U9 TP02EUSB30_DNI

Optional ESD Protection

C74 1uF
 R78 330 Ohm 5%
 FB1, FB2, FB3, FB4, FB5
 C76, C77, C78, C79, C80, C81, C82, C83, C84, C85, C86, C87, C88, C89, C90, C91, C92, C93, C94, C95, C96, C97, C98, C99, C100, C101, C102, C103, C104, C105, C106, C107