ET1100 connection

Pin No.	PHY0	STRAP	Value	Pin No.	PHY1	STRAP	Value	
F2	CLK25OUT1			F2(sharing)	CLK25OUT1			⇔
K12	MI_DATA			K12(sharing)	MI_DATA			⇔
K11	MI_CLK	LINKPOL	PD(LINK_MII=Active Low)	K11(sharing)	MI_CLK	LINKPOL		⇔
L12	RX_D(0)[3]			M6	RX_D(1)[3]			⇔
L11	RX_D(0)[2]			L5	RX_D(1)[2]			⇔
M12	RX_D(0)[1]			M5	RX_D(1)[1]			⇔
K10	RX_D(0)[0]			L4	RX_D(1)[0]			⇔
M11	RX_DV(0)			M4	RX_DV(1)			⇔
L10	RX_CLK(0)			K4	RX_CLK(1)			⇔
M10	RX_ERR(0)			L6	RX_ERR(1)			⇔
M9	TX_ENA(0)			M3	TX_ENA(1)			⇔
L8	TX_D(0)[0]	C25_ENA	PD(C25_ENA=0)	L3	TX_D(1)[0]	TRANS_MODE_ENA	PD(normal)	⇔
M8	TX_D(0)[1]			M2	TX_D(1)[1]			⇔
L7	TX_D(0)[2]	C25_SHI[0]	PD(TX_shift=not delay)	L2	TX_D(1)[2]	P_MODE[0]	PD(2ports)	⇔
M7	TX_D(0)[3]	C25_SHI[1]	PD(TX_shift=not delay)	M1	TX_D(1)[3]	P_MODE[1]	PD(2ports)	⇔
L9	LINK_MII(0)			K3	LINK_MII(1)			⇔

PD:Pull-Down PU:Pull-Up

KSZ8081MNX

→**Replace DP83826<ENHANCED>**

can't output LINK_MII for ET1100

Pin No.	Pin	STRAP	Value	Pin	STRAP	Value	**difference**	DP83826 BASIC Mode Pin	Value
1	GND			Mode Select	ENHANCED(VDD or NC)		pattern	Mode Select	BASIC(GND)
2	VDD_1.2			CEXT				CEXT	
3	VDDA_3.3			VDDA3V3				VDDA3V3	
4	RXM			RD_M				RD_M	
5	RXP			RD_P				RD_P	
6	TXM			TD_M				TD_M	
7	TXP			TD_P				TD_P	
8	XO			XO				XO	
9	XI			**XI**/50MHzIn				**XI**/50MHzIn	
10	REXT			RBIAS				RBIAS	
11	MDIO			MDIO				MDIO	
12	MDC			MDC				MDC	
13	RXD3	PHYAD0	PD/PU	RX_D3	STRAP7=fast link-drop	PU(enable)	strap	RX_D3	STRAP7=PHY_ADD0
14	RXD2	PHYAD1	PD	RX_D2	STRAP8=MII or RMII	PD(MII)		RX_D2	STRAP8=PHY_ADD1
15	RXD1	PHYAD2	PD	RX_D1	STRAP9=Auto MDIX	PD(enable)		RX_D1	STRAP9=PHY_ADD2
16	RXD0	DUPLEX	PD(Full)	RX_D0	STRAP0=auto nego	PD(enable)		RX_D0	STRAP0=Duplex Half or Full
17	VDDIO			VDDIO				VDDIO	
18	RXDV	CONFIG2	PD(MII)	**RX_DV**/CRS _DV	STRAP10=MDIX or MDI	PD(MDIX)		**RX_DV**/CRS _DV	STRAP10=No Function
19	RXC	B-CAST_OFF	PU(unique PHY)	**RX_CLK**/50 MHz_RMII			strap(disuse)	**RX_CLK**/50MHz_RMII	
20	RXER	ISO	PD(Disable)	RX_ER	STRAP6=pin31 Sel(CLKOUT25 or LED1)	PU(LED1)	strap	RX_ER	STRAP6=MII iso
21	INTRP	NAND_Tree#	PU(Disable)	**PWRDN**/INT			strap(disuse)	**PWRDN**/INT	
22	TXC			TX_CLK	STRAP5=(RMII mode val)	-		TX_CLK	STRAP5=No Function
23	TXEN			TX_EN				TX_EN	
24	TXD0			TX_D0				TX_D0	
25	TXD1			TX_D1				TX_D1	
26	TXD2			TX_D2				TX_D2	
27	TXD3			TX_D3				TX_D3	
28	COL	CONFIG0	PD(MII)	**COL**/LED2/GPIO	STRAP4=PHY_ADD2	PD		COL	STRAP4=MII or RMII-M or RMII-S
29	CRS	CONFIG1	PD(MII)	**CRS**/LED3	STRAP3=PHY_ADD1	PD		CRS	STRAP3=No Function
30	LED0=LINK_Active	NWAYEN	PU(Enable=auto nego)	LED0=LINK_Active	STRAP2=PHY_ADD0	PD/PU	strap	LED0=LINK_Active	STRAP2=Auto nego
31	LED1=100M#/10M	SPEED	PU(100M)	CLKOUT/**LED1**=100M_N/10M-NoLink	STRAP1=No Function?			**TX_ER**/LED1	STRAP1=SPEED 10M or 100M
32	RST#			RST_N				RST_N	

NOTE:**Bold letter** is select signal by strap

NOTE:**Bold letter** is select signal by strap