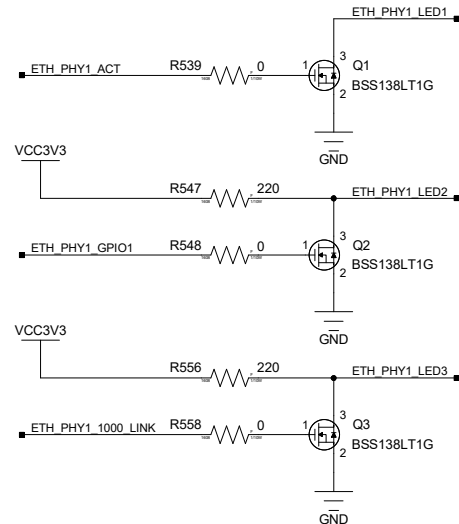
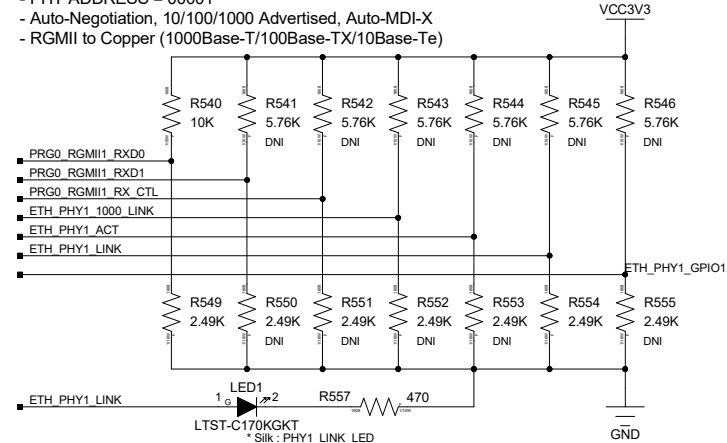
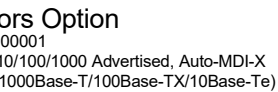


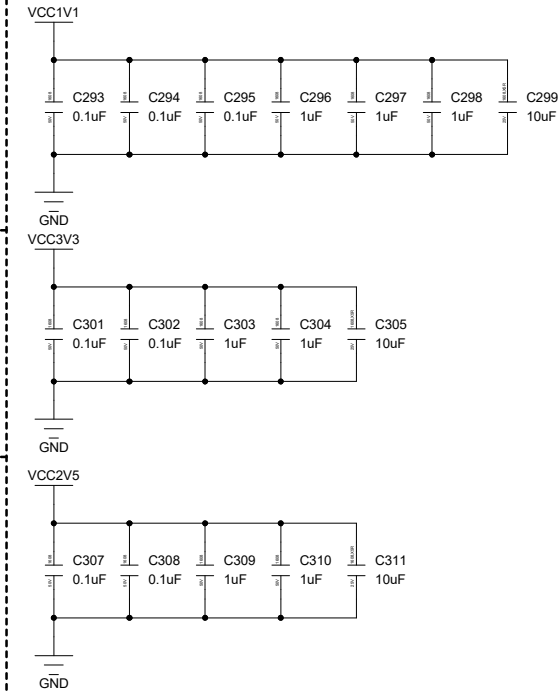
ETHERNET PHY1 Decoupling CAP.



ETHERNET PHY1 X-TAL



ETHERNET PHY1 Decoupling CAP.



■ PRG0_RGMII1_TXD0	○ PRG0_RGMII1_TXD0	■ PRG0_RGMII1_RXD0	○ PRG0_RGMII1_RXD0
■ PRG0_RGMII1_TXD1	○ PRG0_RGMII1_TXD1	■ PRG0_RGMII1_RXD1	○ PRG0_RGMII1_RXD1
■ PRG0_RGMII1_TXD2	○ PRG0_RGMII1_TXD2	■ PRG0_RGMII1_RXD2	○ PRG0_RGMII1_RXD2
■ PRG0_RGMII1_TXD3	○ PRG0_RGMII1_TXD3	■ PRG0_RGMII1_RXD3	○ PRG0_RGMII1_RXD3
■ PRG0_RGMII1_TX_CTL	○ PRG0_RGMII1_TX_CTL	■ PRG0_RGMII1_RX_CTL	○ PRG0_RGMII1_RX_CTL
		■ PRG0_RGMII1_RXC	○ PRG0_RGMII1_RXC
		■ PRG0_RGMII1_INTN	○ PRG0_RGMII1_INTN
■ PRG0_RGMII_MDIO	○ PRG0_RGMII_MDIO		
■ PRG0_RGMII_MDC	○ PRG0_RGMII_MDC		
■ ETH_PHY1_LINK	○ ETH_PHY1_LINK		
■ ETH_PHY1_RX_ER	○ ETH_PHY1_RX_ER		
■ ETH_PHY_RESETn	○ ETH_PHY_RESETn		

CHANGE	NO	DESCRIPTION	CHANGED BY	CHANGED DATE				SEQUENCE CONTROL B/D		SHEETS	
						SCALE	DIMENSION	DATE	TITLE	Ethernet PHY1 Interface	18
						1 : 1	A2	2023-03-07	VERSION	V0.1	26
						DRAWN	DESIGNED	CHECKED	APPROVED		
						shleef	shleef	jwhonga	hhkang		