

A

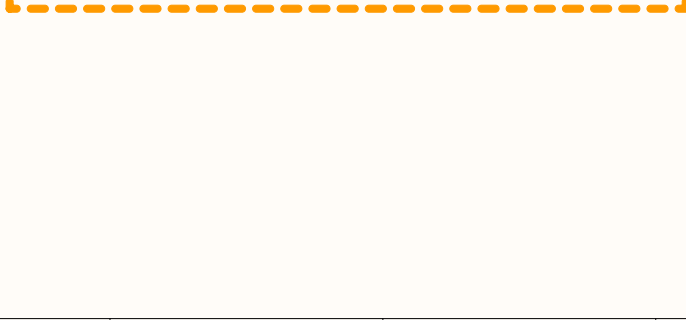


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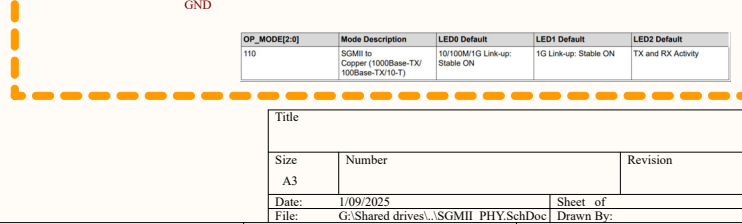


3.3V 25MHz OSC

The diagram shows a 3.3V 25MHz oscillator circuit. A 3.3V supply is connected to the VDD pin (pin 4) of the SG3225CAN 25.0000M-TIGAX crystal (U9). The ST pin (pin 1) is connected to ground through a 1K resistor (R115). The OUT pin (pin 3) is connected to the ETH SW CLK 25MHz output through a 220R resistor (R114). A 0.22uF capacitor (C120) is connected between the 3.3V supply and ground. A 100nF capacitor (C122) is connected between the VDD pin and ground.



BOOT STRAPS



OP_MODE[2:0]	Mode Description	LED0 Default	LED1 Default	LED2 Default
110	SGMII to Copper (100Base-TX/100Base-TX/10-T)	10/100M/1G Link-up: Stable ON	1G Link-up: Stable ON	TX and RX Activity

Title		
Size A3	Number	Revision
Date:	1/09/2025	Sheet of
File:	G:\Shared drives\...\SGMII_PHY.SchDoc	Drawn By: