



- [16] GMAC1_TXCLK
- [16] GMAC1_TXEN
- [16] GMAC1_TXD0
- [16] GMAC1_TXD1
- [16] GMAC1_TXD2
- [16] GMAC1_TXD3
- [16] GMAC1_RXCLK
- [16] GMAC1_RXDV_CK
- [16] GMAC1_RXD0
- [16] GMAC1_RXD1
- [16] GMAC1_RXD2
- [16] GMAC1_RXD3

PIN33	MODE	PHY_ADD1	PHY_ADD0
RX D0	0	0	0
	1	0	1
	2	1	0
	3	1	1

PIN34	MODE	PHY_ADD3	PHY_ADD2
RX D1	0	0	0
	1	0	1
	2	1	0
	3	1	1

PIN22	JTAG TDO/GPIO	OPMODE	FUNCTIONAL MODES
PIN22	RX D3	1 0	000 RGMII to Copper
PIN36	RX D3	0	011 SGMI to Copper
PIN35	RX D2	0	

PIN47	LED 0	MODE	FUNCTIONAL MODES
PIN47	LED 0	0	Auto-negotiation
PIN46	LED 1	0	Auto MDI-X
PIN45	LED 2	0	

PIN38	MODE	Port Mirroring
RX CTRL	0	0 Disabled

PIN32	MODE	Link Loss Pass Thru
RX CLK	0	0 Enabled

Autoneg Enable 10/100/1000M RGMII to Copper
 (SGMII to Copper :OPMODE_1&2=1)
 PHY_ADD[3:0]=0000

- ETH2_RX_D0 PHY_ADDR1/ADDR0 Mode 0
- ETH2_RX_D1 PHY_ADDR3/ADDR2 Mode 0
- ETH2_GPIO_1 OPMODE_0 Mode 0
- ETH2_RX_D3 OPMODE_1 Mode 0
- ETH2_RX_D2 OPMODE_2 Mode 0
- ETH2_LINK ANEG_DIS Mode 0
- ETH2_LED_1 ANEGSEL_0 Mode 0
- ETH2_ACT ANEGSEL_1 Mode 0
- ETH2_RX_DV MIRROR_EN Mode 0
- ETH2_RX_CLK LINK_LOSS Mode 0