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| **Functional section**  | **Observations**  |
| MDI interface  | copper interface set as mirror mode, i assume this is connected to the switch and the connections/required coupling is taken care  |
| Clock  | Recommend placing 0R resistor on the X0 pin for debug |
| Clk\_Out  | Please place the TP near to the device  |
| INT/PWDN\_N | Did you consider using this interrupt. A TP would be a good idea for debug.  |
| GPIO\_1, GPIO\_2 | Recommend using 2.2kΩ for improved noise performance  |
| /PHY\_RESET0 | Please see below figure to implement the reset without host. If there is no host, use individual resets. |
| MDC, MDIO |  This pin requires pullup resistor. The IEEE specified resistor value is 1.5kΩ, but a 2.2kΩ is acceptable. Good to have this provision  |
| Strap  | RX\_D1, RX\_D0 – pull up – any reason to use 6.04 ? |
| JTAG termination | Consider terminating the JTAG signals as per the EVM |
| LED8, LED10, LED12 | Pull up strap resistor value. Please refer to 4-Level Strap Resistor Ratios of the datasheet |
| Power supply Decap  | Recommend using a load switch similar to TPS22948 |

Reset circuit

