



PIN NAME	PIN NO.	DOMAIN	MODE	STRAP FUNCTION	Description
RX_DV/ RX_CTRL	15	VDDMAC	MODE = 1 (PIN Open)	PHY_AD[0] = 0, PHY_AD[2] = 0	PHY Address: 0b00000 (0x0)
RX_ER	14	VDDMAC	MODE = 1 (PIN Open)	PHY_AD[1] = 0, PHY_AD[3] = 0	
RX_D0	26	VDDMAC	MODE = 1 (PIN Open)	MAC[0] = 0	RGMII(TX andRXInternal DelayMode)
RX_D1	25	VDDMAC	MODE = 2 (Pullup 2.49K)	MAC[1] = 1	
RX_D2	24	VDDMAC	MODE = 2 (Pullup 2.49K)	MAC[2] = 1	
RX_D3	23	VDDMAC	MODE = 1 (PIN Open)	CLKOUT_PIN = 1	Pin 16 is Clock output, Pin 6 is LED_1 pin.
LED_0	35	VDDIO	MODE = 1 (PIN Open)	MS = 0	100BASE-T1 Slave Configuration
LED_1	6	VDDIO	MODE = 1 (PIN Open)	AUTO = 0	Autonomous Mode, PHY able to establish link after power-up