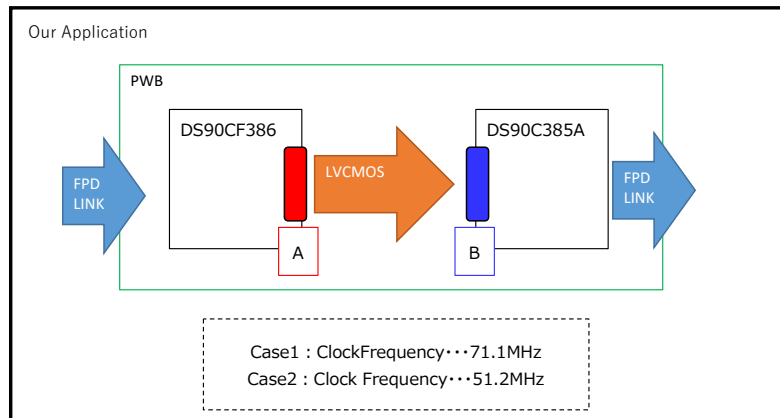




A OutputSpecification : DS90CF386

6.6 Switching Characteristics

over operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP ⁽¹⁾	MAX	UNIT
CLHT	CMOS or LVCMOS low-to-high transition time	See Figure 4		2	3.5	ns
CHLT	CMOS or LVCMOS high-to-low transition time	See Figure 4		1.8	3.5	ns
RSPos0	Receiver input strobe position for bit 0	f = 85 MHz, see Figure 11 and Figure 12	0.49	0.84	1.19	ns
RSPos1	Receiver input strobe position for bit 1	f = 85 MHz	2.17	2.52	2.87	ns
RSPos2	Receiver input strobe position for bit 2	f = 85 MHz	3.85	4.2	4.55	ns
RSPos3	Receiver input strobe position for bit 3	f = 85 MHz	5.53	5.88	6.23	ns
RSPos4	Receiver input strobe position for bit 4	f = 85 MHz	7.21	7.56	7.91	ns
RSPos5	Receiver input strobe position for bit 5	f = 85 MHz	8.89	9.24	9.59	ns
RSPos6	Receiver input strobe position for bit 6	f = 85 MHz	10.57	10.92	11.27	ns
RSKM	RxIN skew margin ⁽²⁾	f = 85 MHz, see Figure 13	290			ps
RCOP	RxCLK OUT period	See Figure 5	11.76	T	50	ns
RCOH	RxCLK OUT high time	f = 85 MHz, see Figure 5	4.5	5	7	ns
RCOL	RxCLK OUT low time	f = 85 MHz, see Figure 5	4	5	6.5	ns
RSRC	RxOUT setup to RxCLK OUT	f = 85 MHz, see Figure 5	2			ns
RHRC	RxOUT hold to RxCLK OUT	f = 85 MHz, see Figure 5	3.5			ns

B InputSpecification : DS90F385A

TSTC	Required TxIN Setup to TxCLK IN (Figure 7) at 85MHz	2.5			ns
THTC	Required TxIN Hold to TxCLK IN (Figure 7) at 87.5 MHz	0.5			ns

DS90CF386

Output SetupTime : Min 2ns @85MHz

DS90C385A

Input SetupTime : Min 2.5ns @85MHz

<Question>

In DS90CF386,

- 1 : Please tell me the specification of Min output SetupTimeValue for 71.1MHz.
- 2 : Please tell me the specification of Min output SetupTimeValue for 51.2MHz.

In DS90C385A,

- 3 : Please tell me the specification of Min input SetupTimeValue for 71.1MHz.
- 4 : Please tell me the specification of Min input SetupTimeValue for 51.2MHz.