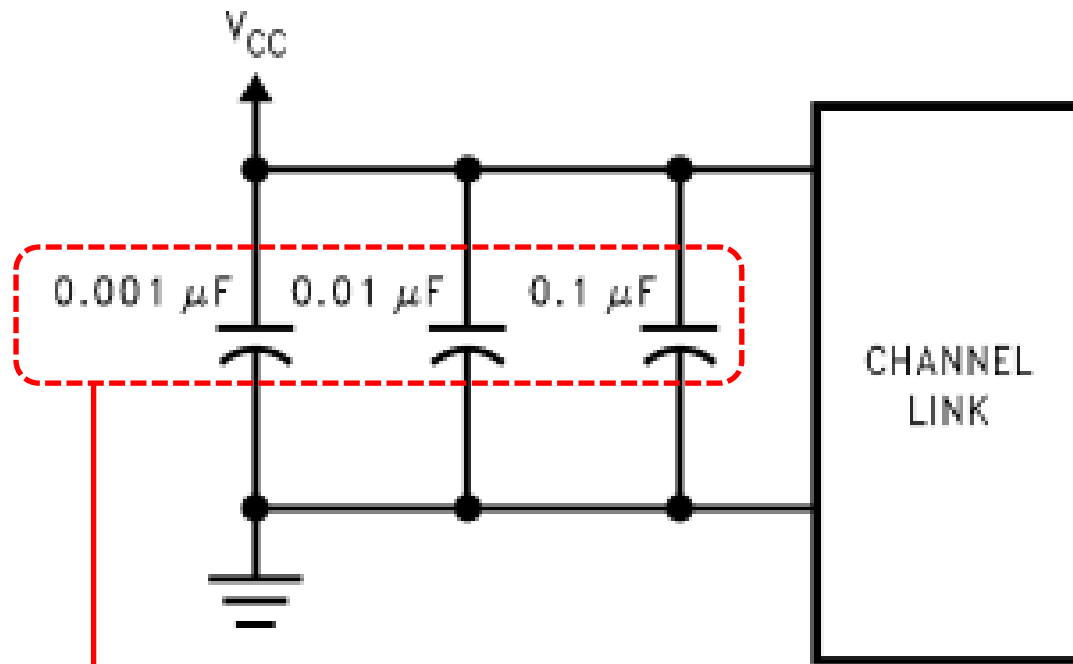
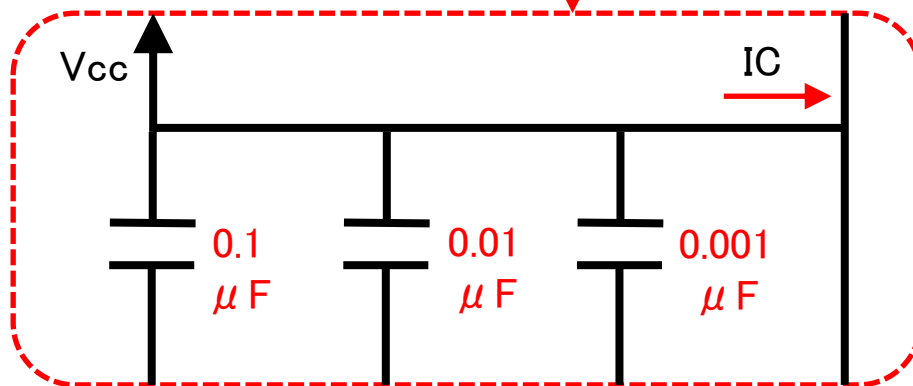




**Figure 14. CHANNEL LINK
Decoupling Configuration**



Layout of those capacitors that we expect