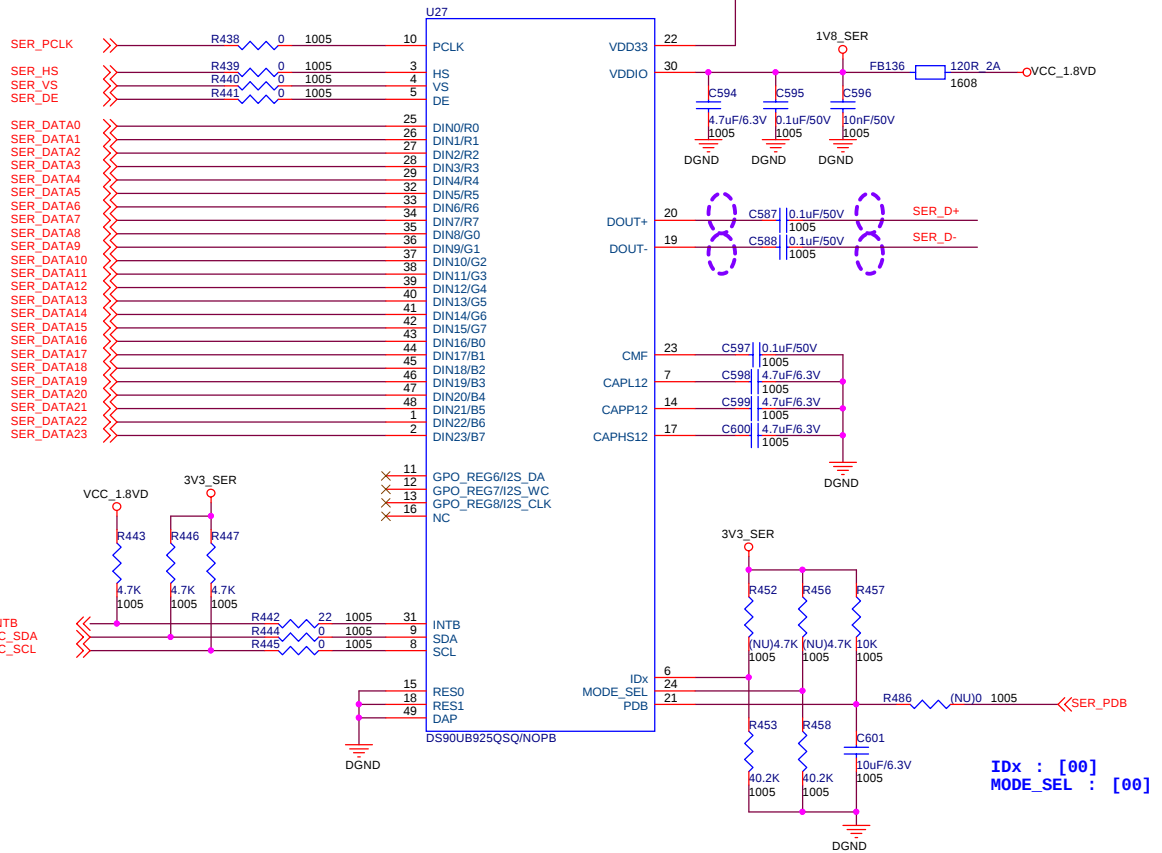
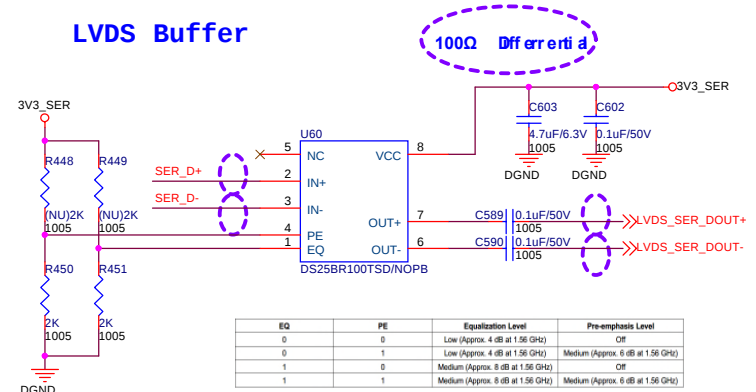




LVDS Buffer



EQ	PE	Equalization Level	Pre-emphasis Level
0	0	Low (Approx. 4 dB at 1.56 GHz)	Off
0	1	Low (Approx. 4 dB at 1.56 GHz)	Medium (Approx. 6 dB at 1.56 GHz)
1	0	Medium (Approx. 8 dB at 1.56 GHz)	Off
1	1	Medium (Approx. 8 dB at 1.56 GHz)	Medium (Approx. 6 dB at 1.56 GHz)

Table 5. Serial Control Bus Addresses for IDx

#	IDEAL RATIO V _{DD} /V _{DDIO}	IDEAL V _{DD} (V)	SUGGESTED RESISTOR R1 kΩ (1% tol)	SUGGESTED RESISTOR R2 kΩ (1% tol)	ADDRESS 7b	ADDRESS 8b APPENDED
1	0	0	Open	40.2 or Any	0x0C	0x18
2	0.121	0.999	254	40.2	0x0D	0x1A

Table 4. Configuration Select (MODE_SEL)

#	IDEAL RATIO V _{DD} /V _{DDIO}	IDEAL V _{DD} (V)	SUGGESTED RESISTOR R3 kΩ (1% tol)	SUGGESTED RESISTOR R4 kΩ (1% tol)	LFMODE	REPEATER	BACKWARD COMPATIBL E	I2S Channel B (1B- Mode)
1	0	0	Open	40.2 or Any	L	L	L	L
2	0.184	0.541	255	40.2	L	H	L	L

LFMODE:
L = frequency range is 15 ~ 85 MHz (Default)
H = frequency range is 9 ~ <15 MHz
Repeater:
L = Repeater OFF (Default)
H = Repeater ON
Backward Compatible:
L = Backward Compatible is OFF (Default)
H = Backward Compatible is ON: DES = DS90UB96GQ or DS90UB96GQ or DS90UB96GQ or DS90UB96GQ
- frequency range = 15 ~ 85 MHz when LFMODE = 0
- frequency range = 9 ~ <15 MHz when LFMODE = 1
I2S Channel B:
L = I2S Channel B is OFF, Normal 24-bit RGB Mode (Default)
H = I2S Channel B is ON, 18-bit RGB Mode with I2S_DB Enabled. Note: use of GPIO(s) on unused inputs must be enabled by register

Idx : [00]
MODE_SEL : [00]