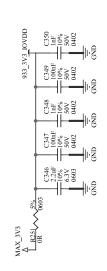


LVDS VIDEO OUTPUT T1933

	OFILM				电路原理图			
				EOBD 11795 AVM			阶段标记 幅面重量比例	
				2024-02-02	田田			
				李金生	签名			
					处数	标准化		40.00
					文件号			
<variant name=""></variant>					1 更改文件号			
iant					飯本 标记	÷ŀ	궑	773
<vai< td=""><td></td><td></td><td></td><td>00</td><td>版本</td><td>敬</td><td>₩</td><td>计书</td></vai<>				00	版本	敬	₩	计书

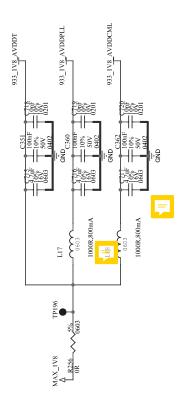


2

ŝ

4

2



VOUTI\_LVDS\_P [20]

C345 100nF 10% 50V 0402 C353 47nF 10% 50V 0402

2

DOUT+

HSYNC VSYNC PCLK MODE

[5] VP\_VOUT2\_HSYNC

933\_1V8\_DVDD

VDDD

DIN[0] DIN[1] DIN[2] DIN[2] DIN[3] DIN[3] DIN[3] DIN[3] DIN[3] DIN[1] DIN[1]

[5] VP\_VOUT2\_D0 [5] VP\_VOUT2\_D1 [5] VP\_VOUT2\_D2 [5] VP\_VOUT2\_D2 [5] VP\_VOUT2\_D4 [5] VP\_VOUT2\_D4 [5] VP\_VOUT2\_D5 [5] VP\_VOUT2\_D5 [5] VP\_VOUT2\_D5

933\_1V8\_AVDDCML 933\_1V8\_AVDDPLL 933\_1V8\_AVDDT

4

VDDT

VDDIO

25 28

933\_3V3\_10VDD

U15

VDDPLL VDDCML

VDDIO:1V8, 2V8, 3V3 VDD\_n:1V8

6 ND

SER\_IDX

D[x]

PDB

T1933 CNTL0 T1933 CNTL1 SER PWDN SER\_MODE

R403 33R R404 33R R405 33R R405 33R

[5] NCSI0-D8 [5] NCSI0-D9 [5] NCSI0-D10 [5] NCSI0-D110 [4] TWI0-SCK [4] TWI0-SDA

GPO[0] GPO[1] GPO[2]/CLKOUT GPO[3]/CLKIN

lı g

lı g

B933TRTVRO

R412 10K 5% 0402

933\_3V3\_IOVDD

33

SCL

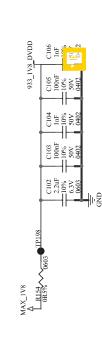
933\_SCL

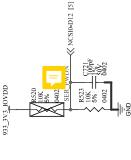
R160 22R TP197 R163 22R TP199

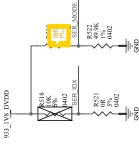
~

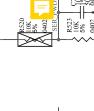
RES EPAD

R253 49R9 1% 0402













ш



