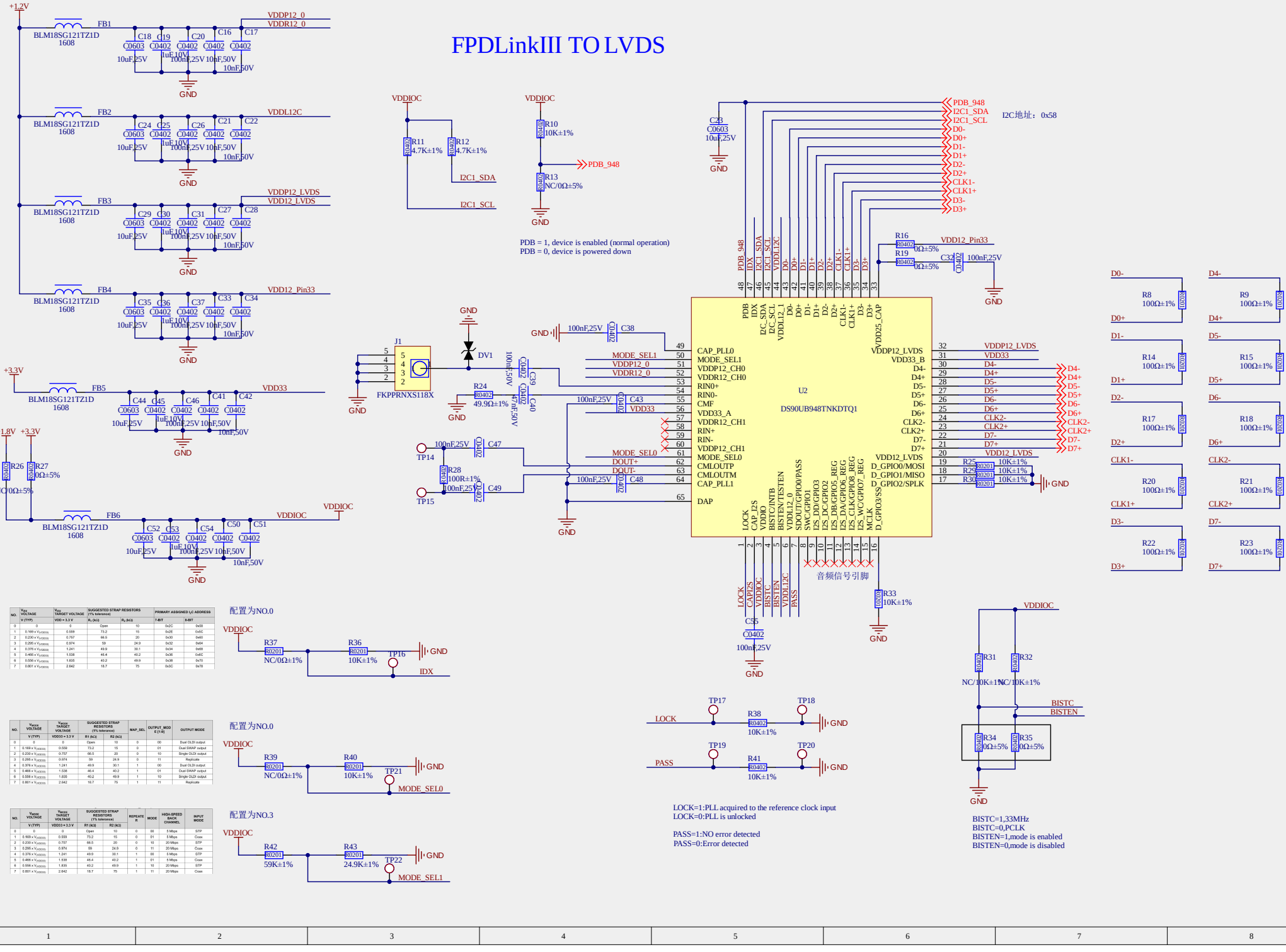
FPDLinkIII TO LVDS



NO.	V_{IH} VOLTAGE	V_{OD} TARGET VOLTAGE	SUGGESTED STRAP RESISTORS (1% tolerance)		PRIMARY ASSIGNED I2C ADDRESS	
	V (TYP)	VDD = 3.3 V	R_1 (kΩ)	R_2 (kΩ)	7-BIT	8-BIT
0	0	0	Open	10	0x2C	0x58
1	0.169 x V_{VDD33}	0.559	73.2	15	0x2E	0x5C
2	0.230 x V_{VDD33}	0.757	66.5	20	0x30	0x60
3	0.295 x V_{VDD33}	0.974	59	24.9	0x32	0x64
4	0.376 x V_{VDD33}	1.241	49.9	30.1	0x34	0x68
5	0.466 x V_{VDD33}	1.538	46.4	40.2	0x36	0x6C
6	0.556 x V_{VDD33}	1.835	40.2	49.9	0x38	0x70
7	0.801 x V_{VDD33}	2.642	16.7	75	0x3C	0x78

NO.	V_{MODE} VOLTAGE	V_{MODE} TARGET VOLTAGE	SUGGESTED STRAP RESISTORS (1% tolerance)		MAP_SEL	OUTPUT_MODE [1:0]	OUTPUT MODE
	V (TYP)	VDD33 = 3.3 V	R_1 (kΩ)	R_2 (kΩ)			
0	0	0	Open	10	0	00	Dual OLDI output
1	0.169 x V_{VDD33}	0.559	73.2	15	0	01	Dual SIMAP output
2	0.230 x V_{VDD33}	0.757	66.5	20	0	10	Single OLDI output
3	0.295 x V_{VDD33}	0.974	59	24.9	0	11	Replicate
4	0.376 x V_{VDD33}	1.241	49.9	30.1	1	00	Dual OLDI output
5	0.466 x V_{VDD33}	1.538	46.4	40.2	1	01	Dual SIMAP output
6	0.556 x V_{VDD33}	1.835	40.2	49.9	1	10	Single OLDI output
7	0.801 x V_{VDD33}	2.642	16.7	75	1	11	Replicate

NO.	V_{MODE} VOLTAGE	V_{MODE} TARGET VOLTAGE	SUGGESTED STRAP RESISTORS (1% tolerance)		REPEATER	MODE	HIGH-SPEED BACK CHANNEL	INPUT MODE
	V (TYP)	VDD33 = 3.3 V	R_1 (kΩ)	R_2 (kΩ)				
0	0	0	Open	10	0	00	5 Mbps	STP
1	0.169 x V_{VDD33}	0.559	73.2	15	0	01	5 Mbps	Coax
2	0.230 x V_{VDD33}	0.757	66.5	20	0	10	20 Mbps	STP
3	0.295 x V_{VDD33}	0.974	59	24.9	0	11	20 Mbps	Coax
4	0.376 x V_{VDD33}	1.241	49.9	30.1	1	00	5 Mbps	STP
5	0.466 x V_{VDD33}	1.538	46.4	40.2	1	01	5 Mbps	Coax
6	0.556 x V_{VDD33}	1.835	40.2	49.9	1	10	20 Mbps	STP
7	0.801 x V_{VDD33}	2.642	16.7	75	1	11	20 Mbps	Coax