

DS90UB953/954/960 Schematic Design Key Points

- I2C pull-up resistors --- 4.7kOhms is recommended
- Power on sequence should follow TI datasheet strictly including PDB pin resistor and capacitor --- 10kohms and 1uF are recommended
- DOUT+ AC coupling capacitor --- 33nF - 100nF; DOUT- AC coupling capacitor --- 15nF - 47nF; DOUT+ and DOUT- AC coupling capacitors are not recommended to be the same
- REFCLK frequency range should be within 23MHz ~ 26MHz; Outside the range is not accepted
- If PoC is used through FPD-LINK, both serializer and deserializer should follow PoC design at the same time