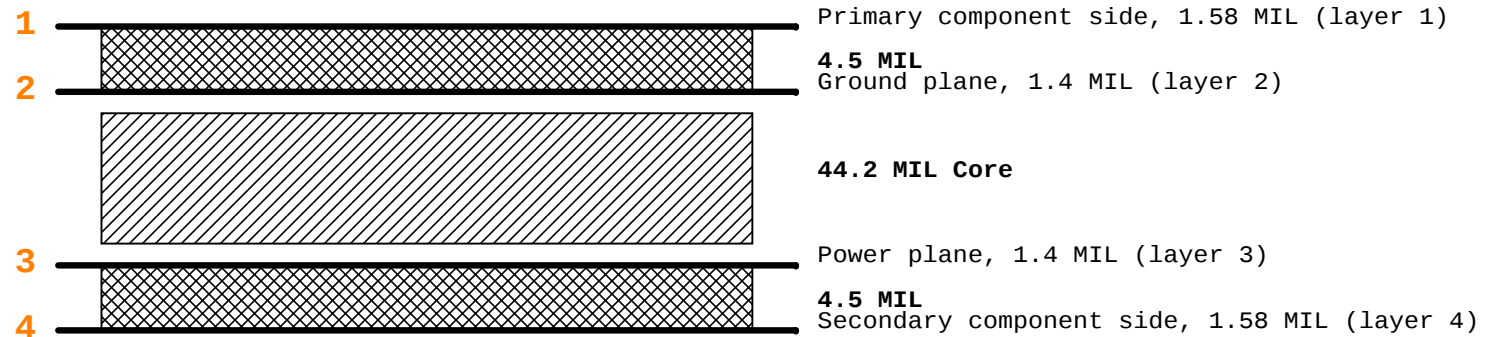


LAYOUT NOTES:

- 1) 4 layer board.**
- 2) Use standard FR-406 or FR-370HR.**
- 3) 5% impedance tolerance.**
- 4) Minimum 4 standoffs on each corner of board. (0.156 X 4).**



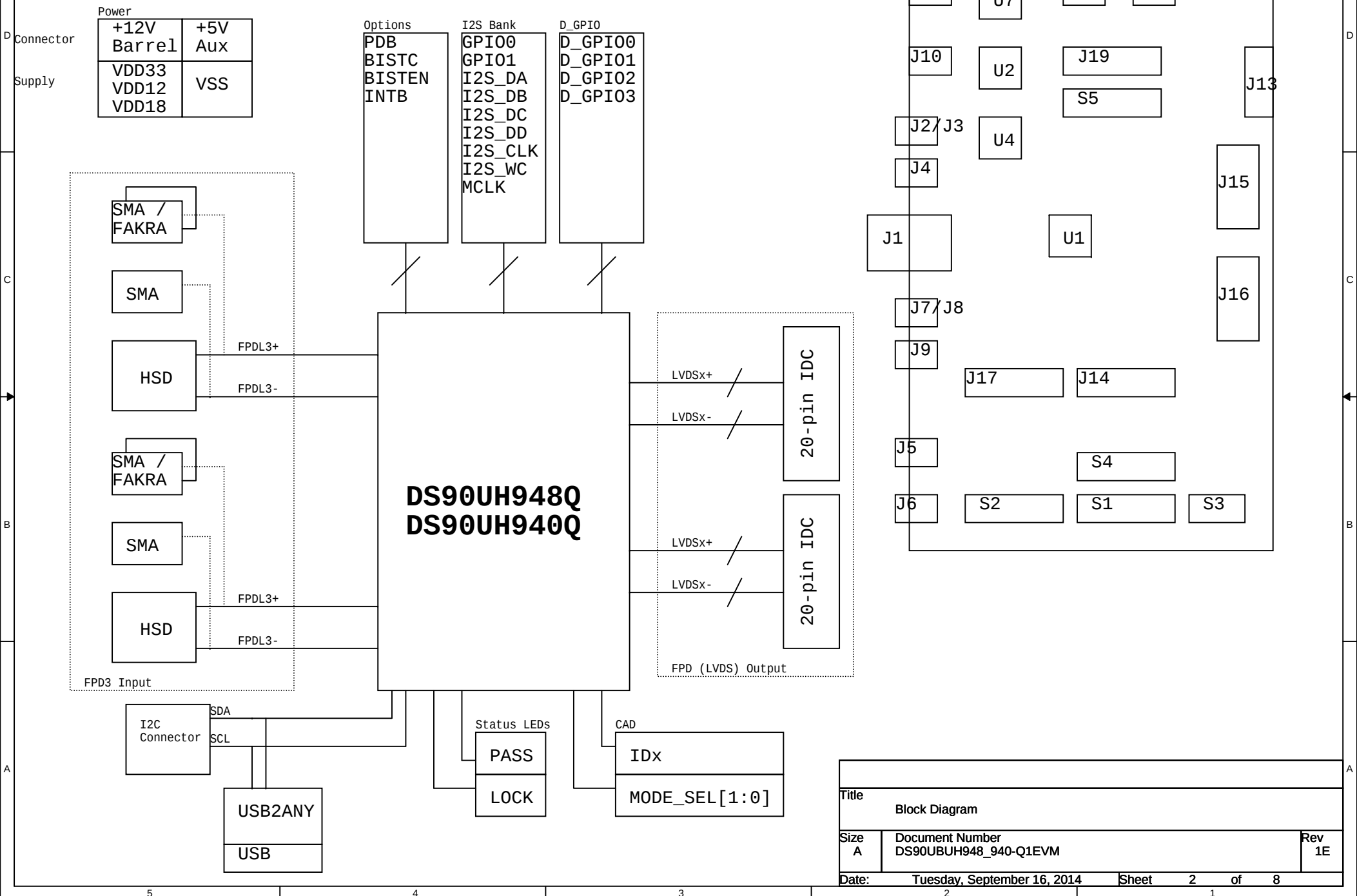
Core Material

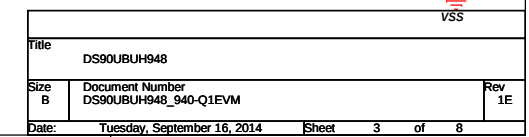


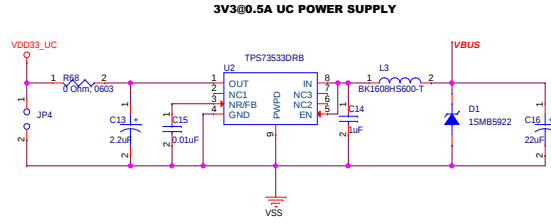
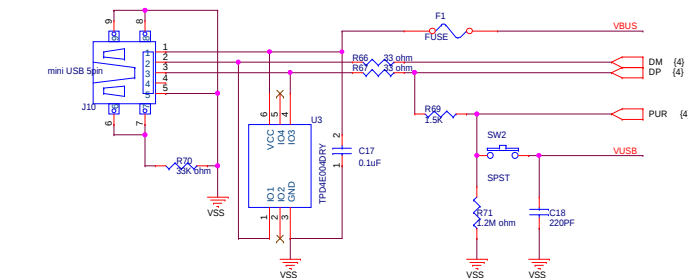
Prepreg Material

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Board Stackup		
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Board Block Diagram

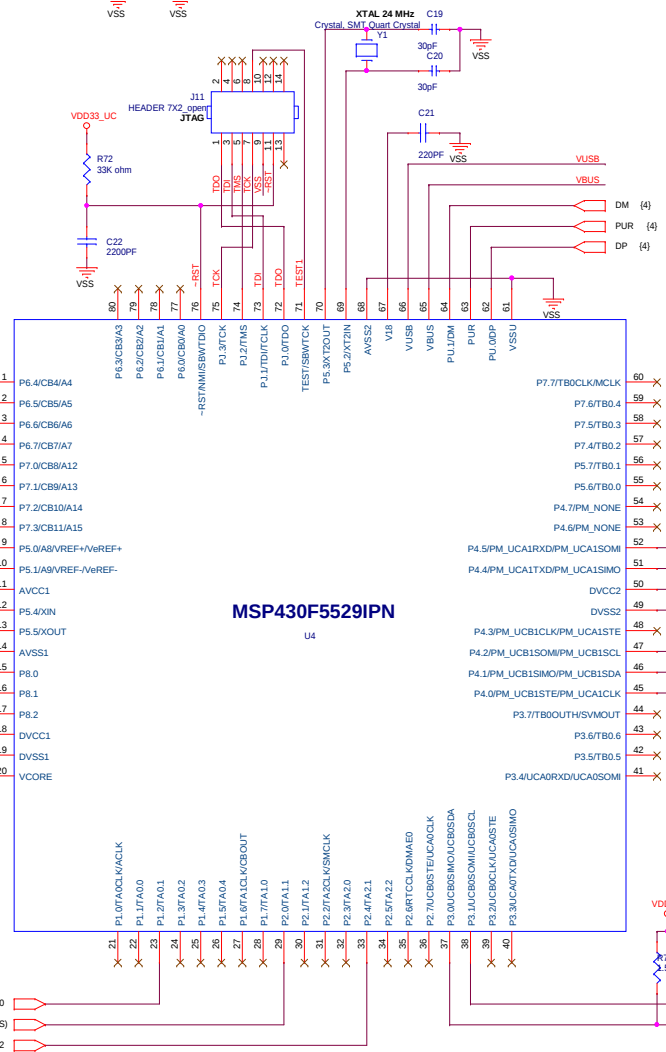
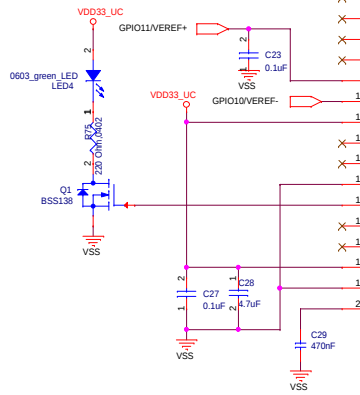




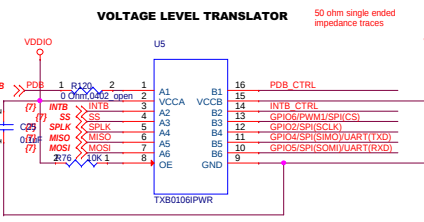
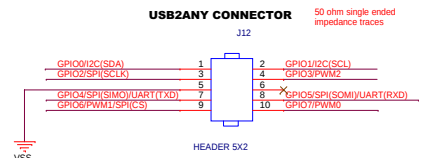


(F) VDD_I2C >> VDD_I2C
(3,5,6) VDDIO >> VDDIO
(3,5,6,7) VSS >> VSS

(3,5,7) I2C_SDA >> I2C_SDA
(3,5,7) I2C_SCL >> I2C_SCL



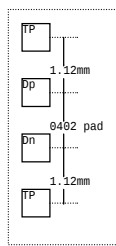
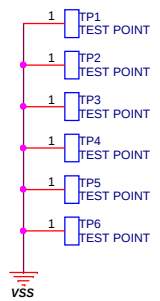
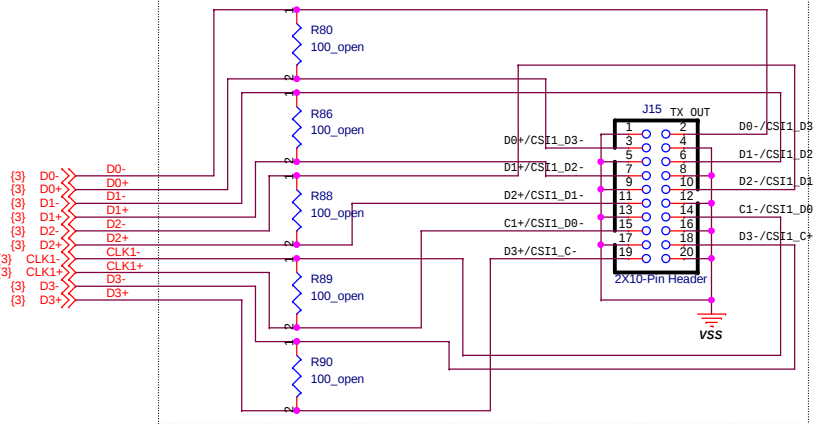
NOTE: Float p2,3, and p2,6 on MSP430 for USB2ANY detection



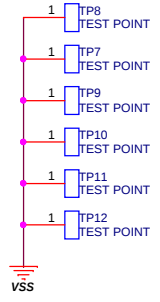
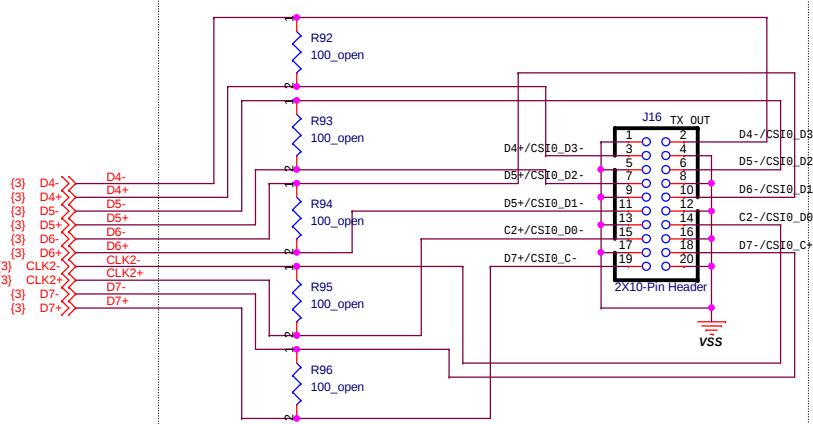
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{3,4,6,7} VSS
 {6,7} VDD33
 {6} VDD18
 {3,4,6} VDDIO

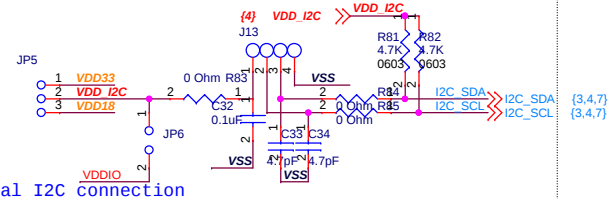
100 ohm diff pair, +/-5%.
 Resistors have to be placed close to U1.
 +/- 1 mil for all inter/intra pairs.



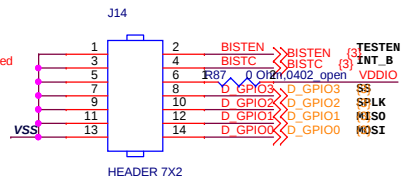
100 ohm diff pair, +/-5%.
 Resistors have to be placed close to U1.
 +/- 1 mil for all inter/intra pairs.



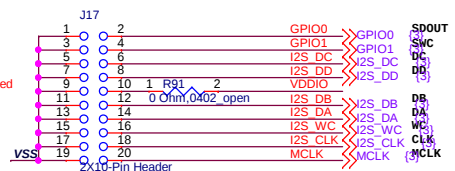
Place at the board edge



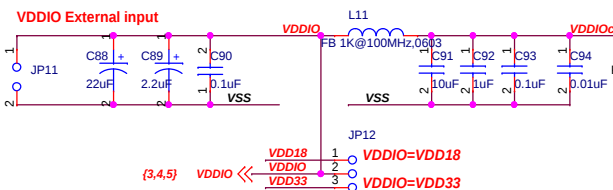
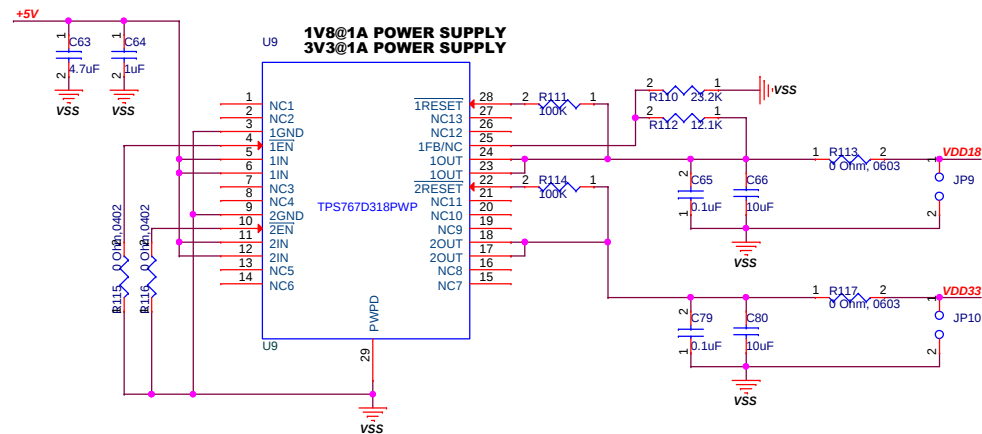
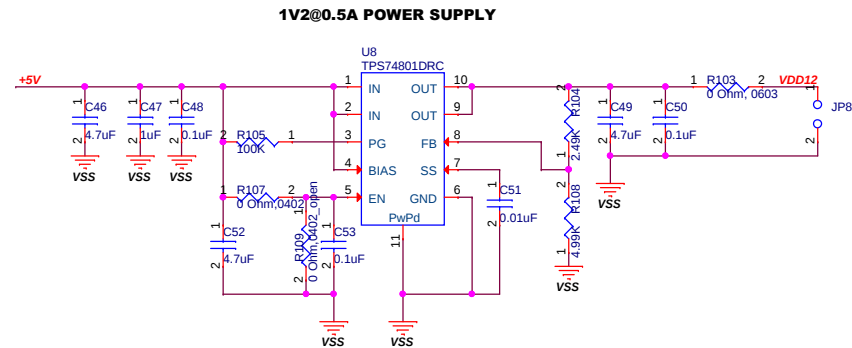
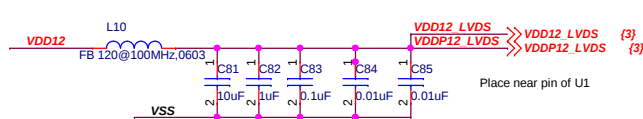
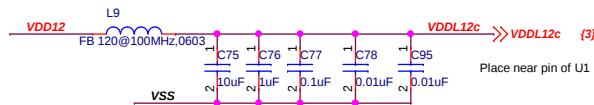
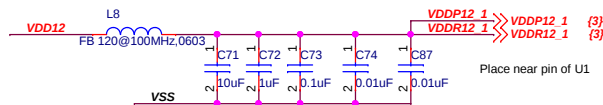
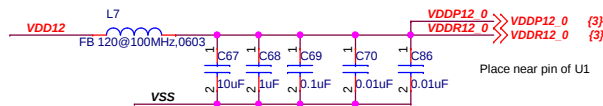
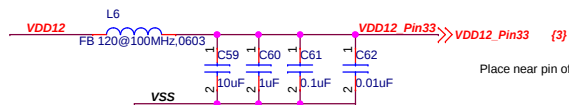
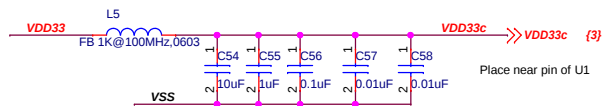
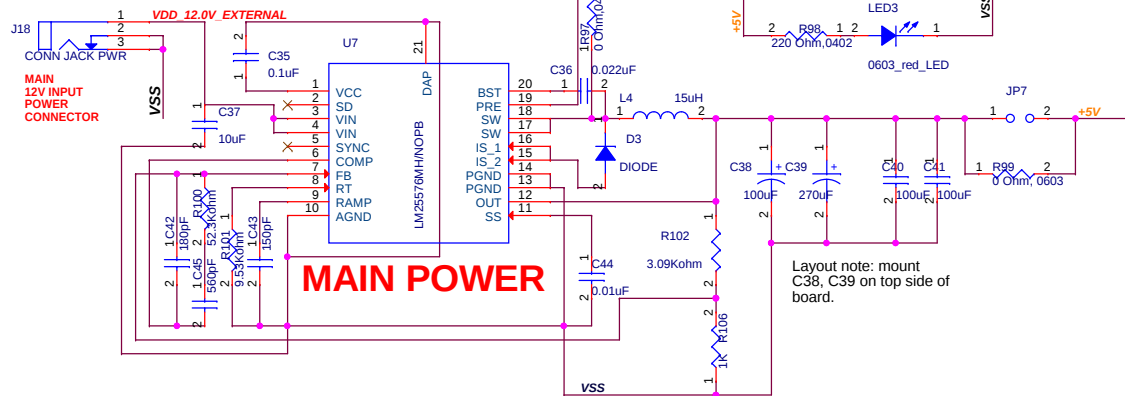
External I2C connection



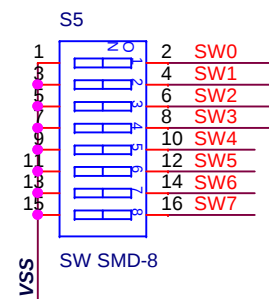
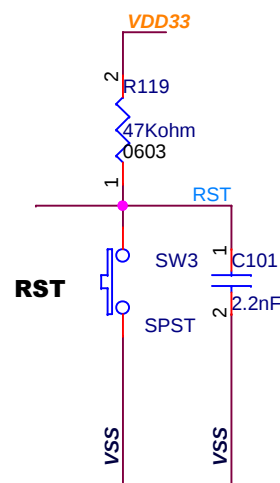
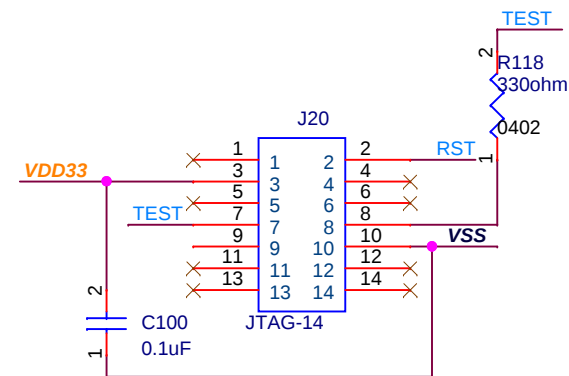
HEADER 7X2



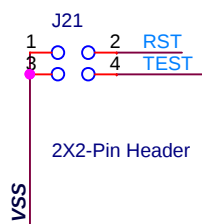
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Put RST switch at the bottom edge of the board. Mark down every Netname to the position



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Rev 1A

-Initial version

Rev 1B

- Changed R21, R29, R52 to 240K
- Changed R22, R30, R53 to 196K
- Changed R23, R31, R54 to 162K
- Changed R24, R32, R55 to 120K
- Changed R25, R33, R56 to 100K
- Changed C3, C4, C6, C7 to 0.033uF
- Added C102, C103 0.1uF
- Corrected BOM mfr p/n for: C15, C44, C51, C57, C58, C62, C70, C74, C78, C84, C85, C97
- Added C86, C87, C95 0.01uF
- Updated BOM p/n for J14

Rev 1C

- Remapped J14 signals for flowthrough
- Deleted R39, R51

Rev 1D

- Connected signal " GPIO5/SPI(SOMI)/UART(RXD) " to the J12
- Corrected BOM for R105, R111, R114
- Updated U5, U7 Value to match Part number in BOM

Rev 1E

- Added VSS connections to LED1, LED2
- Updated MODE_SEL0/1 resistors R5-R36
- Updated IDX resistors R40-R47
- J17 changed p/n
- Added R120
- Updated LED3 p/n

Revision History		
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