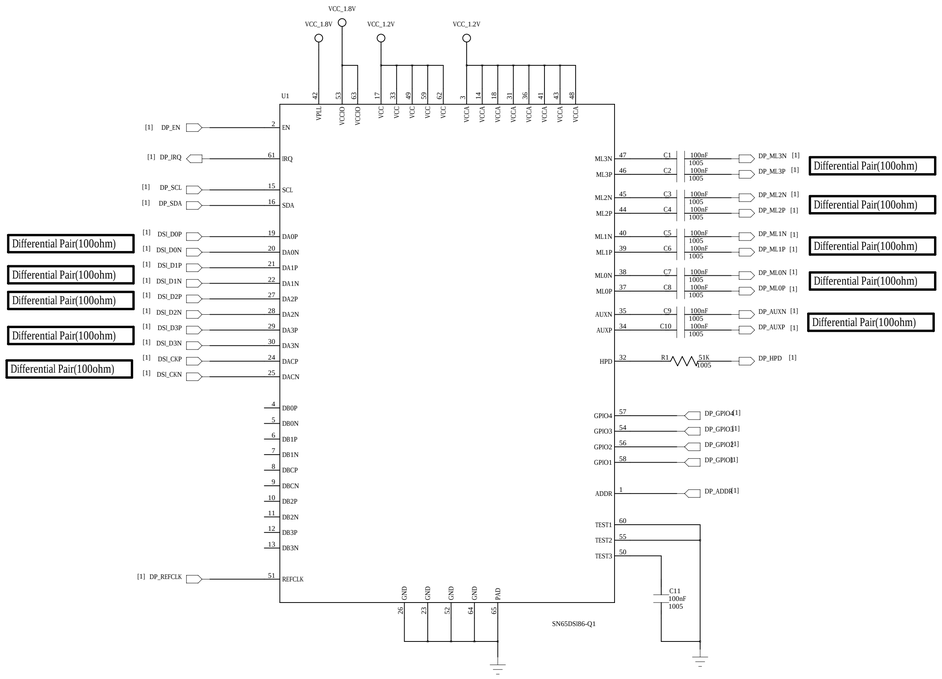
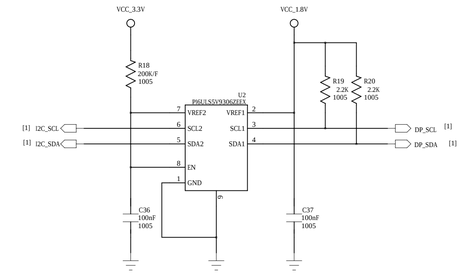
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

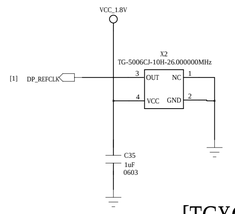
[DSI to DisplayPort]



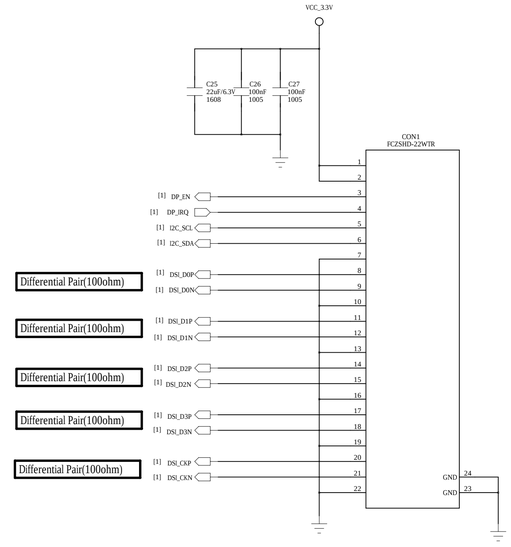
[I2C Level Shifter]



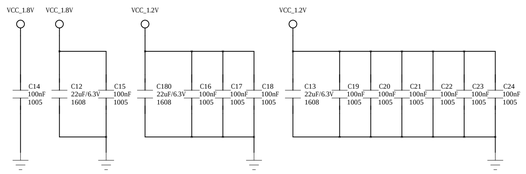
[TCXO]



[DSI INPUT CONNECTOR]



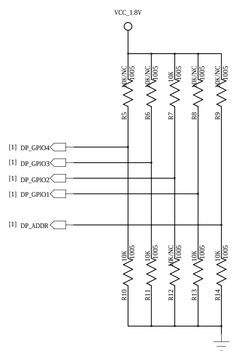
[Bypass CAP]



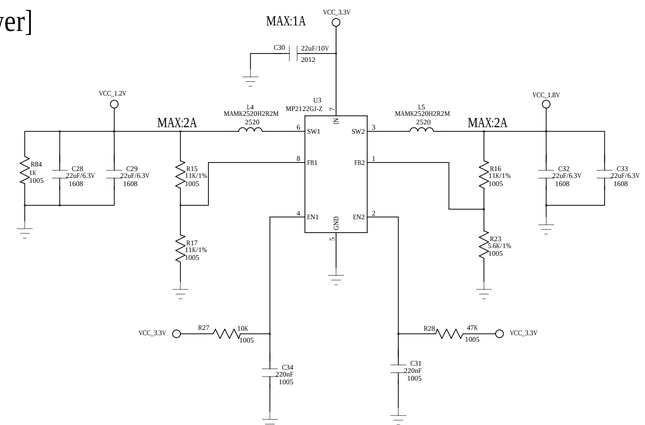
[Config]

GPIO[3:1]	REFCLK FREQUENCY
3'b000	12MHz
3'b001	19.2MHz
3'b010	26MHz
3'b011	27MHz
3'b100	38.4MHz
3'b101~111	19.2MHz

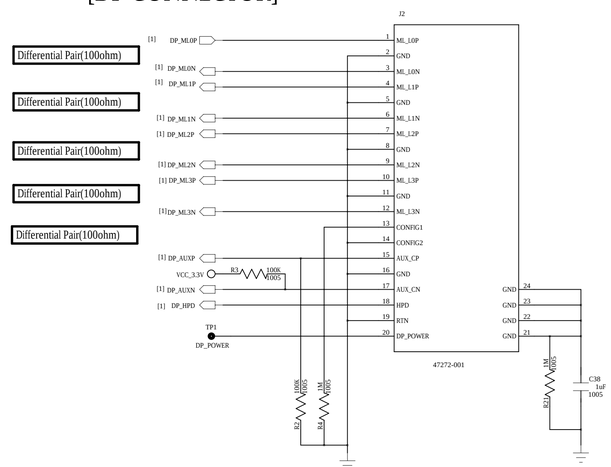
ADDR	I2C ADDRESS
0	0x58 (Write) / 0x59 (Read)
1	0x5A (Write) / 0x5B (Read)



[Power]



[DP CONNECTOR]



COMPANY: PittaSoft			
TITLE: DSI to DP			
DRAWN: <Drawn By>	DATED: <Drawn Date>		
CHECKED: <Checked By>	DATED: <Checked Date>	CODE:	SIZE: A1
QUALITY CONTROL: <QC By>	DATED: <QC Date>	DRAWING NO:	REV: 0.1
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>	SHEET: 1 OF 1