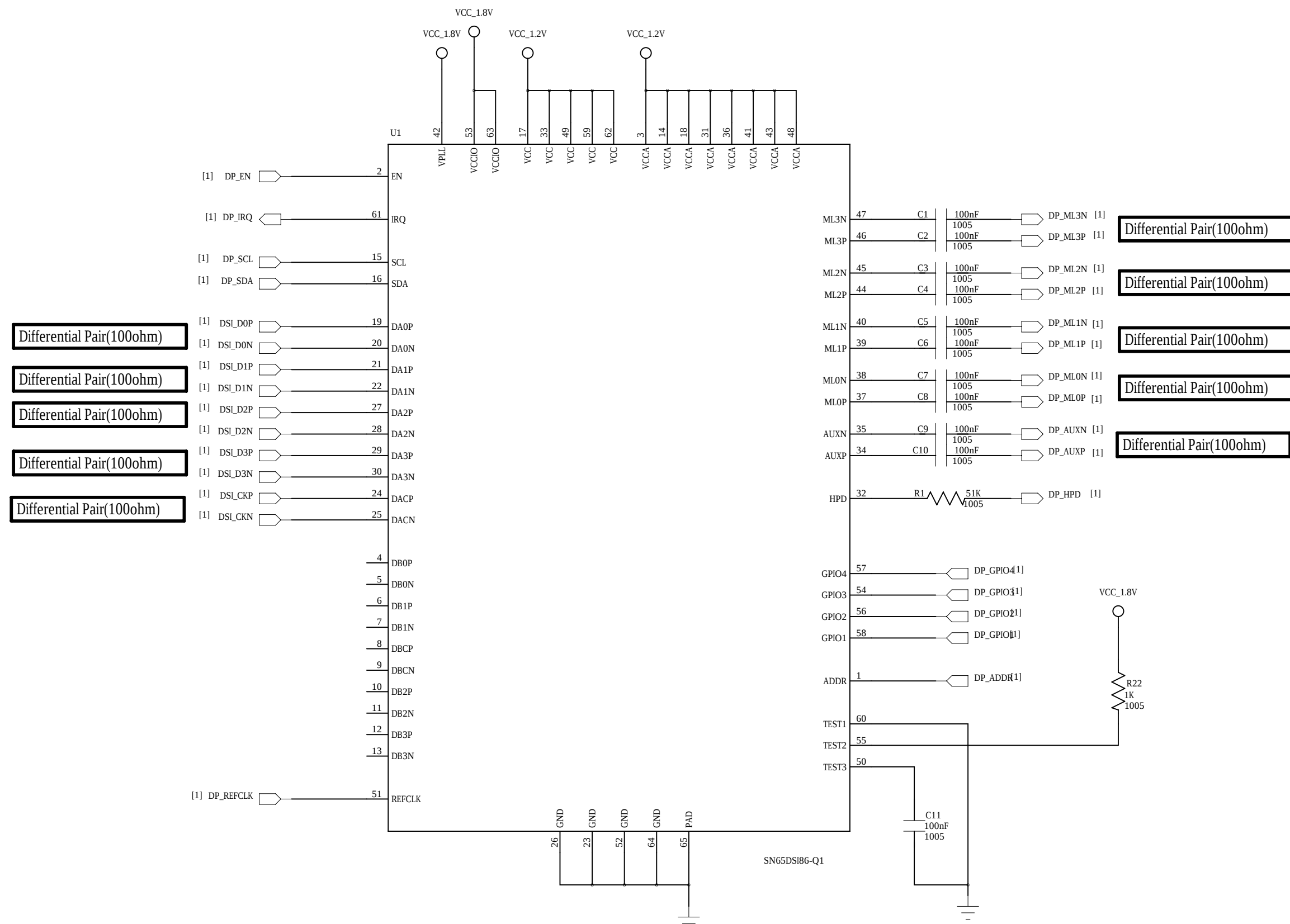
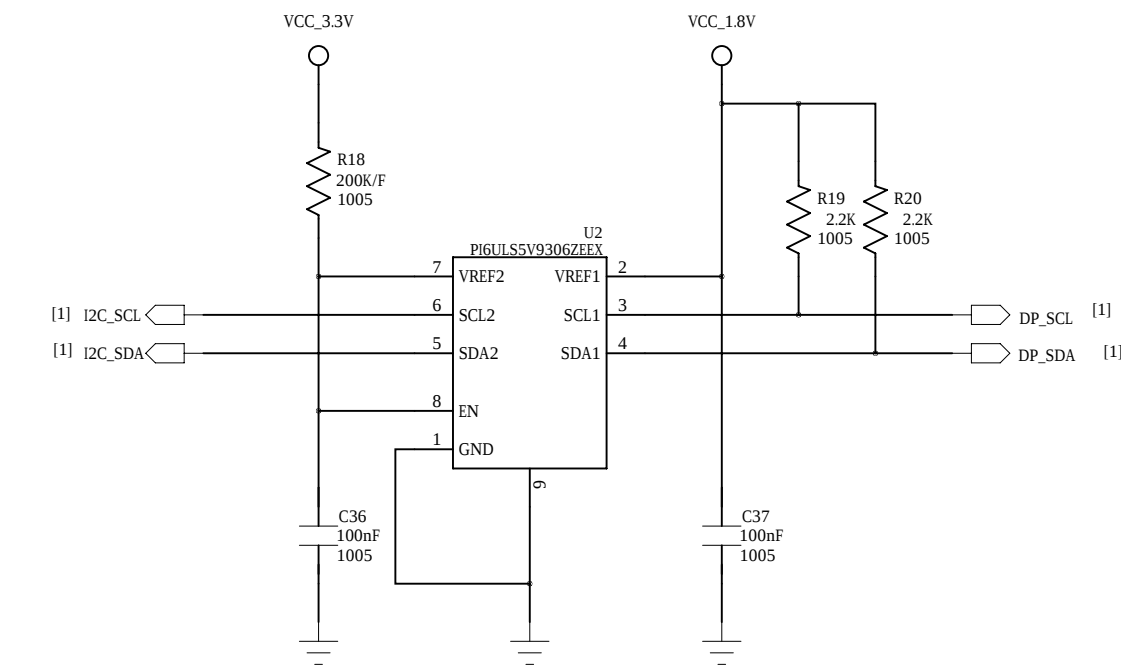


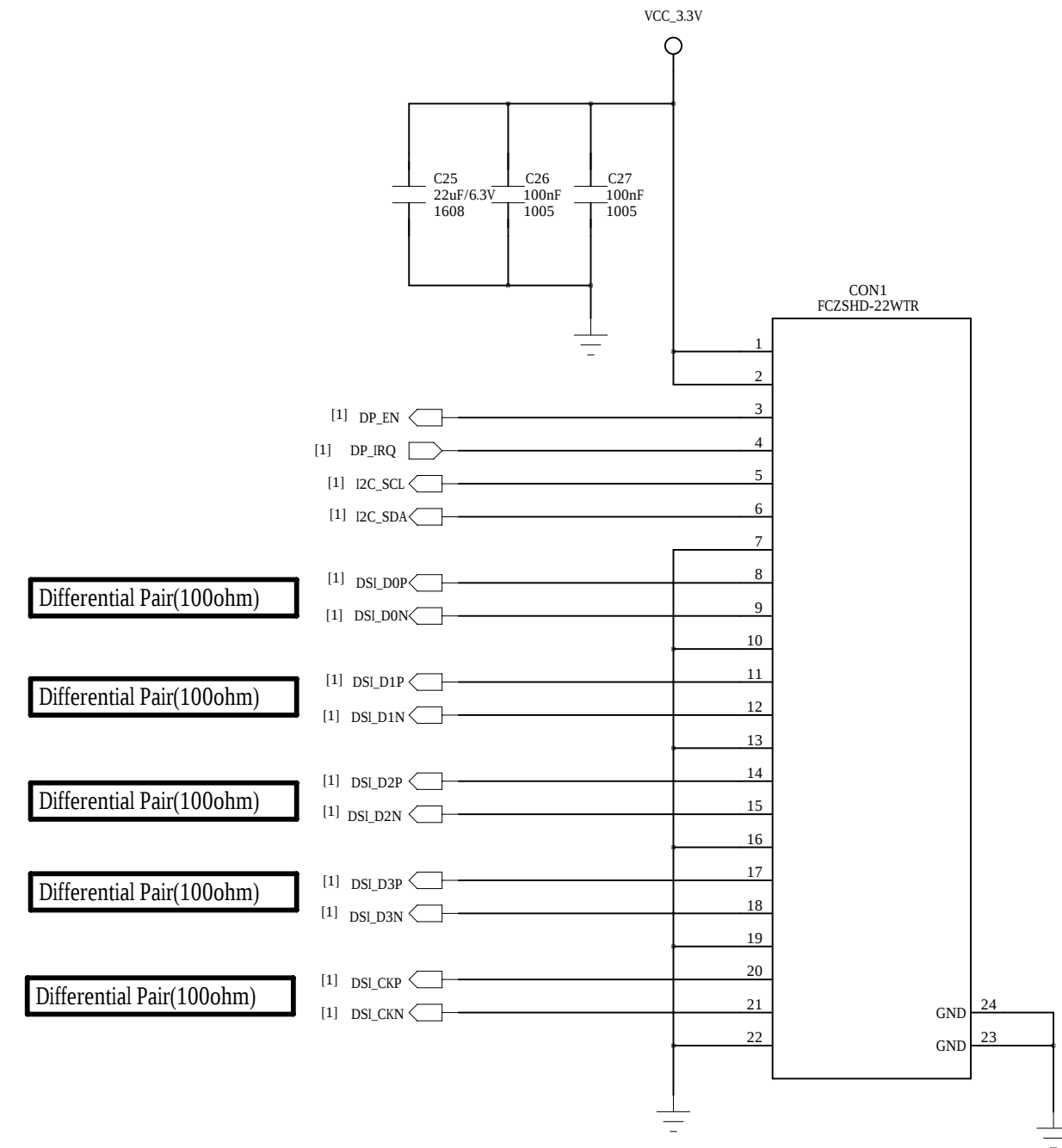
[DSI to DisplayPort]



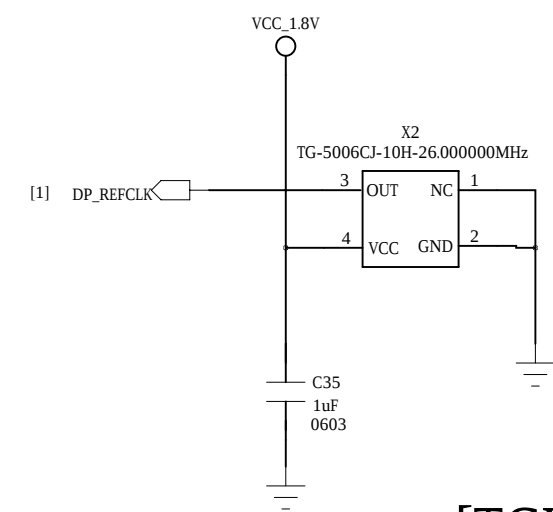
[I2C Level Shifter]



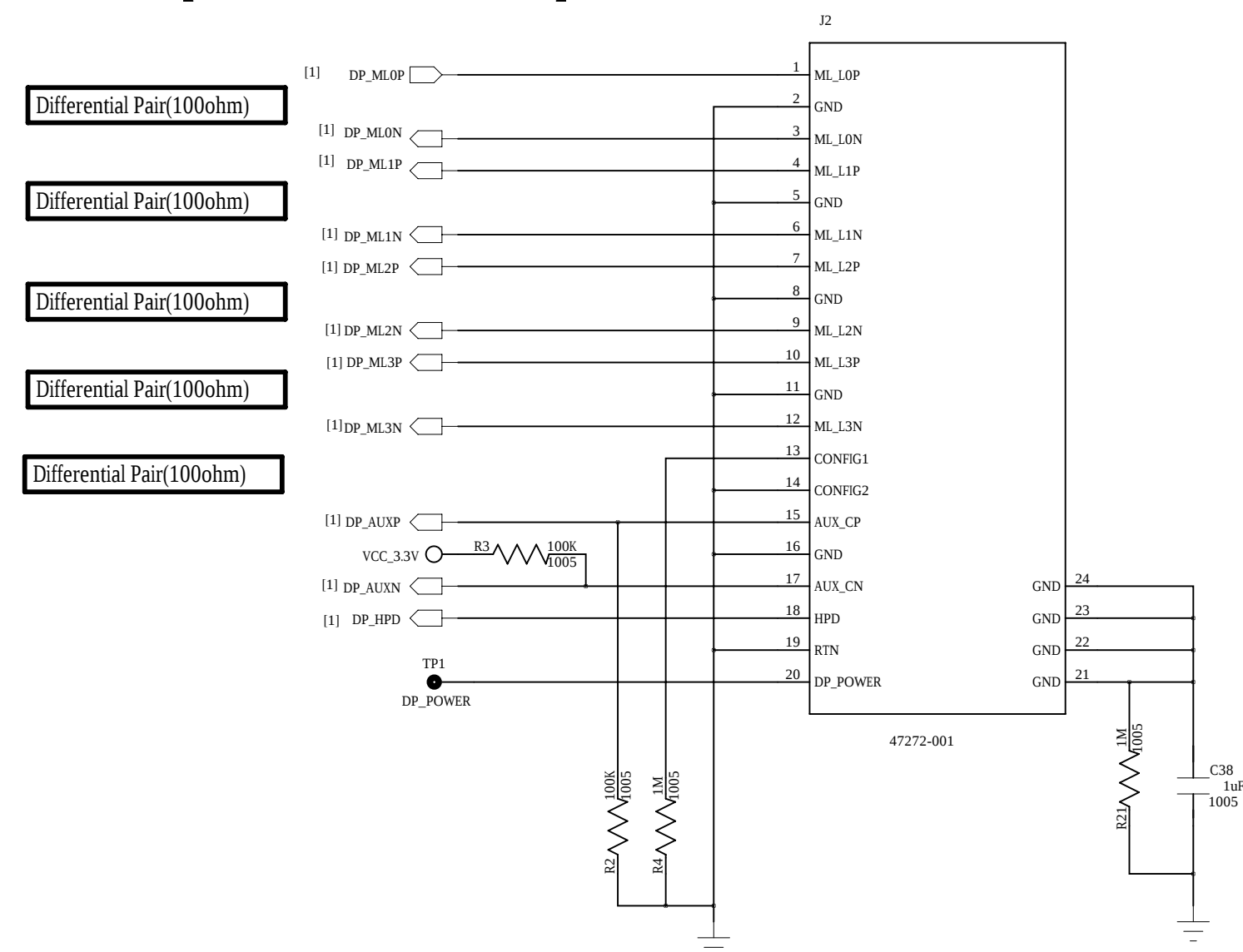
[DSI INPUT CONNECTOR]



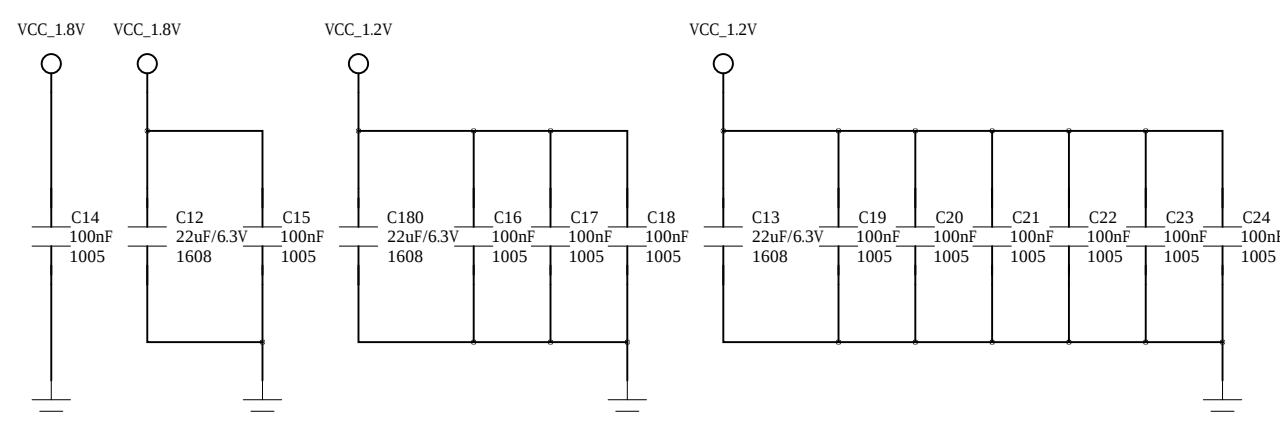
[TCXO]



[DP CONNECTOR]

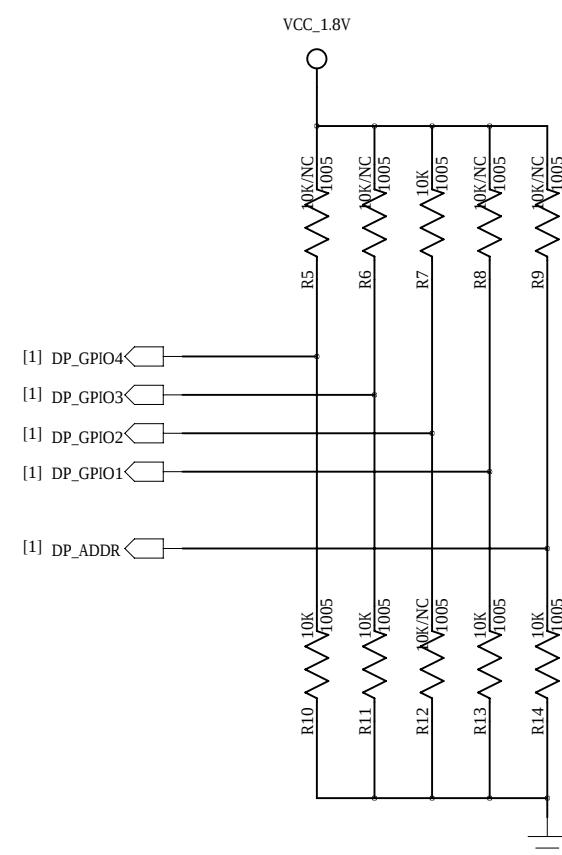


[Bypass CAP]

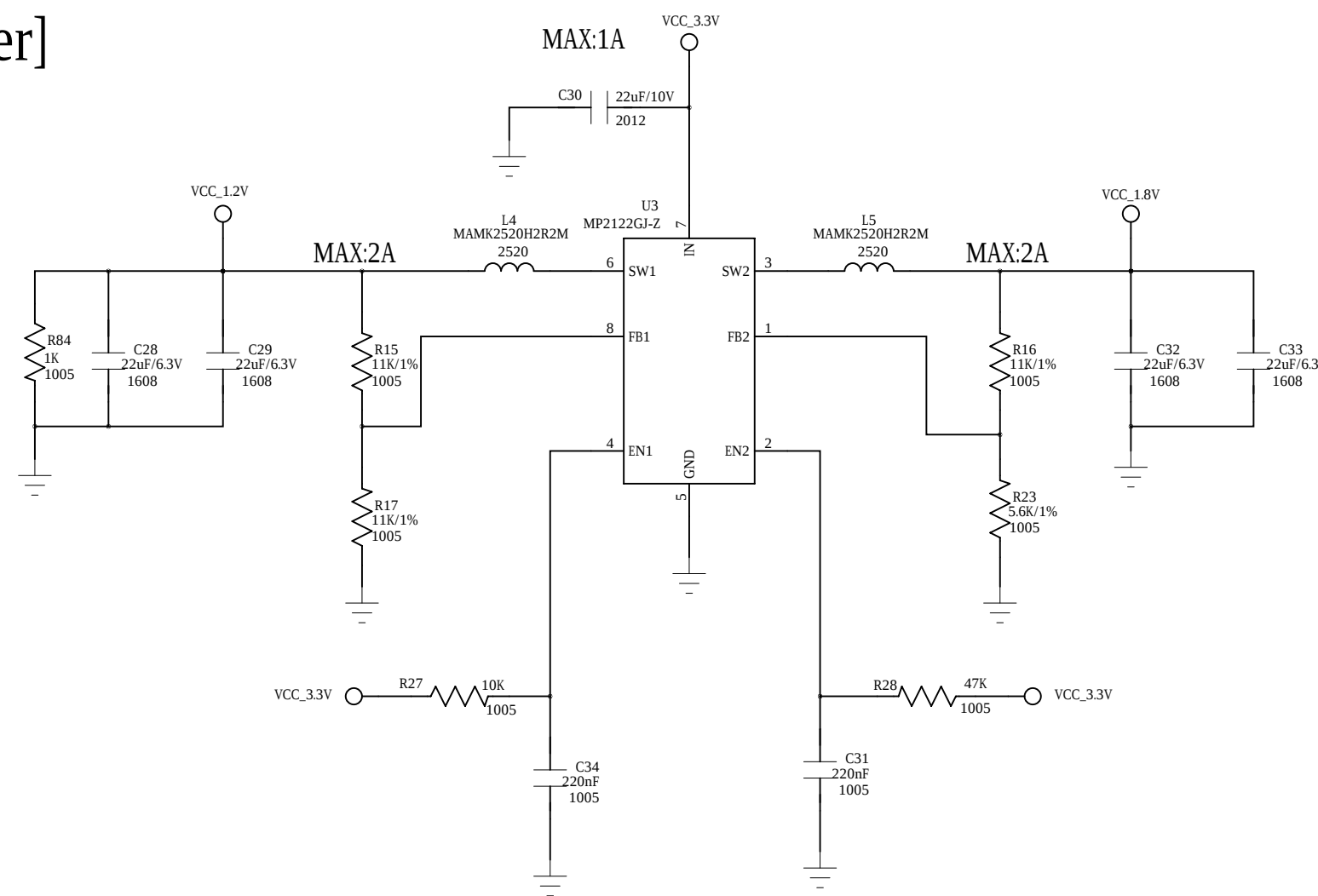


[Config]

GPI0[3:1]	REFCLK FREQUENCY
3 ₁ ⁻ b000	12MHz
3 ₁ ⁻ b001	19.2MHz
3 ₁ ⁻ b010	26MHz
3 ₁ ⁻ b011	27MHz
3 ₁ ⁻ b100	38.4MHz
3 ₁ ⁻ b101~111	19.2MHz



[Power]



ADDR	I2C ADDRESS
0	0x58 (Write) / 0x59 (Read)
1	0x5A (Write) / 0x5B (Read)