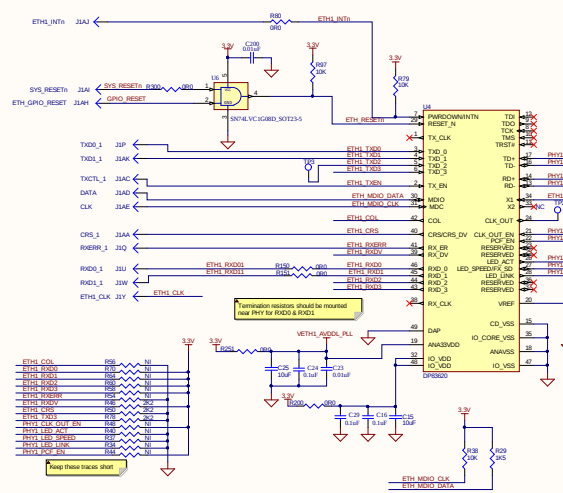


ETH0 & ETH1 channels should be length matched



Termination resistors should be mounted near PHY for RX0 & RX1

Place caps close to termination resistors and place both resistors and caps near TX0, TX1, and RX0, RX1

Place caps close to SPMR center tap

Place proper caps and resistors close to PHY

Place proper caps and resistors close to PHY

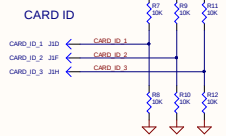
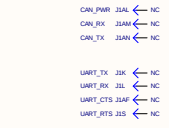
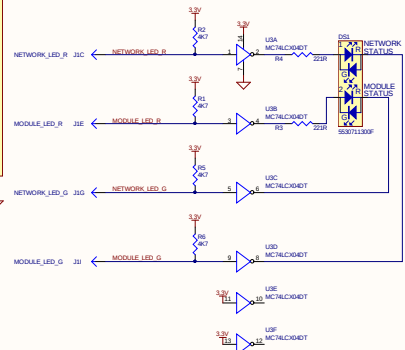


TABLE 1

Card Type	ID	3	2	1
Dual RJ-45	0	0	0	0
Dual Fiber	0	1	1	1
Mixed Eth	0	0	0	1
DeviceNet	1	0	0	0
Eth & Serial	1	0	1	1

Place proper caps and resistors close to PHY

Place proper caps and resistors close to PHY