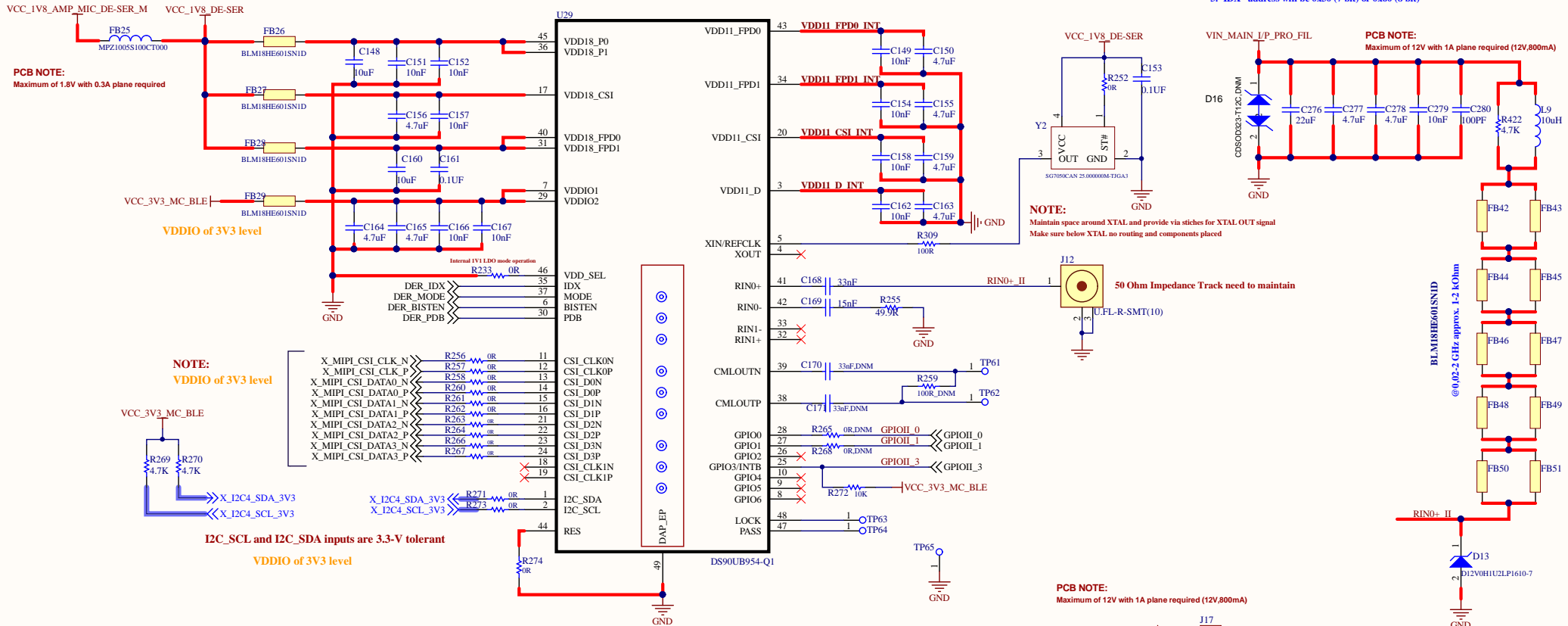


De-SERIALIZER



NOTE:
 1. Set "RX_PORT_CTL" register bit 1 = 0 to disable (To make RIN1 port unmount)
 2. Make "SEL3P3V" ass 1 to utilise "I2C_SCL, I2C_SDA and INTB" pins in 3v3 level
 3. "IDX" address will be 0x30 (7 bit) or 0x60 (8 bit)

PCB NOTE:
 Maximum of 1.8V with 0.3A plane required

PCB NOTE:
 Maximum of 12V with 1A plane required (12V,800mA)

VDDIO of 3V3 level

NOTE:
 VDDIO of 3V3 level

I2C_SCL and I2C_SDA inputs are 3.3-V tolerant

VDDIO of 3V3 level

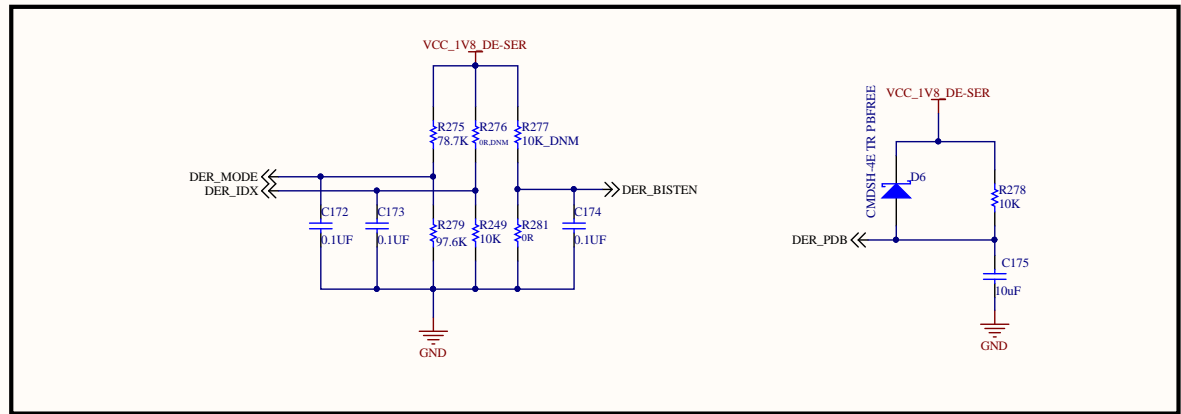
NOTE:
 Maintain space around XTAL and provide via stitches for XTAL OUT signal
 Make sure below XTAL no routing and components placed

50 Ohm Impedance Track need to maintain

PCB NOTE:
 Maximum of 12V with 1A plane required (12V,800mA)

Place ESD Diodes near to J12 connector (UFL) Conn

PCB NOTE:
 1). https://www.ti.com/lit/ug/snlu223d/snlu223d.pdf?ts=1706531172589&ref_url=https%253A%252F%252Fwww.ti.com%252Fproduct%252FDS90UB954-Q1



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