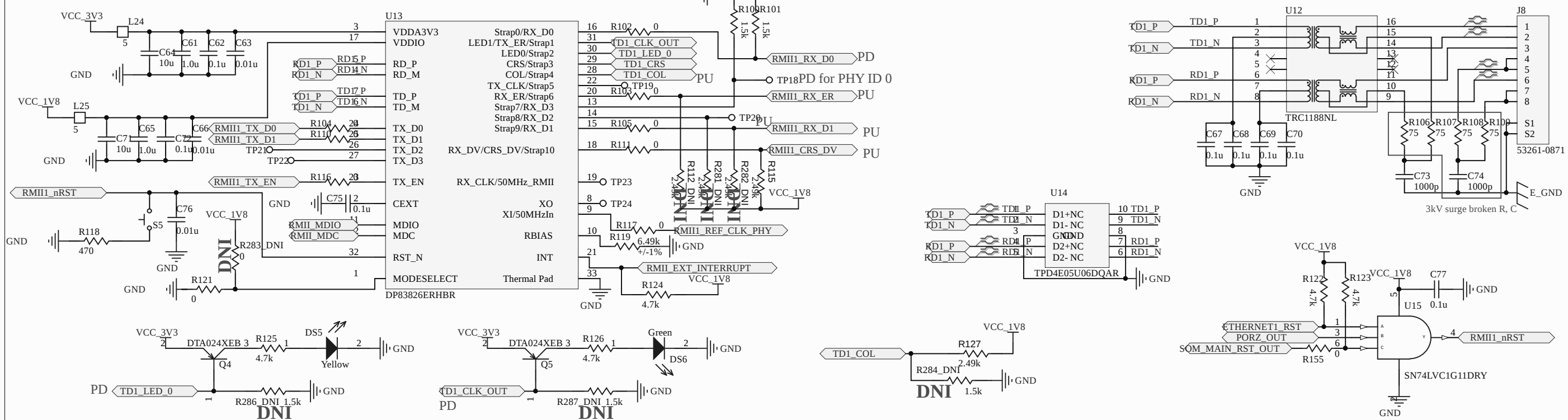
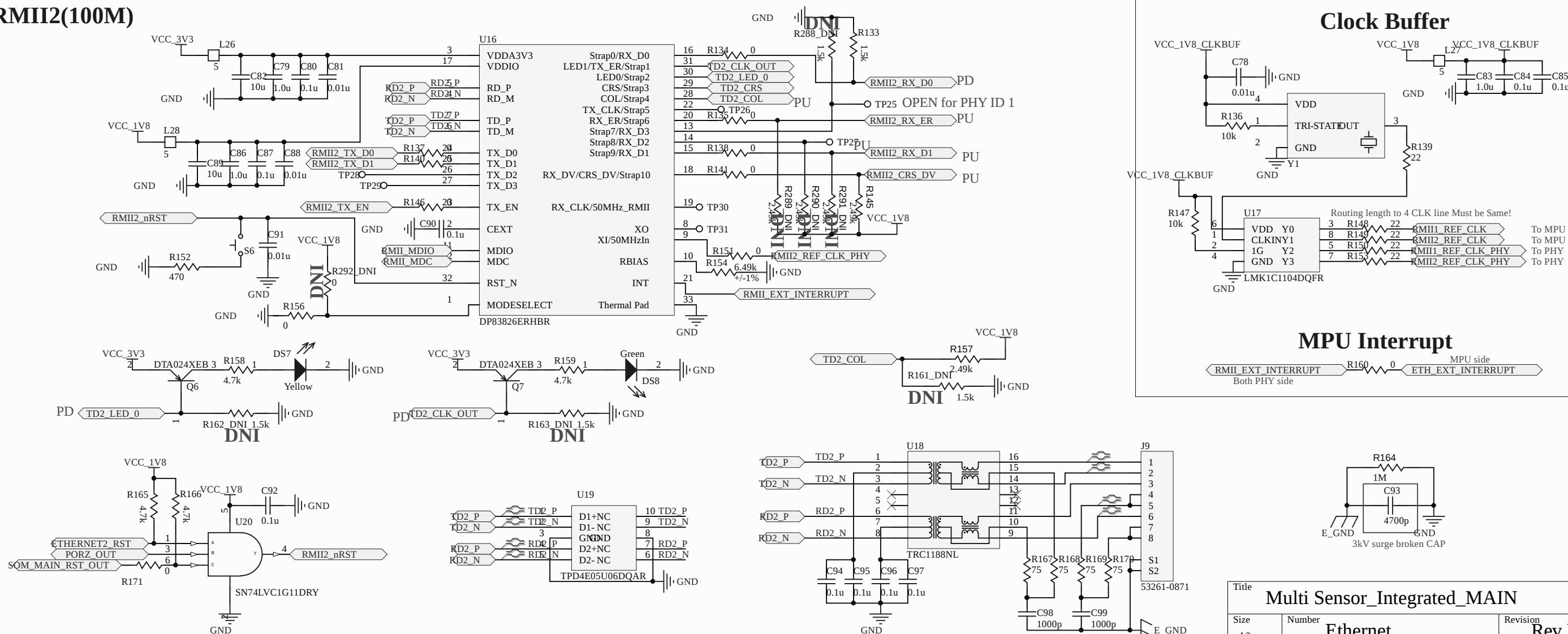
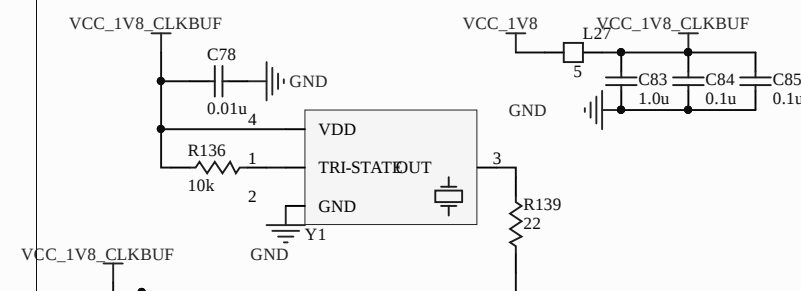


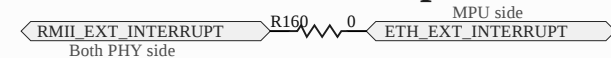
RMII1(100M)

**RMII2(100M)**

Clock Buffer



MPU Interrupt



Title			Multi Sensor_Integrated_MAIN		
Size	Number	Revision			
A3	Ethernet	Rev.E			
Date:	2025-02-12	Sheet	of		
File:	\\.\08 Ethernet Rev E.SchDoc	Drawn By:			