Table 7. Configuration Select (MODE_SEL0)

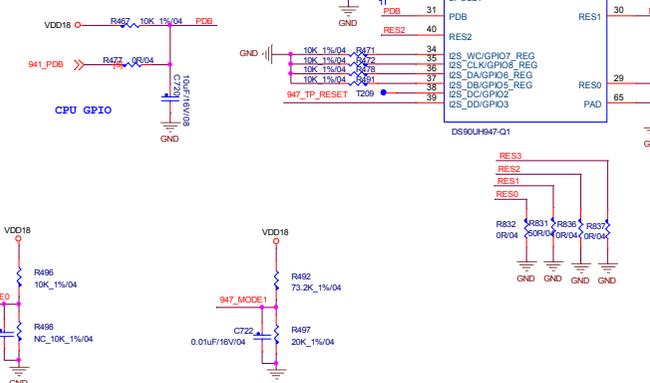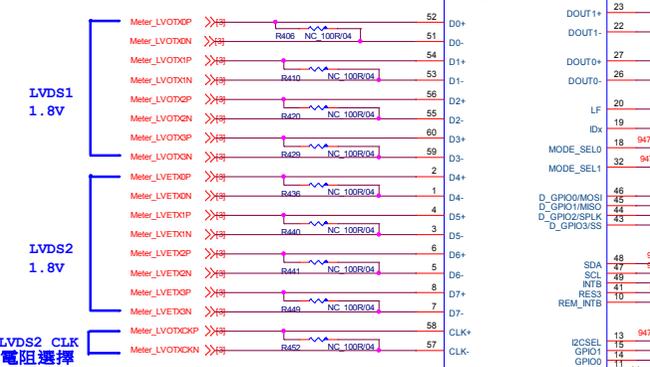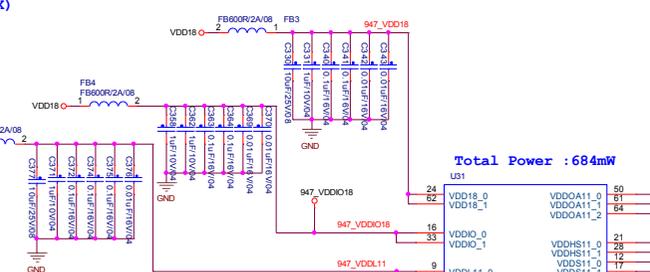| #	RATIO V_{R4}/V_{DD18}	TARGET V_{R4} (V)	SUGGESTED RESISTOR PULL-UP R3 kΩ (1% tol)	SUGGESTED RESISTOR PULL-DOWN R4 kΩ (1% tol)	OLDI_DUAL	REPEATER
1	0	0	OPEN	Any value less than 100	0	0
2	0.213	0.383	115	30.9	0	1
5	0.560	1.008	82.5	105	1	0
6	0.676	1.216	51.1	107	1	1

Table 8. Configuration Select (MODE_SEL1)

#	RATIO V_{R6}/V_{DD18}	TARGET V_{R6} (V)	SUGGESTED RESISTOR PULL-UP R5 kΩ (1% tol)	SUGGESTED RESISTOR PULL-DOWN R6 kΩ (1% tol)	MAPSEL	COAX
1	0	0	OPEN	Any value less than 100	0	0
2	0.213	0.383	115	30.9	0	0
3	0.328	0.591	107	52.3	0	1
4	0.444	0.799	113	90.9	0	1
5	0.560	1.008	82.5	105	1	0
6	0.676	1.216	51.1	107	1	0
7	0.792	1.425	30.9	116	1	1
8	1	1.8	Any value less than 100	OPEN	1	1

Table 9. Serial Control Bus Addresses For IDx

#	RATIO V_{R2}/V_{DD18}	IDEAL V_{R2} (V)	SUGGESTED RESISTOR R1 kΩ (1% tol)	SUGGESTED RESISTOR R2 kΩ (1% tol)	7-BIT ADDRESS	8-BIT ADDRESS
1	0	0	Any value less than 100	40.2	0x0C	0x18
2	0.212	0.381	133	35.7	0x0E	0x1C
3	0.327	0.589	147	71.5	0x10	0x20
4	0.442	0.795	115	90.9	0x12	0x24
5	0.557	1.002	90.9	115	0x14	0x28
6	0.673	1.212	66.5	137	0x16	0x2C
7	0.789	1.421	21.5	80.6	0x18	0x30
8	1	1.8	Any value less than 100	OPEN	0x1A	0x34

The strapped values can be viewed and/or modified in the following locations:
- OLDI_DUAL : Latched into OLDI_IN_MODE (0x4F[6], inverted from strap value).
- REPEATER : Latched into TX_RPTR (0xC2[5]).
- MAPSEL : Latched into OLDI_MAPSEL (0x4F[7]).
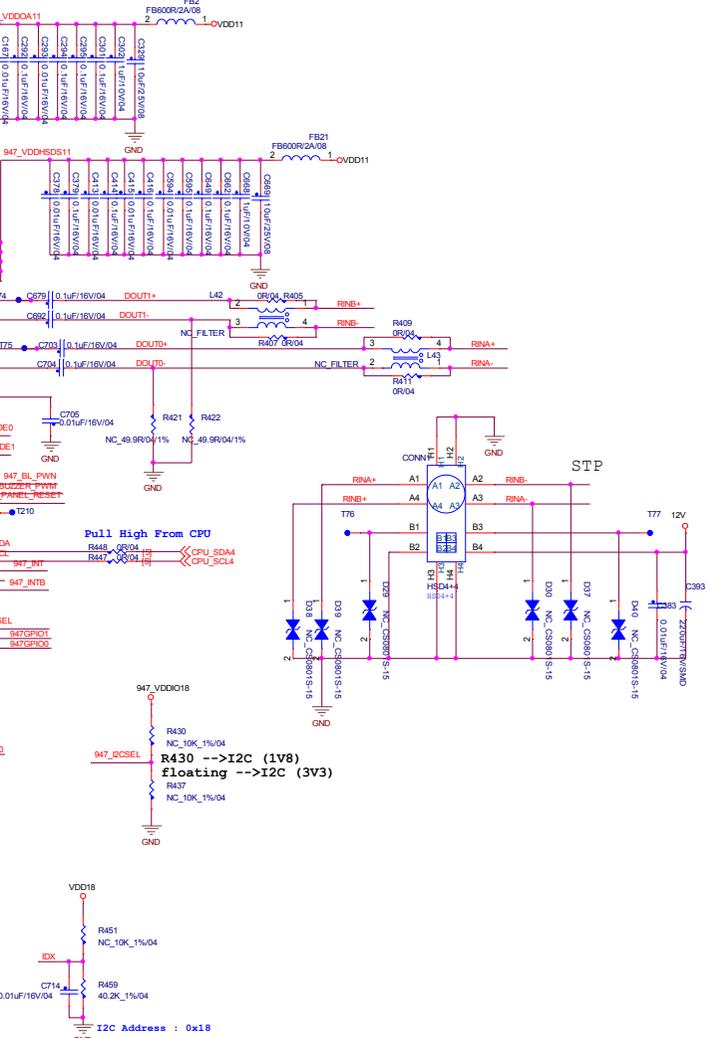- COAX : Latched into DUAL_CTL1[7], COAX_MODE (0x5B[7]).

IO_Level_shifter

LVDS1 1.8V

LVDS2 1.8V

LVDS1 or LVDS2 CLK
透過CPU端電阻選擇

CPU GPIO

75mA (947 MAX)
2A
Vout=0.5X(1+22K/8.45K)=1.8V
469mA (947 MAX)
2A
Vout=0.5X(1+14K/10K)=1.2V

Total Power : 684mW

Pull High From CPU

STP

R430 --> I2C (1V8)
floating --> I2C (3V3)

I2C Address : 0x18