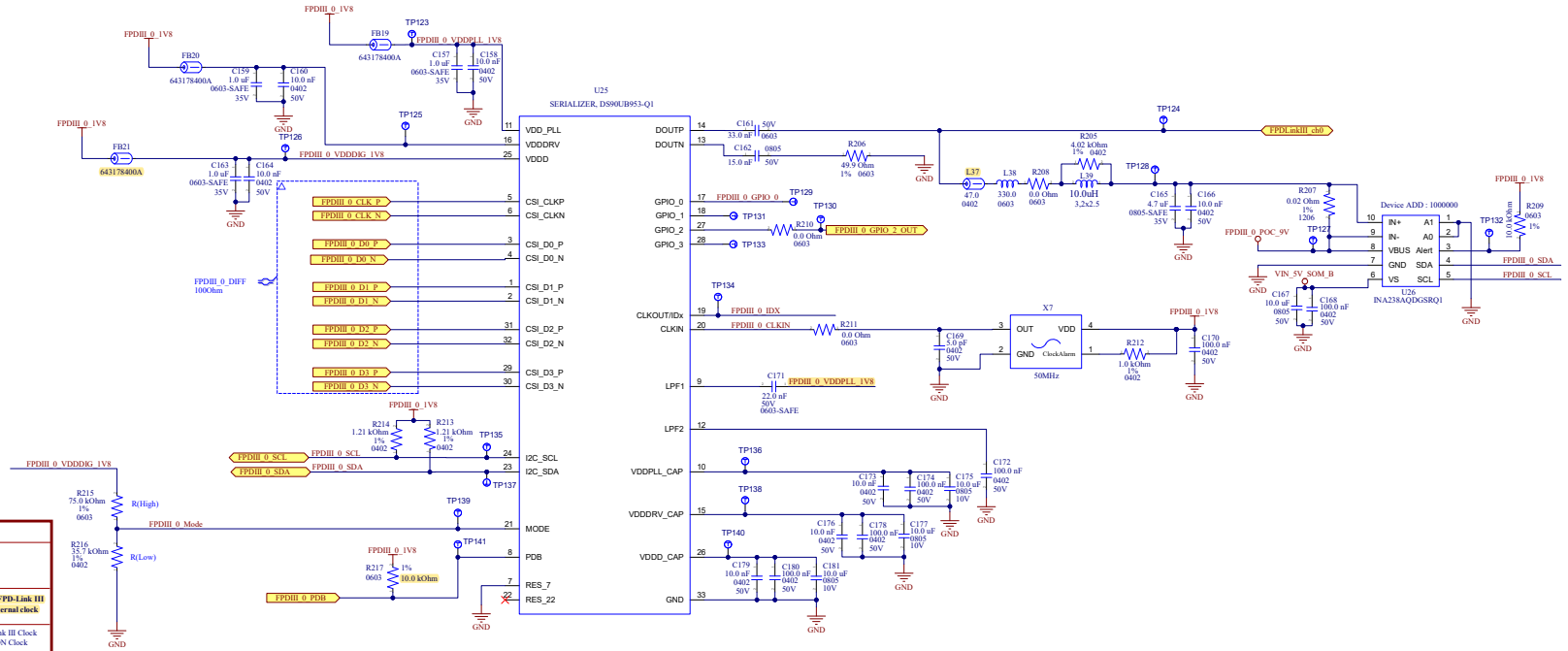
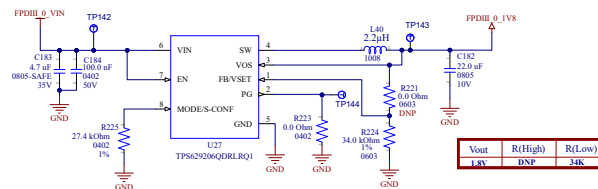
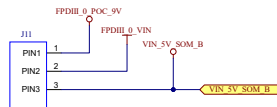


FPDLinkIII_CHANNEL-0

Strap Configuration Mode Select			
V(Target) STRAP VOLTAGE	Suggested Strap resistance(±% TOL)		DESCRIPTION
V(VDD)=1.8V	R(High)	R(Low)	
0.586	75k	35.7k	CSI-2 Non-synchronous clock - FPD-Link III Clock reference derived from external clock reference input on CLKIN pin.
0.729	71.5k	56.2k	CSI-2 Non-synchronous - FPD-Link III Clock reference derived from internal AON Clock

FPDLink III -0 VOLTAGE SELECTION



VCC	GPIO VIN (V)	CODE
VIN < 1.41	VIN < 0.85	000
1.41 < VIN < 1.49	0.85 < VIN < 0.90	001
1.49 < VIN < 1.57	0.90 < VIN < 0.95	010
1.57 < VIN < 1.65	0.95 < VIN < 1.00	011
1.65 < VIN < 1.73	1.00 < VIN < 1.05	100
1.73 < VIN < 1.82	1.05 < VIN < 1.10	101
1.82 < VIN < 1.9	1.10 < VIN < 1.15	110
1.9 < VIN < 2	1.15 < VIN < 1.21	111

Note : GPIO_0