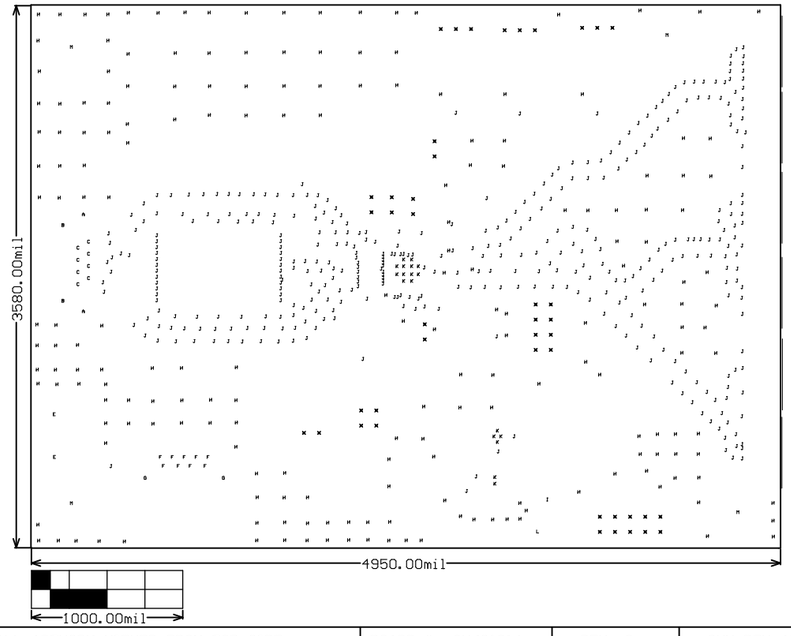
Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0.70mil	3.5	
3	Top Layer	Copper	1.78mil		
4	Dielectric1	FR-4	8.00mil	4.8	
5	Signal Layer 1	Copper	1.30mil		
6	Dielectric 3		8.00mil	4.2	
7	Signal Layer 2	Copper	1.30mil		
8	Dielectric 4		20.00mil	4.2	
9	Signal Layer 3	Copper	1.30mil		
10	Dielectric 5		8.00mil	4.2	
11	Signal Layer 4	Copper	1.30mil		
12	Dielectric 2		8.00mil	4.2	
13	Bottom Layer	Copper	1.78mil		
14	Bottom Solder	Solder Resist	0.70mil	3.5	
15	Bottom Overlay				

Symbol	Quantity	Finished Hole Size	Plated	Hole Type
J	387	6.00mil (0.152mm)	PTH	Round
K	18	7.87mil (0.200mm)	PTH	Round
H	199	10.00mil (0.254mm)	PTH	Round
I	1	20.00mil (0.508mm)	PTH	Round
F	9	27.56mil (0.700mm)	PTH	Round
L	1	28.00mil (0.711mm)	PTH	Round
C	8	35.04mil (0.890mm)	PTH	Round
M	4	37.99mil (0.965mm)	PTH	Round
✖	43	40.00mil (1.016mm)	PTH	Round
A	2	61.81mil (1.570mm)	PTH	Round
G	2	90.55mil (2.300mm)	PTH	Round
E	2	98.43mil (2.500mm)	PTH	Round
B	2	128.00mil (3.251mm)	PTH	Round
	678 Total			



DESIGN INFORMATION

MIN. TRACK WIDTH: 8 MIL
MIN. CLEARANCE: 0.2 mm
MIN. VIA PAD SIZE: 24 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL

MATERIAL:
[ ] FR-408 [X] FR-4 High Tg [ ] OTHER
THICKNESS: [X] 62 MIL (1.6mm) +/-10% [ ] OTHER
TOLERANCE: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/-
BOW & TWIST: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/-

DRILLING:
REFERENCE: [X] AS SHOWN [X] NC_DRILL FILES
PTH MIN COPPER THICKNESS: [X] 1MIL [ ] OTHER
BOARD FINISH:
SILKSCREEN: [X] TOP [X] BOTTOM
SILKSCREEN COLOR: [X] WHITE [ ] OTHER
SOLDER RESIST COLOR:
[X] GREEN [ ] BLUE [ ] OTHER
SURFACE FINISH: [X] IMMERSION GOLD (ENIG) [ ] ENEPIG
[ ] IMM. TIN/SILVER OR EQUIV [ ] OTHER
ARRAY/PANEL: [ ] CUT AND TRIM PER MECH LAYER 1
[ ] N.C. ROUTE [X] V. SCORE
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF:
[X] ANSI IPC-A-600F CLASS -> [ ] 1 [X] 2 [ ] 3
[X] RoHS [ ] OTHER PER ORDER
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS.
PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER
ADDITIONAL REQUIREMENTS:
MICROSECTION: [ ] YES
BARE BOARD ELEC. TEST: [ ] NONE [X] REQUIRED [ ] PER ORDER
MANUFACTURER'S UL: [ ] RAIL [ ] METAL [X] SILK

TEXAS INSTRUMENTS

PROJECT TITLE: DP83867ERGZ-S-EVM
DESIGNED FOR: Public Release
FILE NAME: SV601214C.PcbDoc

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SV601214	REV: C	SVN REV: Not In VersionControl
LAYER NAME = [illegible]	TID #: N/A		
PLOT NAME = Fabrication Drawing	GENERATED : 3/21/2022 9:46:52 AM		TEXAS INSTRUMENTS

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ENGINEER:	LAYOUT BY:
Aniruddha Khadye	John Hamilton
SCALE: 1.00	ALTIUM DESIGNER VERSION: 18.1.11.251