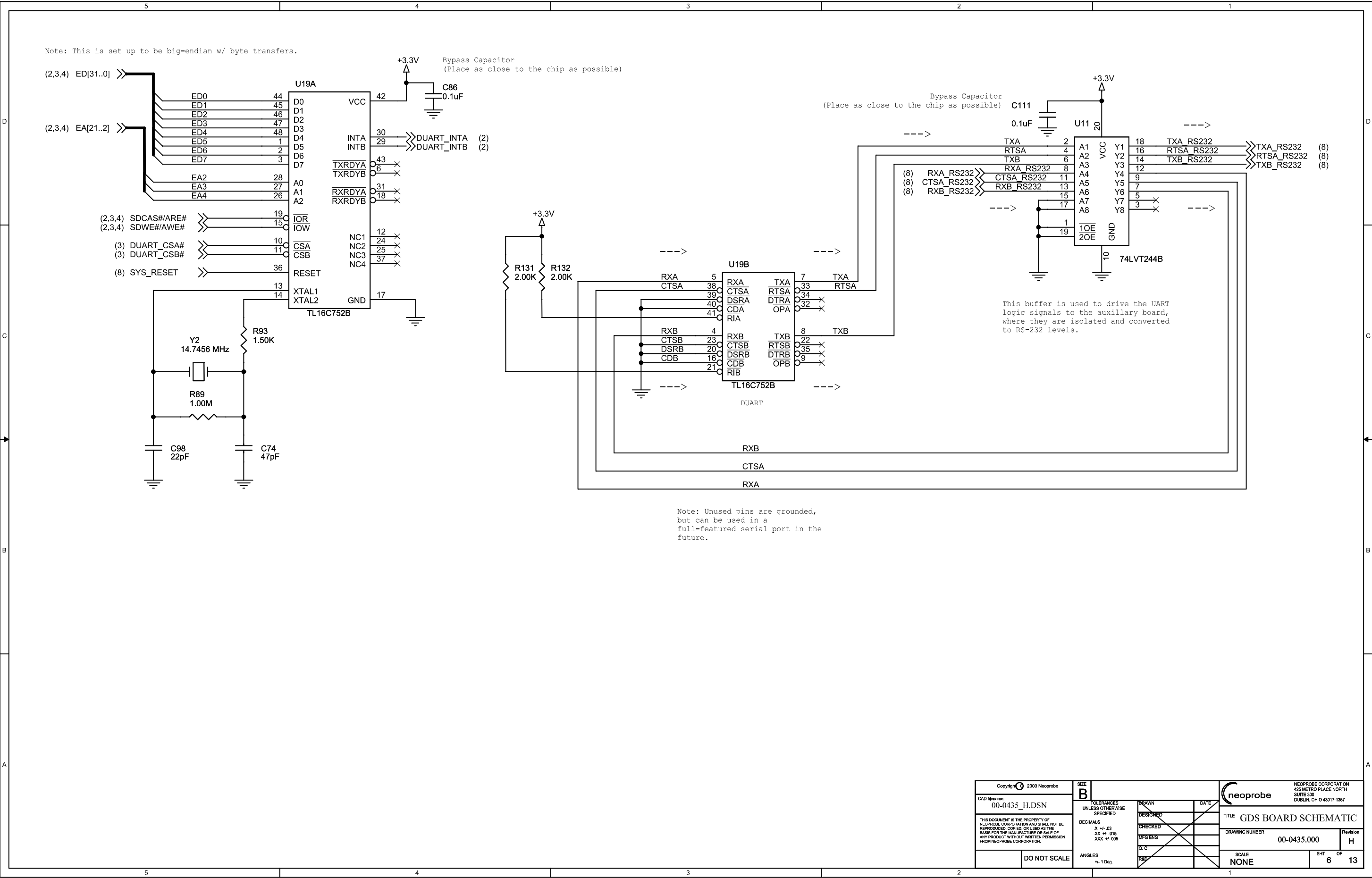




Note: This is set up to be big-endian w/ byte transfers.

+3.3V Bypass Capacitor
(Place as close to the chip as possible)

+3.3V Bypass Capacitor
(Place as close to the chip as possible)

This buffer is used to drive the UART logic signals to the auxillary board, where they are isolated and converted to RS-232 levels.

Note: Unused pins are grounded, but can be used in a full-featured serial port in the future.

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CAD filename: 00-0435_H.DSN		TOLERANCES UNLESS OTHERWISE SPECIFIED DECIMALS X +/- .03 XX +/- .015 XXX +/- .005	DESIGNED	DATE	TITLE GDS BOARD SCHEMATIC		Revision
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			MFG ENG				
			D. C.				
DO NOT SCALE		ANGLES +/- 1 Deg.	SCALE NONE	SHT 6	OF 13		