

# PD CONTROLLER TI 65994

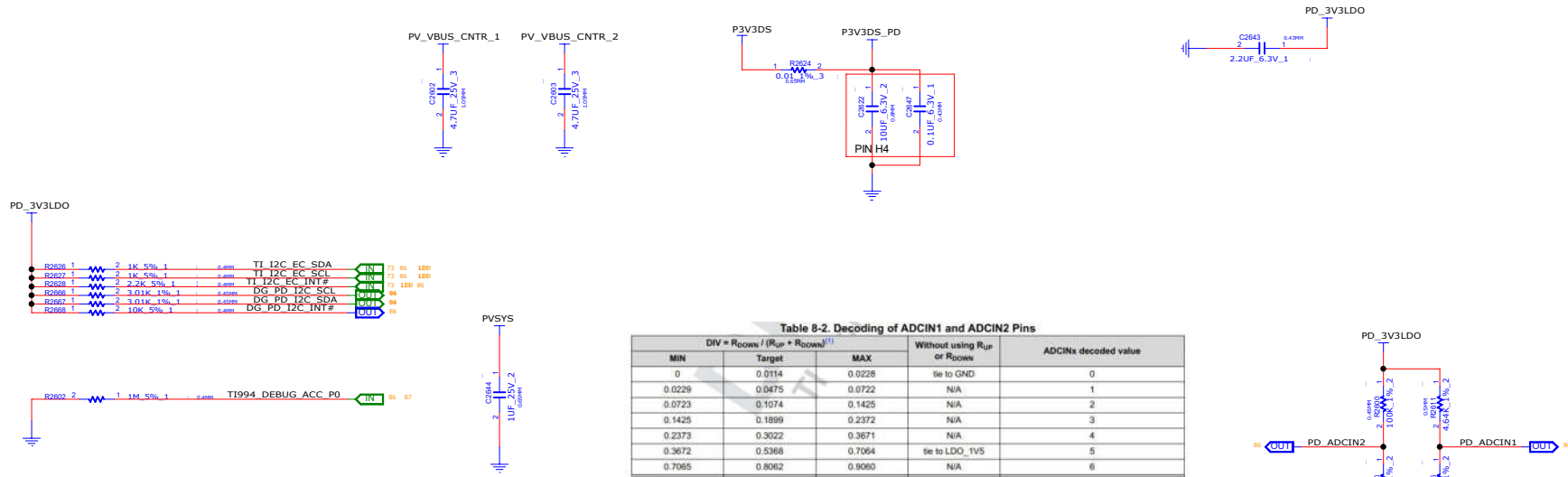
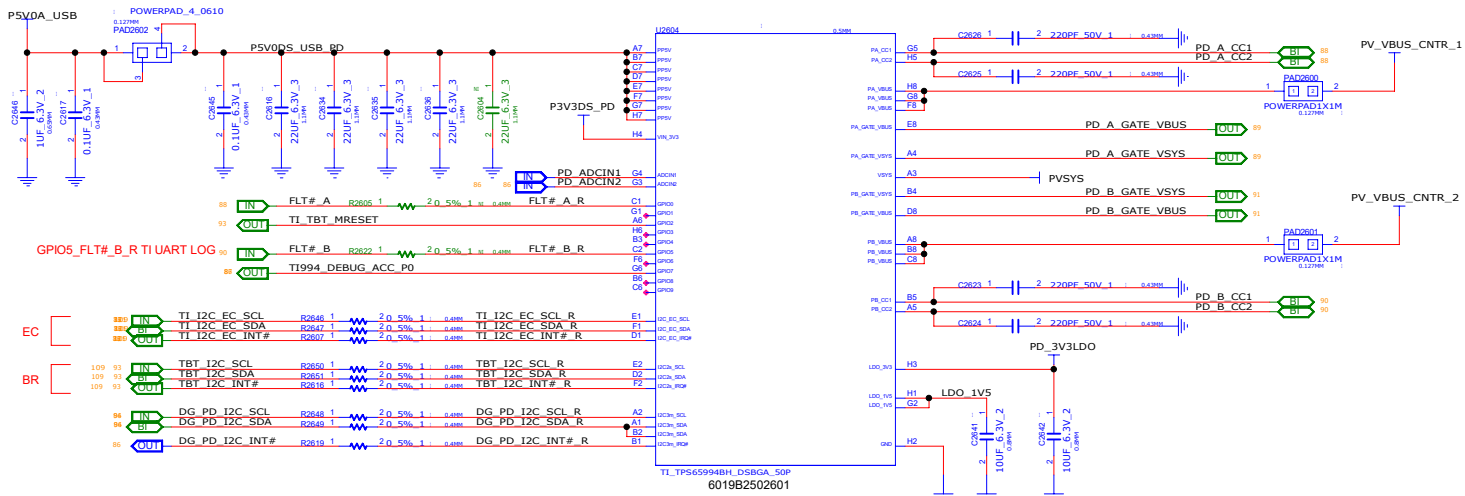


Table 8-2. Decoding of ADCIN1 and ADCIN2 Pins

DIV = R <sub>DOWN</sub> / (R <sub>UP</sub> + R <sub>DOWN</sub> ) <sup>2</sup>			Without using R <sub>UP</sub> or R <sub>DOWN</sub>	ADCINx decoded value
MIN	Target	MAX	tie to GND or tie to LDO_1V5	
0	0.0114	0.0228	tie to GND	0
0.0228	0.0475	0.0722	N/A	1
0.0723	0.1074	0.1425	N/A	2
0.1425	0.1899	0.2372	N/A	3
0.2373	0.3022	0.3671	N/A	4
0.3672	0.5368	0.7064	tie to LDO_1V5	5
0.7065	0.8082	0.9060	N/A	6
0.9061	0.9530	1.0	tie to LDO_3V3	7

DECODED VALUE:  
ADCIN1:7  
ADCIN2:3

LOCATION NUMBER:2600~2699

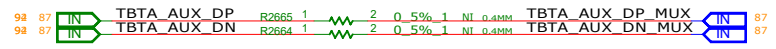
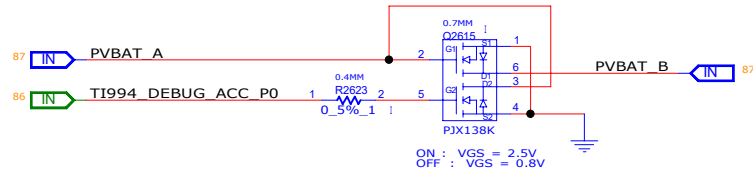
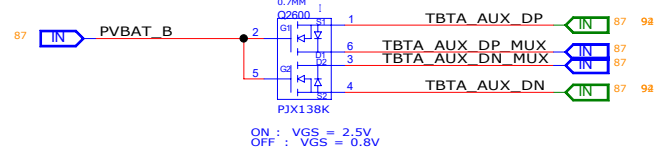
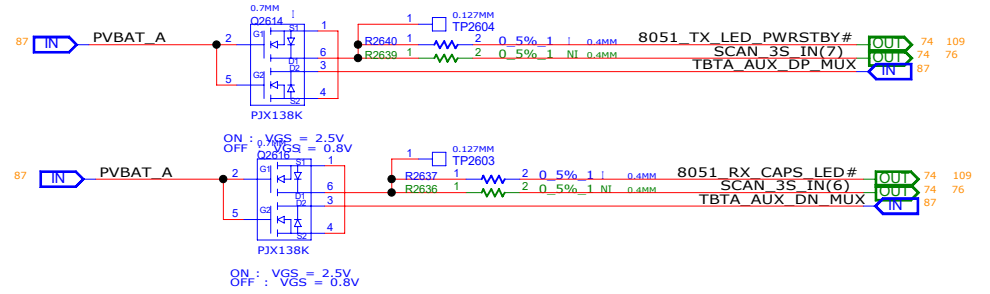
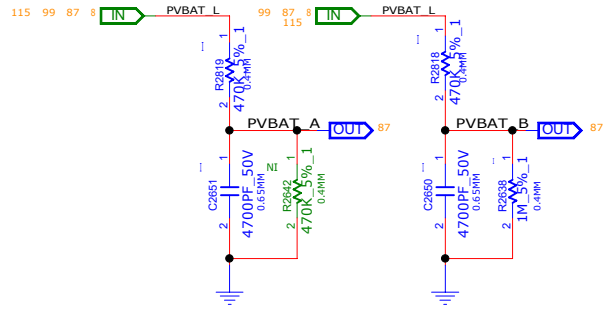
**INVENTEC**

TITLE  
MODEL,PROJECT,FUNCTION  
USBC\_PD\_1

CHANGE# PCB PIN XXXXXXXXXXXX DATE PCB VER 21-OCT-2002 XXX

SIZE A3 CODE CS DOC NUMBER 1310XXXXXXXXX-0-0 REV X01 SHEET 01 of 01

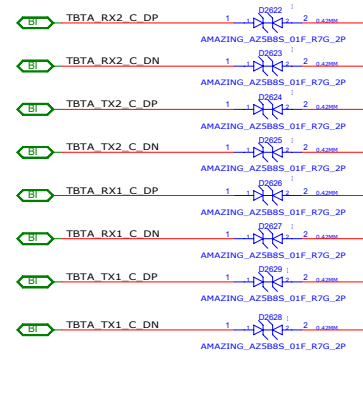
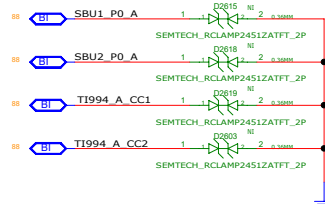
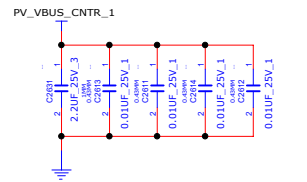
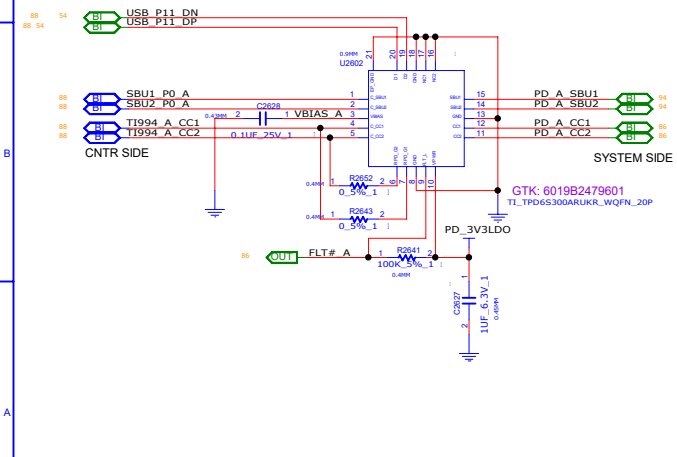
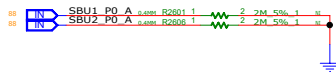
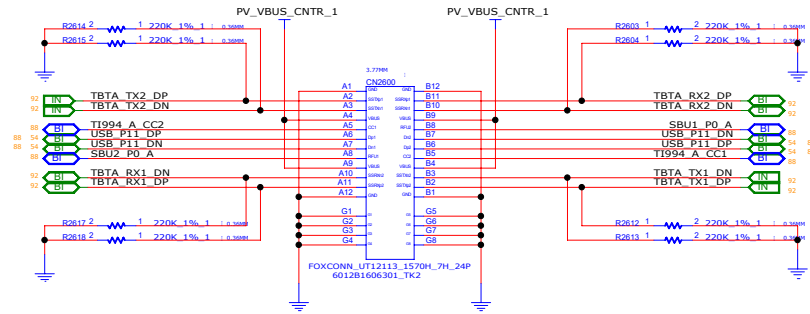
# PD DEBUG FUNCTION



LOCATION NUMBER:2600~2699

<b>INVENTEC</b>			
TITLE MODEL,PROJECT,FUNCTION			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
CHANGE by PCB P/N		DATE PCB VER	21-OCT-2002 XXX
SHEET		23 of 32	

# USB TYPE-C PORT A USB TYPE - C CONN



LOCATION NUMBER:2600~2699

**INVENTEC**

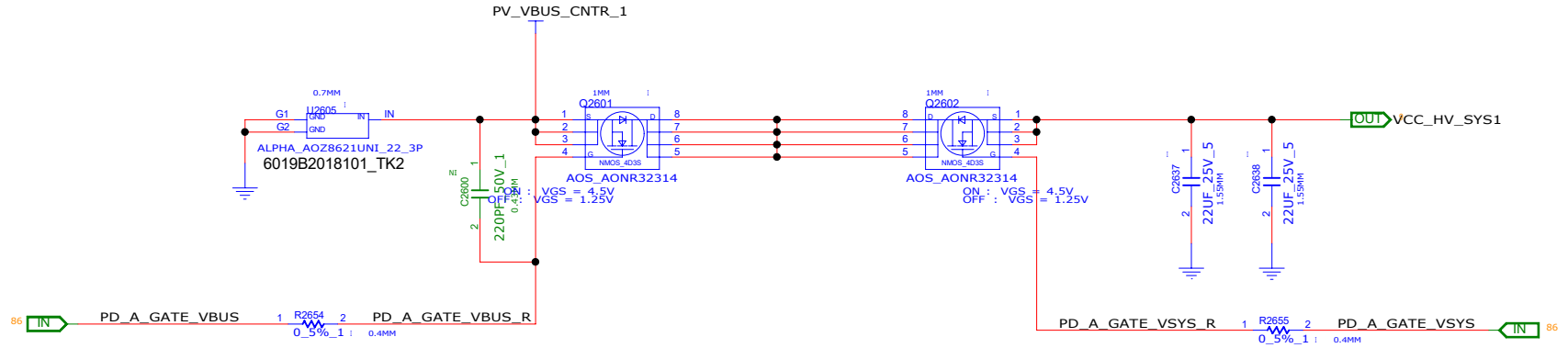
TITLE MODEL\_PROJECT\_FUNCTION  
USB\_TYPE-C\_A-1

CHANGE # XXX DATE 21-OCT-2002  
P/B.P/N 60xxxxxxx SHEET VER XXX

SIZE A1 CODE 1310XXXXX-D REV V01  
SHEET 41 of 41

# USB TYPE-C PORT A

## HIGH VOLTAGE PATH



LOCATION NUMBER:2600~2699

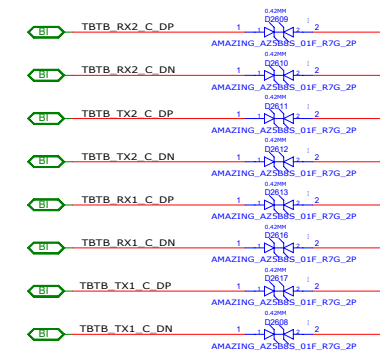
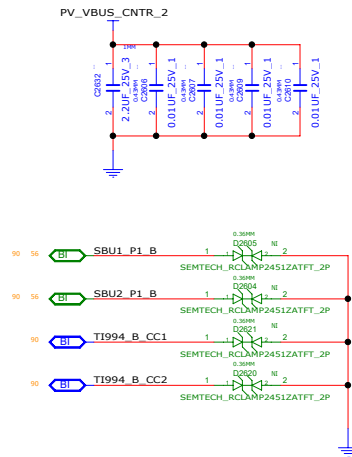
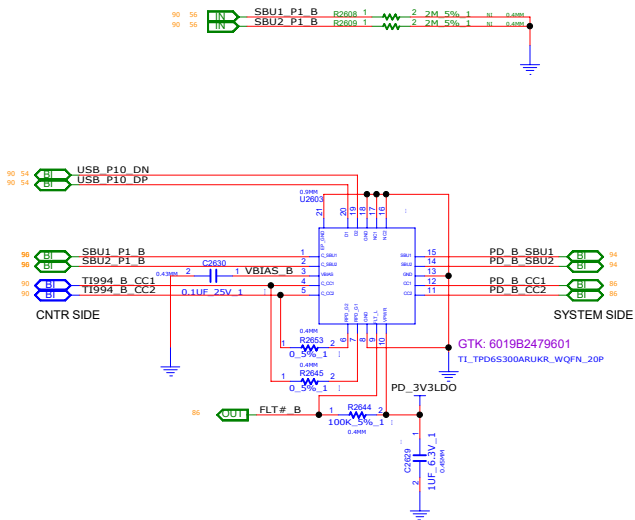
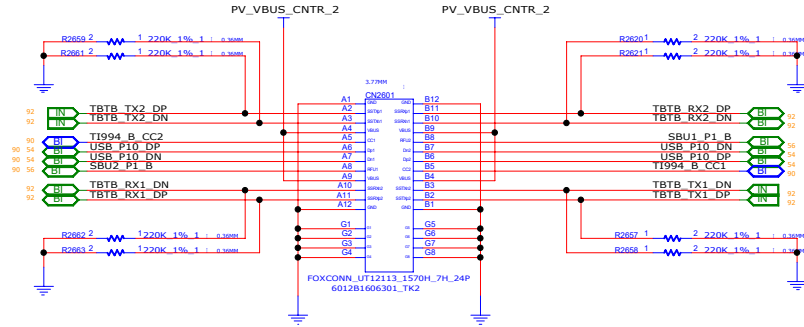
**INVENTEC**

TITLE  
MODEL PROJECT,FUNCTION  
USB\_TYPE-C\_A-2 (RIGHT)

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET		25 of 32	

CHANGE by PCB P/N	XXX 60xxxxxxxxxxx	DATE PCB VER	21-OCT-2002 XXX
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# USB TYPE-C PORT B USB TYPE - C CONN



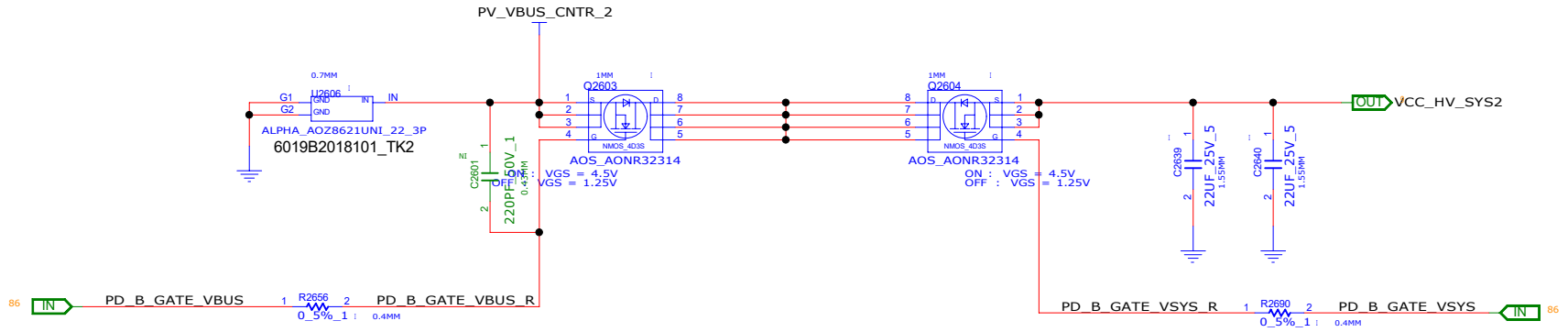
LOCATION NUMBER:2600~2699

<b>INVENTEC</b>			
TITLE MODEL,PROJECT,FUNCTION USB_TYPE-C_B-1			
CHANGE#	XXX	DATE	21-OCT-2002
PCB PIN	XXXXXXXXXXXX	PCB VER	XXX
SIZE	A3	CODE	1310XXXXXX(0-D)
SHEET	16	REV	X01
		DOC NUMBER	
		REV	

CHANGE#	XXX	DATE	21-OCT-2002
PCB PIN	XXXXXXXXXXXX	PCB VER	XXX

# HIGH VOLTAGE PATH

# USB TYPE-C PORT B



LOCATION NUMBER:2600~2699

**INVENTEC**

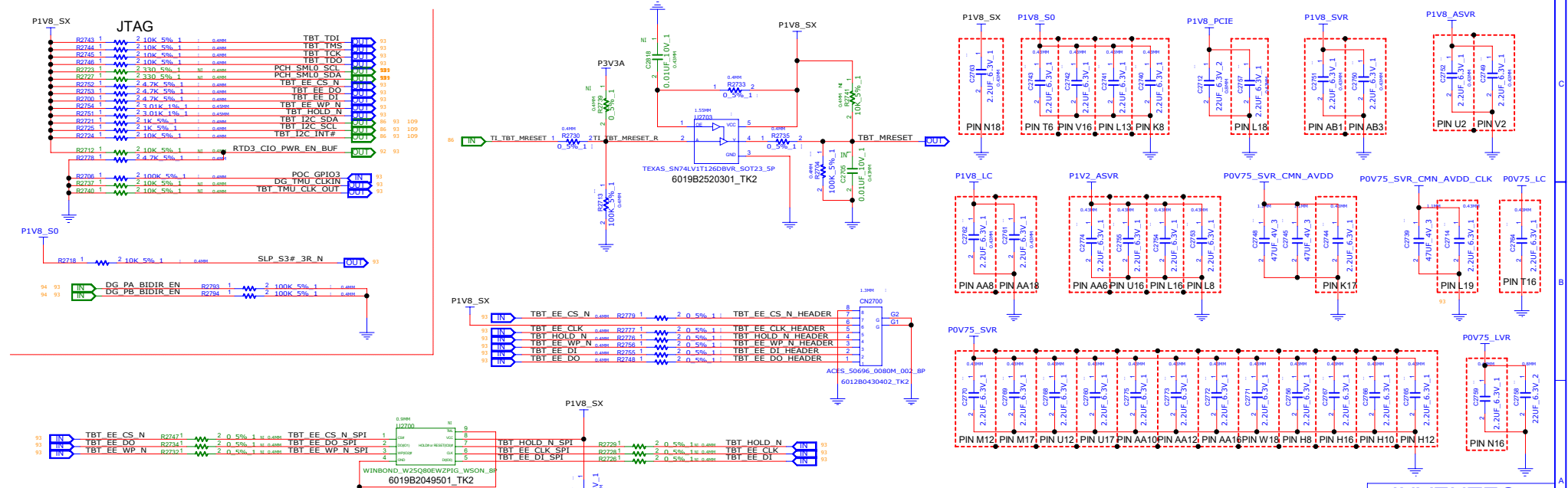
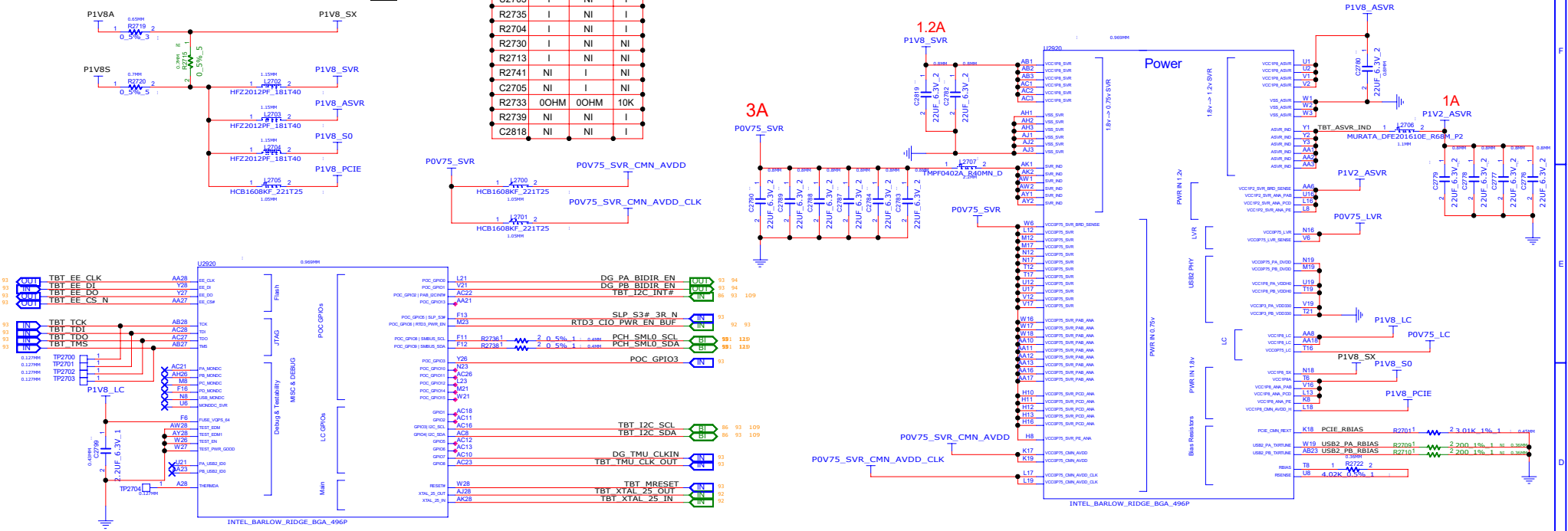
TITLE			
MODEL PROJECT, FUNCTION			
USB TYPE-C B-2 (RIGHT)			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET		27 of 32	

CHANGE BY	XXX	DATE	21-OCT-2002
PCB P/N	60xxxxxxxxxxx	PCB VER	XXX



# BARLOW RIDGE\_2

	MABA	BOBCAT	IEC
U2703	I	NI	I
R2735	I	NI	I
R2704	I	NI	I
R2730	I	NI	NI
R2713	I	NI	NI
R2741	NI	I	NI
C2705	NI	I	10K
R2733	DOHM	DOHM	10K
R2739	NI	NI	I
C2818	NI	NI	I

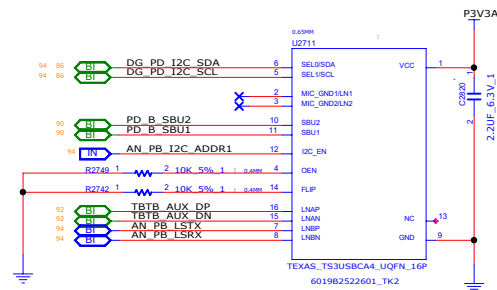
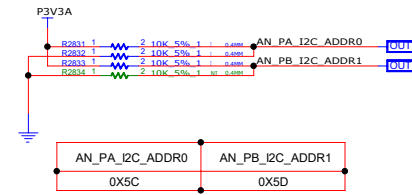
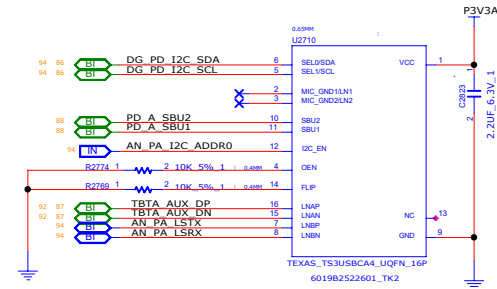
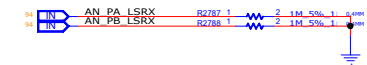
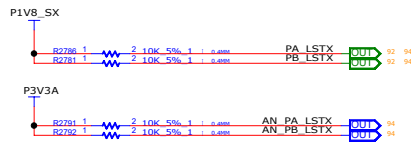
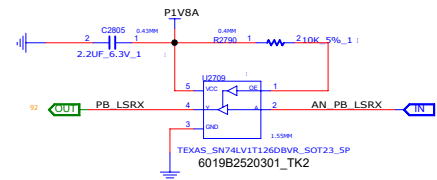
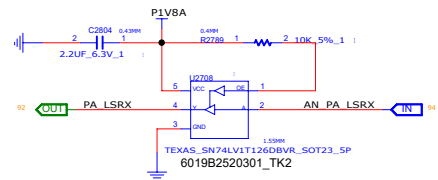
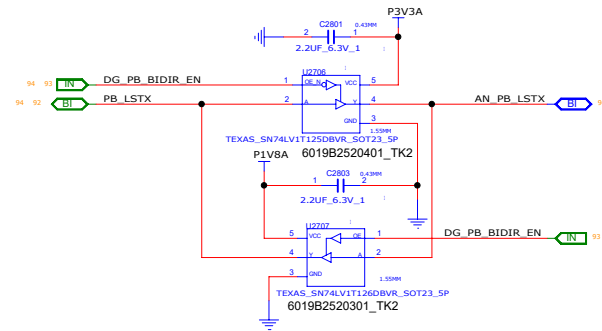
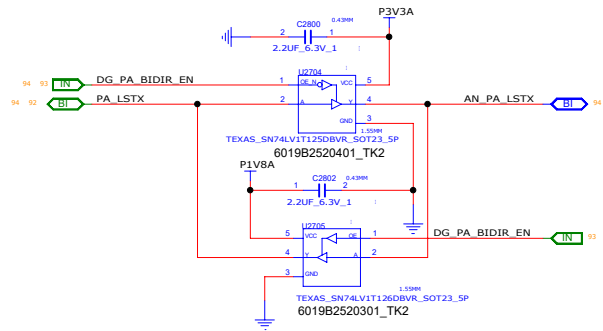


LOCATION NUMBER:2700~2949

<b>INVENTEC</b>			
TITLE	MODEL/PROJECT_FUNCTION		
SHEET	29	OF	32
DATE	21-OCT-2002	DATE	21-OCT-2002
REV	X01	DOC NUMBER	1310XXXXX-D-0
CHANGE	XXX	DATE	21-OCT-2002
P/CB	60XXXXXXX	P/CB	VER XXX



# BARLOW RIDGE\_3



LOCATION NUMBER:2700~2949

<b>INVENTEC</b>			
MODEL,PROJECT,FUNCTION			
BARLOW RIDGE_3			
CHANGE#	DATE	DOC NUMBER	REV
A3	31_OCT-2002	1310XXXXX-D-0	X01
SHEET 30 of 32			

CHANGE#	DATE	DOC NUMBER	REV
A3	31_OCT-2002	1310XXXXX-D-0	X01
SHEET 30 of 32			

# BARLOW RIDGE\_4

