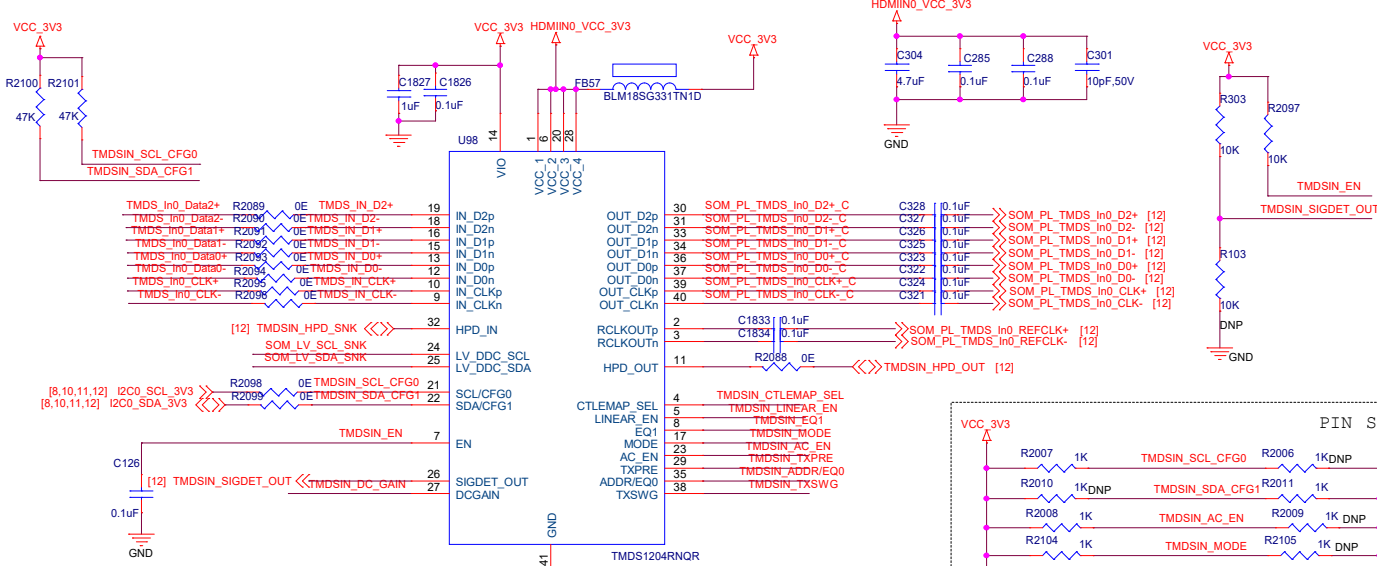
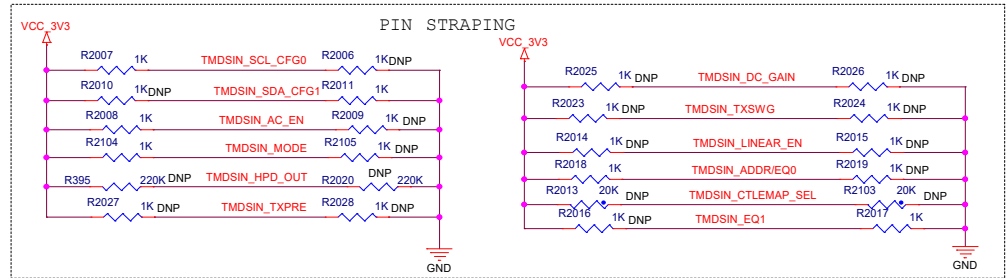


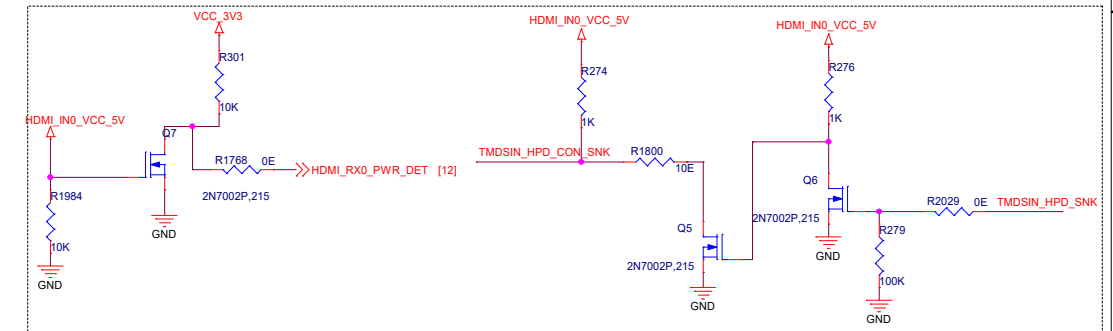
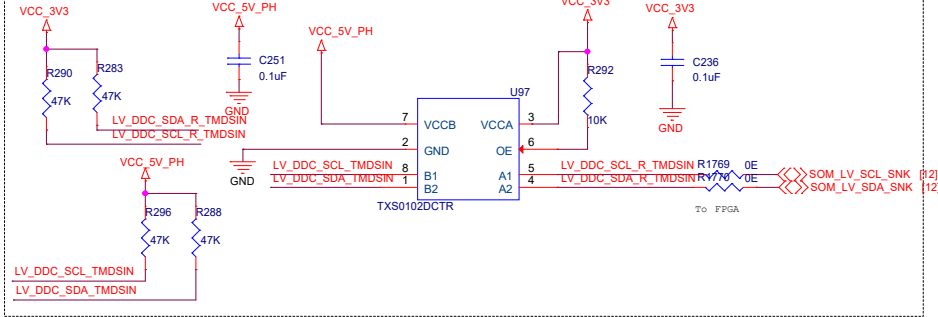
HDMI INPUT0 INTERFACE



PIN STRAPPING SETTINGS	
PIN NAME	Description
SCL_CFG0	SCL_CFG0 Pin = Low(0), in Pin-Strape mode when it is Sink operation, operating as Normal HDMI in both sink and source operations.
SDA_CFG1	Pin SDA_CFG1 as Low (0) in pin-strap mode, when it is Sink operation. Pin SDA_CFG1 as High (1) in pin-strap mode, when it is Source operation.
AC_EN	AC_EN pin = High(1), in Pin-Strape mode when it is Sink operation. AC_EN pin = Low(0), in Pin-Strape mode when it is Source operation.
MODE	Mode = High (1), in Pin-Strape mode when it is Sink operation. Mode = Low (0), in Pin-Strape mode when it is Source operation.
TXPRE	TXPRE pin = Default 0dB of pre-emphasis. Float TXPRE pin in sink mode & Float TXPRE pin in sink mode
DC_GAIN	*0*: -3dB, *R*: 0dB & *I*: +1dB in sink and source applications
TXSWG	TXSWG Pin = Default TX swing level. Float TXSWG pin in both Sink and source applications
LINEAR_EN	Linear redriver (LINEAR_EN pin = "F") recommended in sink application Limited redriver (LINEAR_EN pin = "0") for Source applications
ADDR/EQ0	ADDR/EQ0 pin: "1" for sink applications ADDR/EQ0 pin: "R" for source applications
CTLEMAP_SEL	For Sink application recommend Map B or C
EQ1	EQ1 pin: "0" for Sink applications EQ1 pin: "R" for Source applications



HDMI INPUT0 CONTROL SIGNAL



HDMI INPUT0 CONNECTOR

