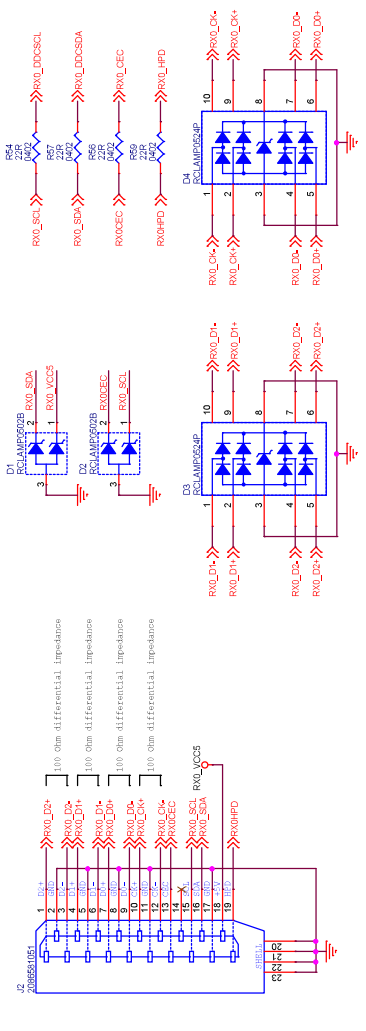
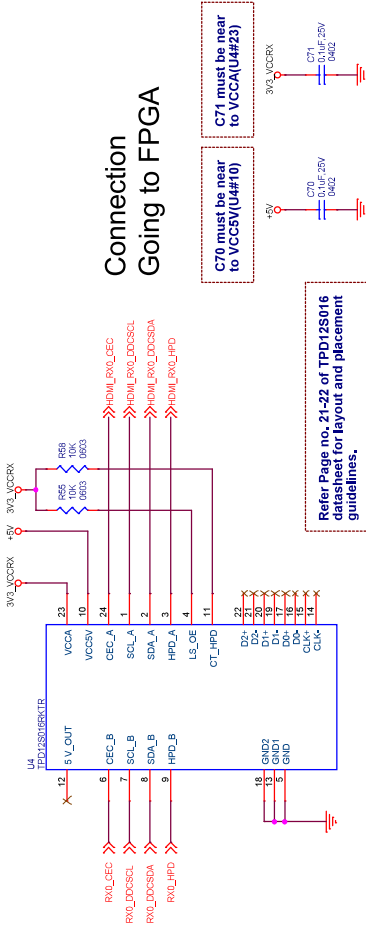
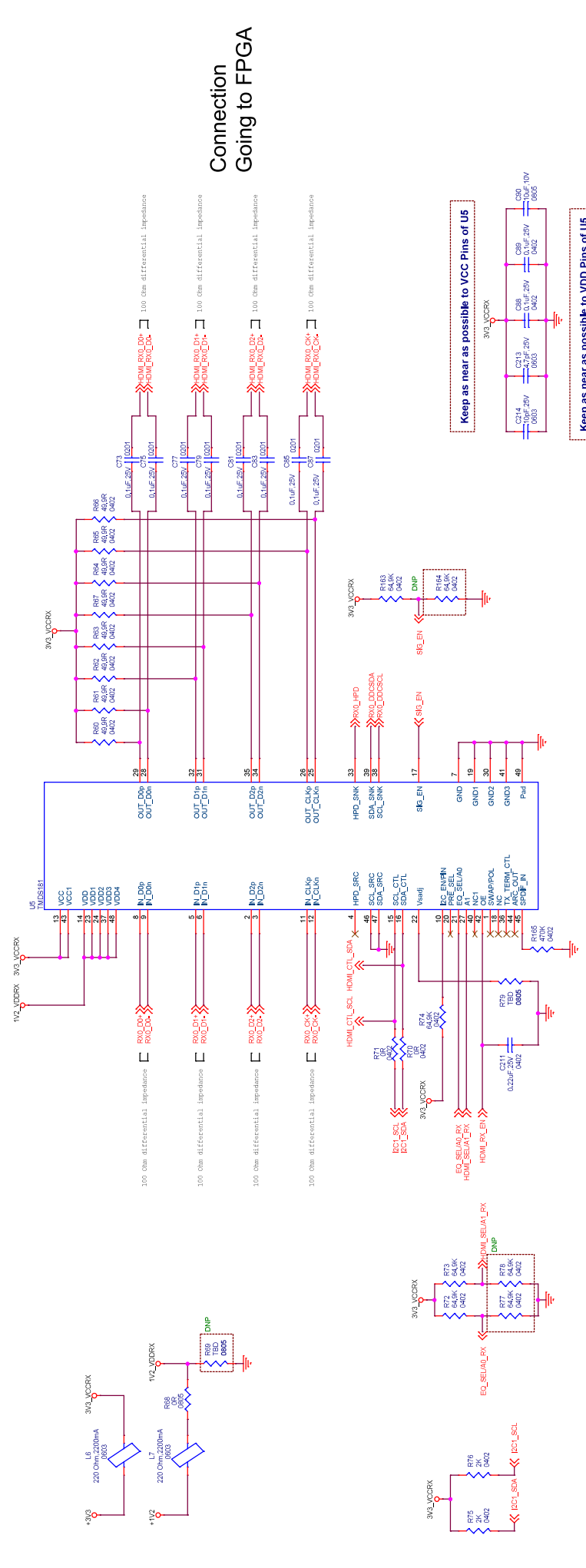


HDMI Input

TPD12S016 Level Shifter IC for I2C, CEC & HPD (5V to 3.3V)



TMD5181 6 Gbps TMD5 Retimer

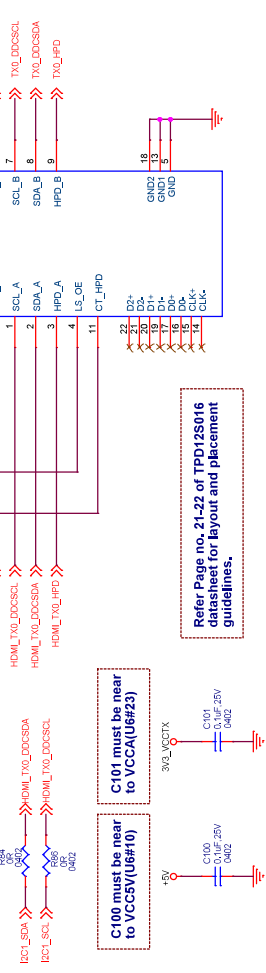
Figure 31. TMD5181 I²C Device Address Description

A1/A0	7 (MSB)	6	5	4	3	2	1	0 (WR)	HEX
00	1	0	1	1	1	1	0	0/1	BCBD
01	1	0	1	1	1	0	1	0/1	BABE
10	1	0	1	1	1	1	0	0/1	B9E9
11	1	0	1	1	1	0	1	0/1	B9E7

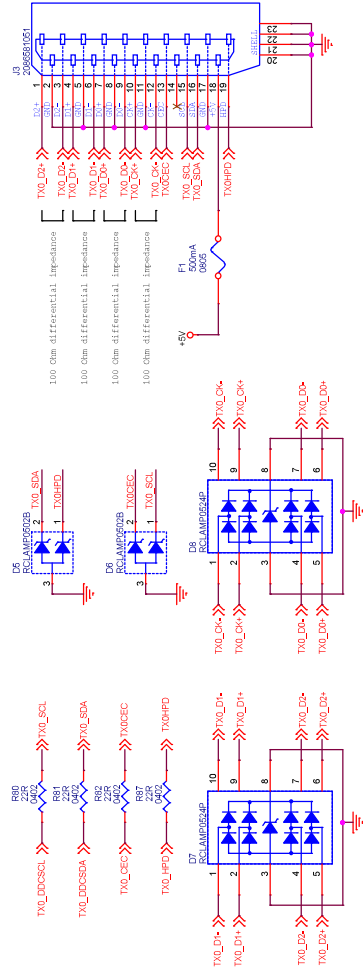
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TPD12S016 Level Shifter IC for I2C , CEC & HPD (3.3V to 5V)

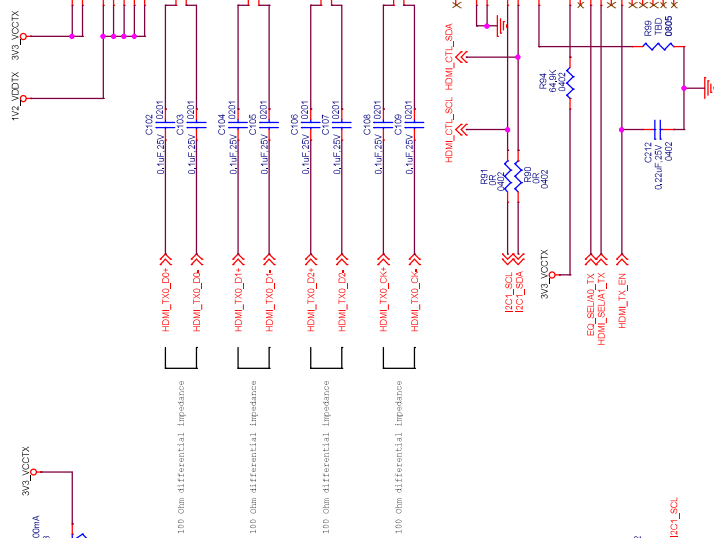
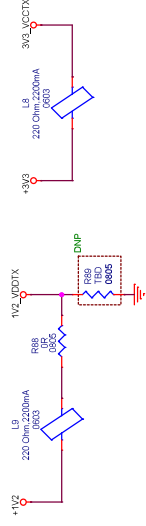
Connection Coming from FPGA



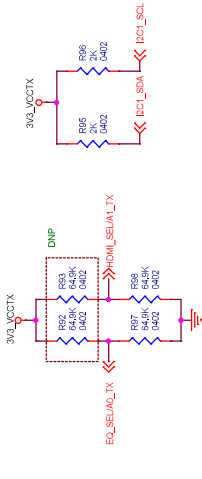
HDMI Output



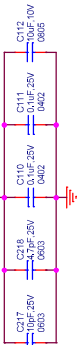
SN65DP159 6-Gbps AC-Coupled TMDs™ to HDMI™ Level Shifter Retimer IC



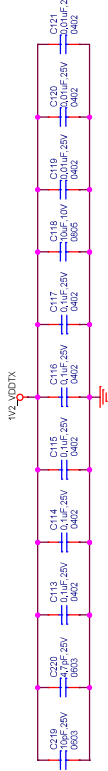
Connection Coming from FPGA



Keep as near as possible to VCC Pins of U7



Keep as near as possible to VDD Pins of U7



i2C Device Address Description

SN65DP159 i2C Device Address		ADD						
A1/A0	7 (MSB)	6	5	4	3	2	1	0 (LSB)
00	1	0	1	1	1	0	0	0/1
01	1	0	1	1	1	0	1	0/1
10	1	0	1	1	1	0	0	0/1
11	1	0	1	1	0	1	1	0/1

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