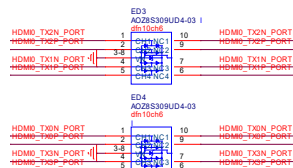


U1000S



U1000T

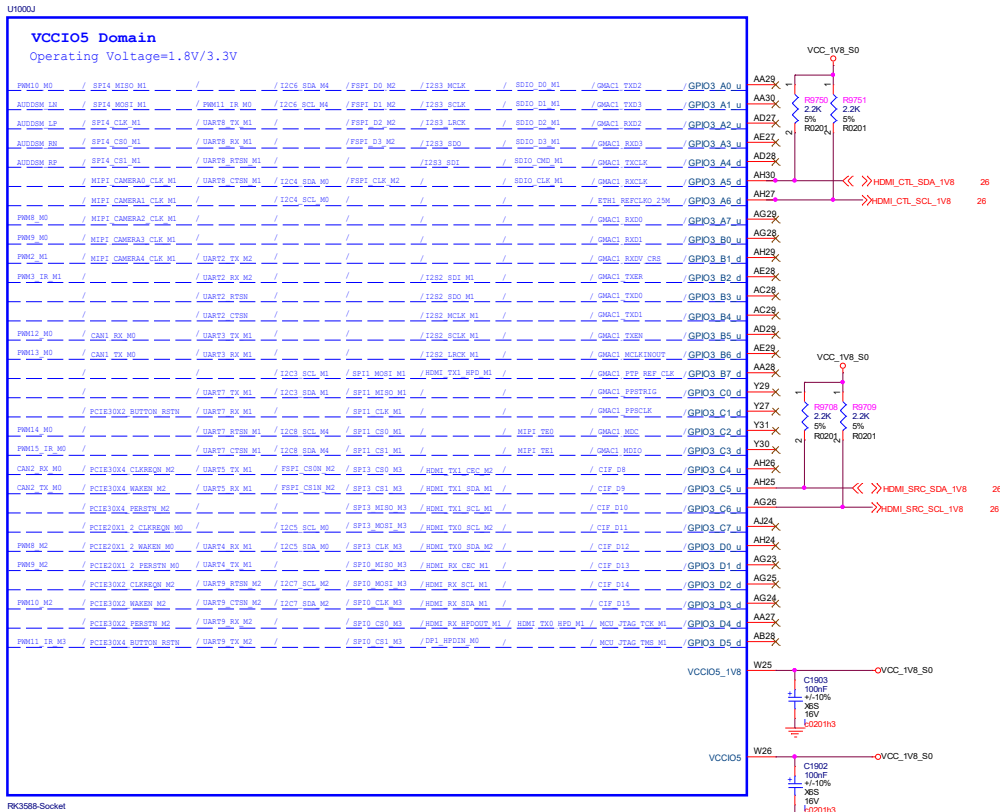


Pin	Signal Assignment	Pin	Signal Assignment
1	TMDS Data 2+	2	TMDS Data 2+ shield
3	TMDS Data 2-1	4	TMDS Data 2 shield
5	TMDS Data 2-1 shield	6	TMDS Data 1-
7	TMDS Data 0+	8	TMDS Data 0 shield
9	TMDS Data 0-	10	TMDS Clock+
11	TMDS Clock shield	12	TMDS Clock-
13	CEC	14	Utility/ HEAC+
15	SCL	16	SDA
17	DDC/CEC/HEAC GND	18	+5V Power
19	HPD/ HEAC-		

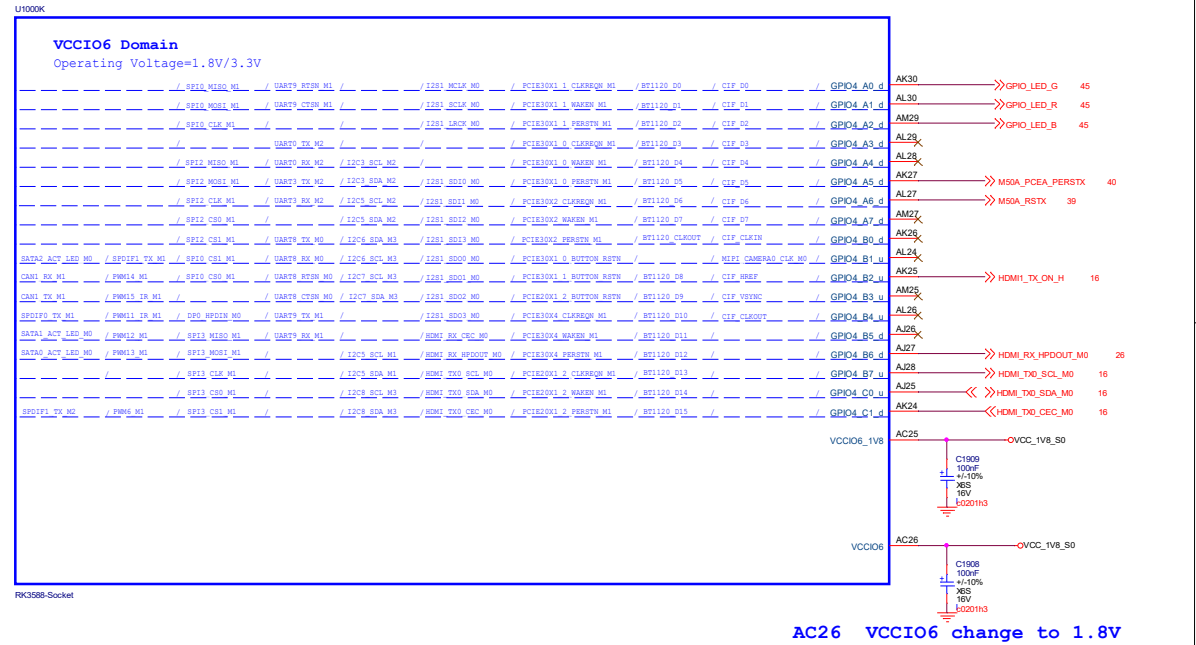


Title		RK3588_HDMI/eDP Interface		Rev	V100
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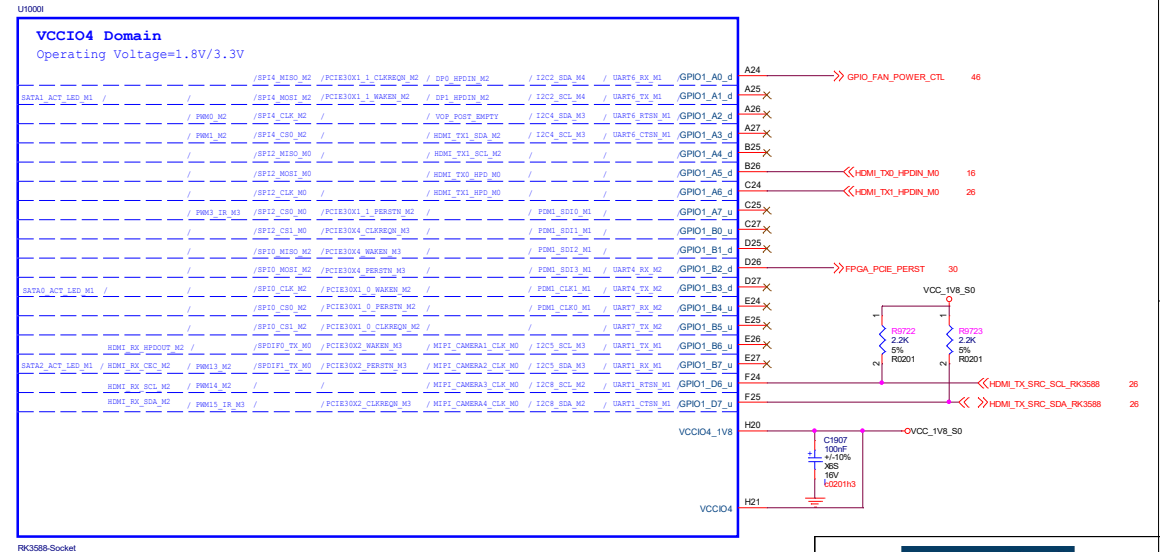
RK3588_J (VCCIO5 Domain)

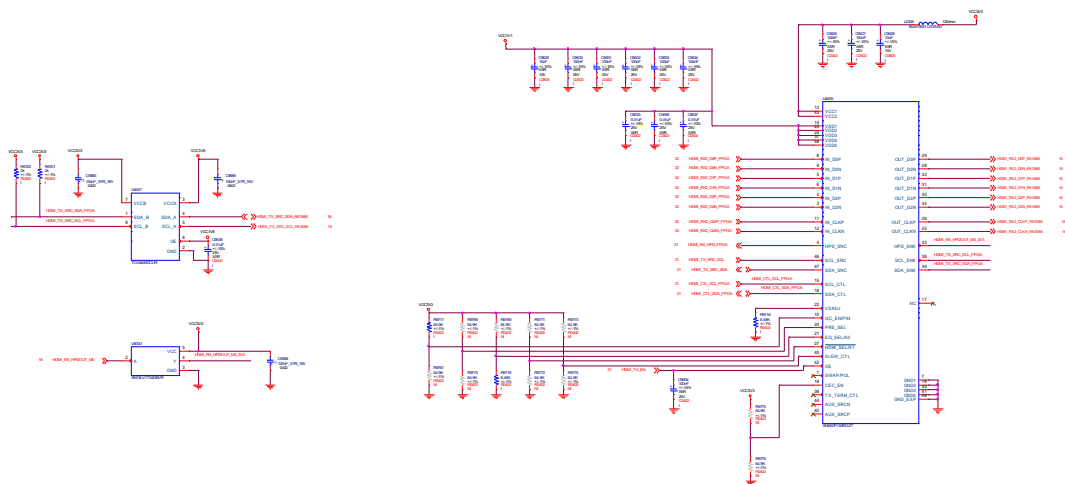
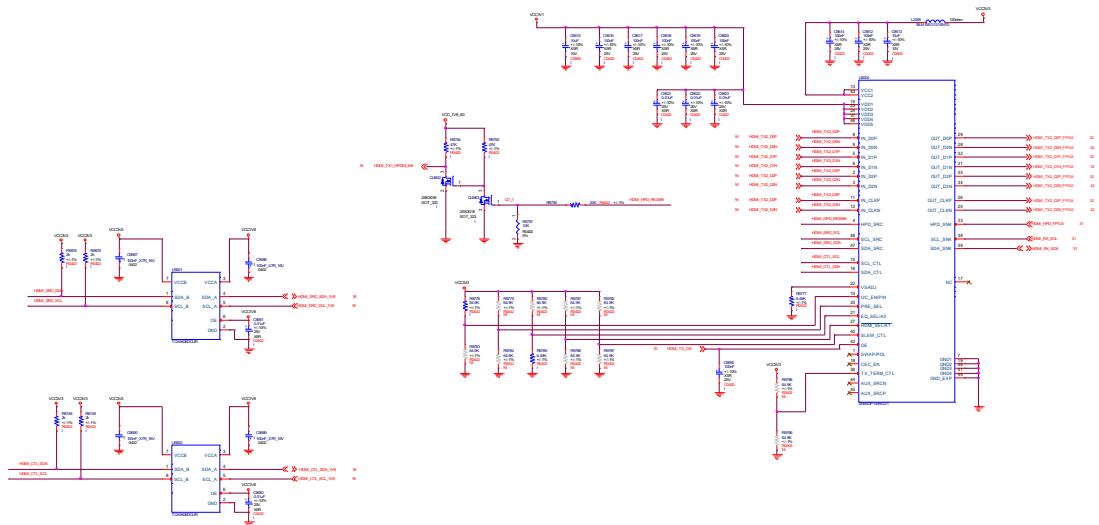


RK3588_K (VCCIO6 Domain)

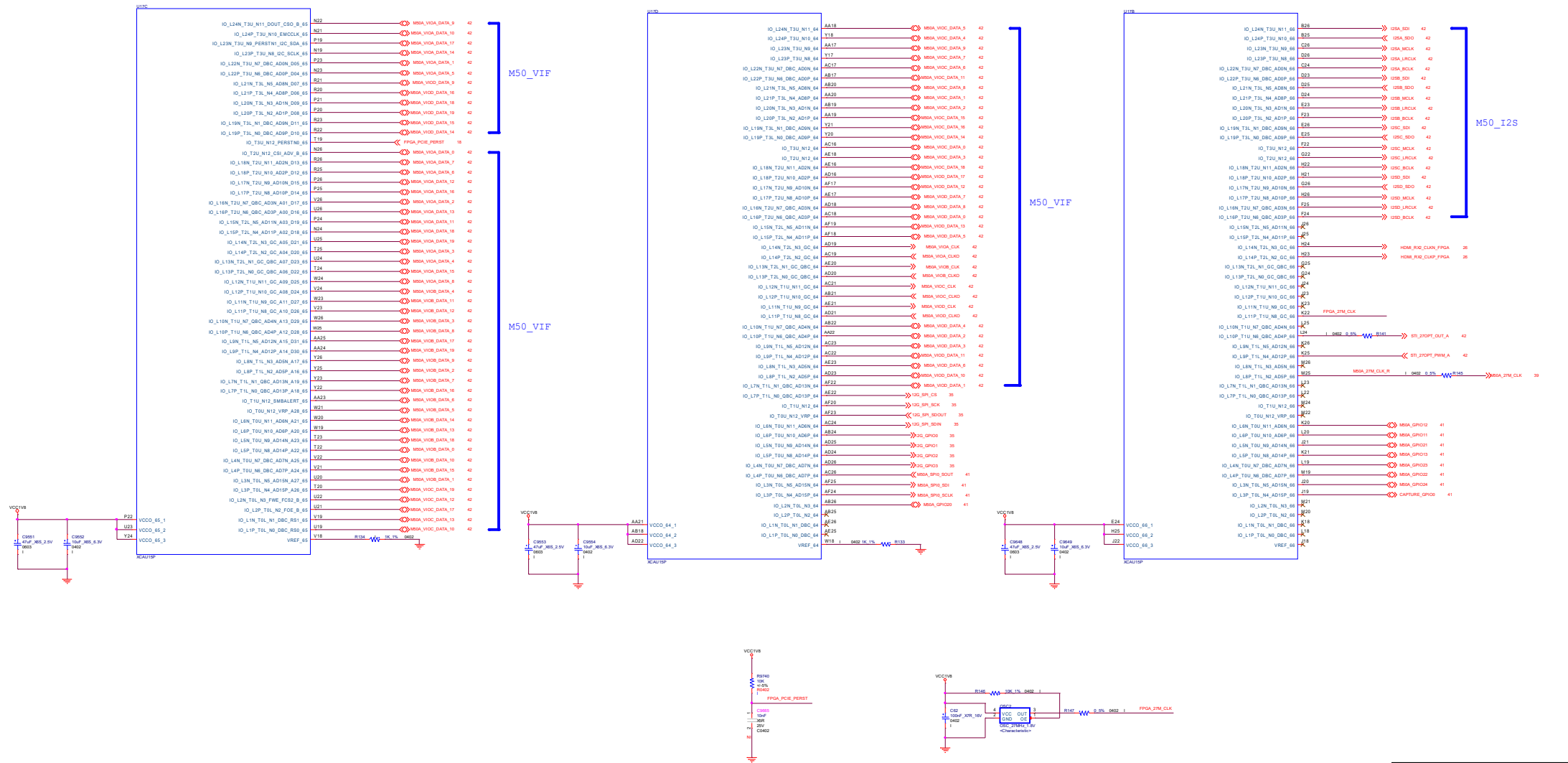


RK3588_I (VCCIO4 Domain)

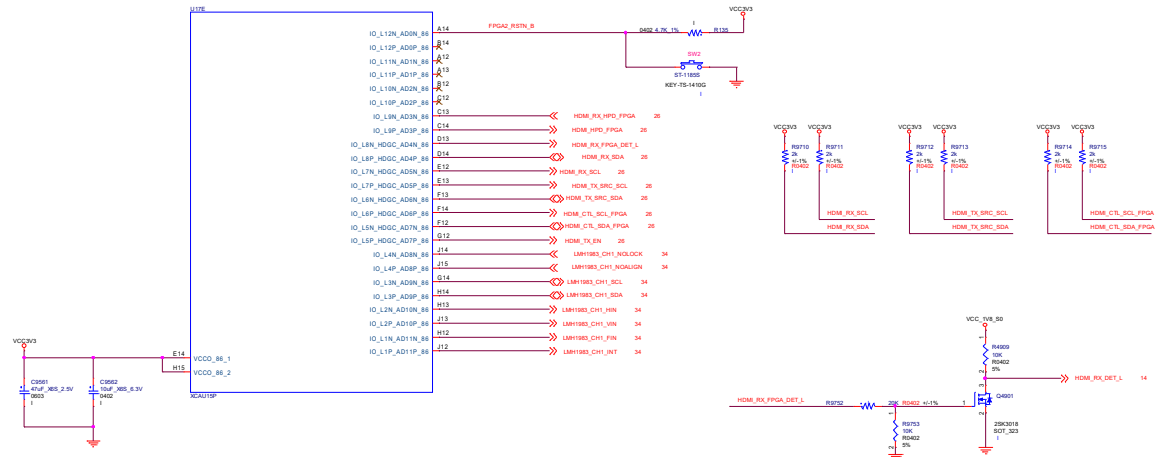
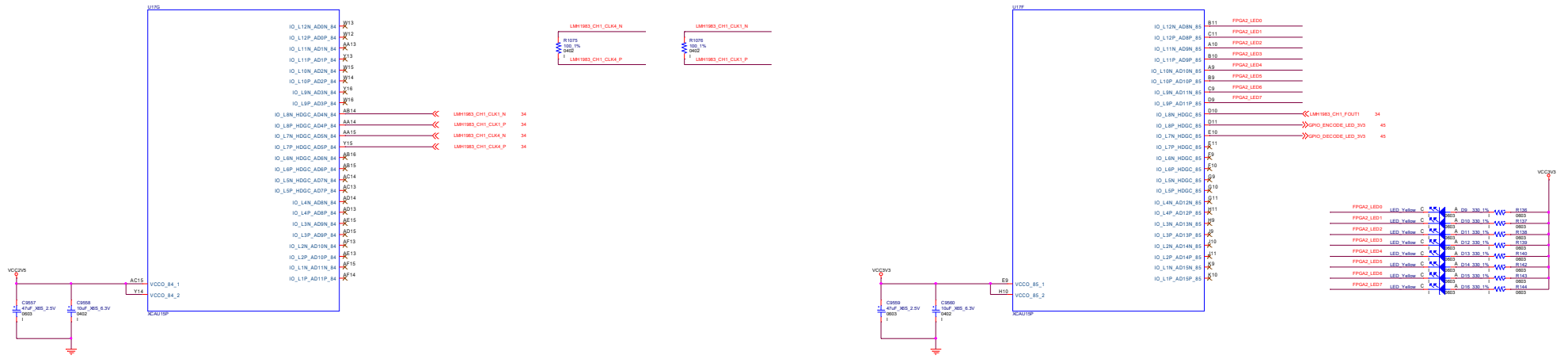




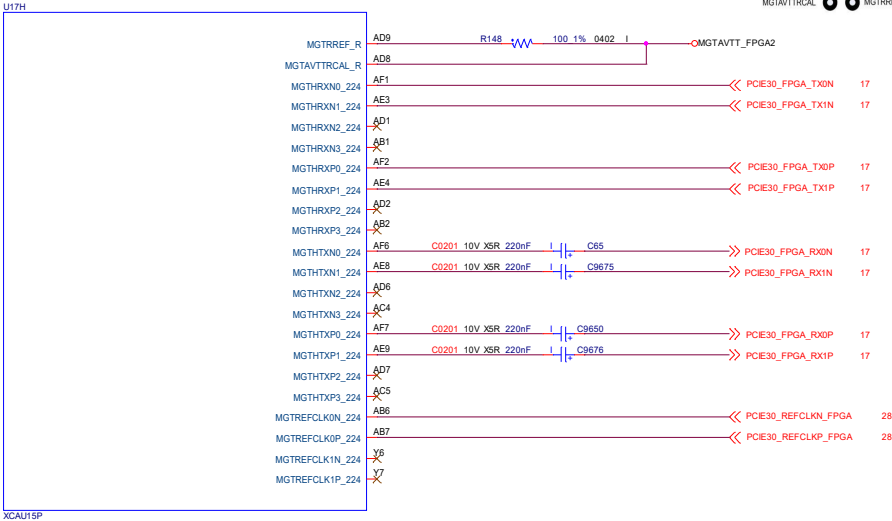
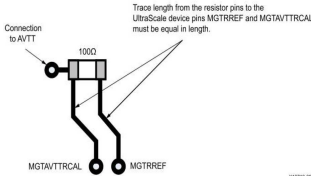
HP BANKS (1V~1.8V)



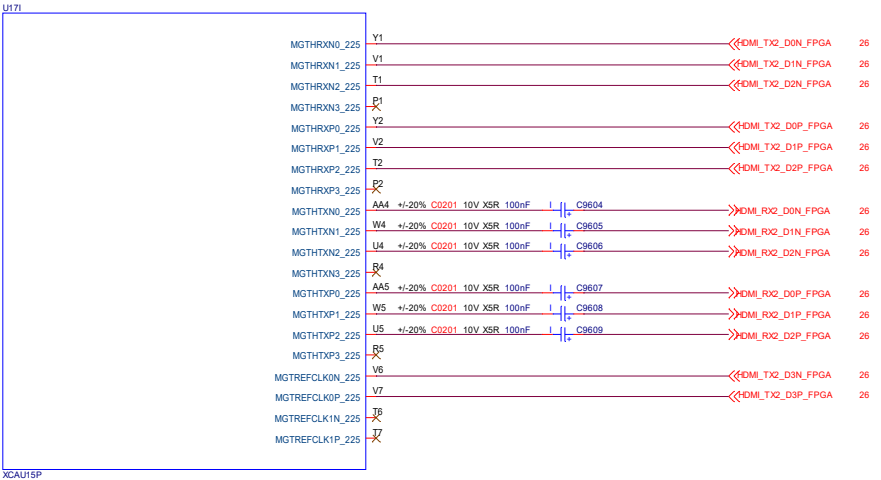
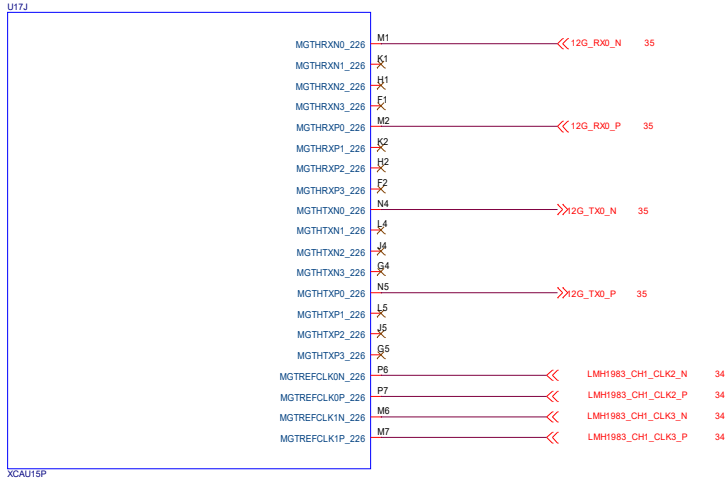
HD BANKS (1V~3.3V)



GTY Transceivers



To RK3588



To RK3588 HDMI