

Pairs 6 and 2 are swapped
For HMM purpose!



25.02.2016

[illegible]

U67A

DC0 IHP N C1 0.11F 16V DC0 CAP IHP N 9 IN_D0N OUT_D0N 28 DC0 IHPF REGT N 2 13 DC0 IHPF REGT P 3 4 HIDMIO OUT_D0 N HIDMIO OUT_D0 P

DC0 IHP P C2 0.11F 16V DC0 CAP IHP P 8 IN_D0P OUT_D0P 29 DC0 IHPF REGT N 3 4 HIDMIO OUT_D0 N HIDMIO OUT_D0 P

DC1 IHP N C3 0.11F 16V DC1 CAP IHP N 6 IN_D1N OUT_D1N 31 DC1 IHPF REGT N 3 4 HIDMIO OUT_D1 N HIDMIO OUT_D1 P

DC1 IHP P C4 0.11F 16V DC1 CAP IHP P 5 IN_D1P OUT_D1P 32 DC1 IHPF REGT P 3 4 HIDMIO OUT_D1 N HIDMIO OUT_D1 P

DC2 IHP N C5 0.11F 16V DC2 CAP IHP N 3 IN_D2N OUT_D2N 34 DC2 IHPF REGT N 2 20 D1W218S90H02L 10 3 HIDMIO OUT_D2 N HIDMIO OUT_D2 P

DC2 IHP P C6 0.11F 16V DC2 CAP IHP P 2 IN_D2P OUT_D2P 35 DC2 IHPF REGT P 3 4 D1W218S90H02L 10 3 HIDMIO OUT_D2 N HIDMIO OUT_D2 P

DC3 IHP N C7 0.11F 16V DC3 CAP IHP N 12 IN_CLKN OUT_CLKN 25 DC3 IHPF REGT N 3 4 D1W218S90H02L 10 3 HIDMIO OUT_D3 N HIDMIO OUT_D3 P

DC3 IHP P C8 0.11F 16V DC3 CAP IHP P 11 IN_CLKP OUT_CLKP 26 DC3 IHPF REGT P 3 4 D1W218S90H02L 10 3 HIDMIO OUT_D3 N HIDMIO OUT_D3 P

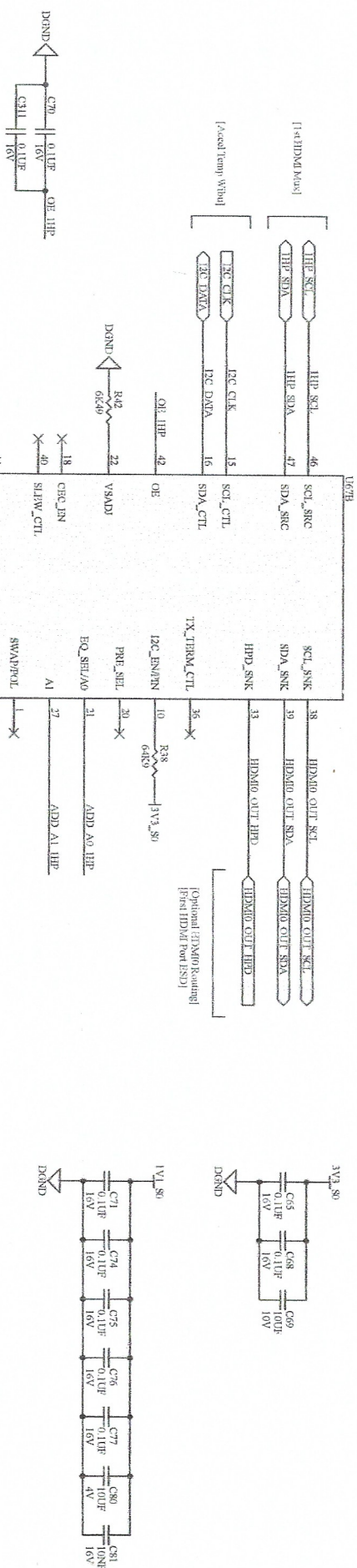
[1 uF 10V] IHP IHPD IHP IHPD 4 HPD_SRC VDD_14 14 VDD_23 23 VDD_24 24 VDD_37 37 VDD_48 48 VDD_19 19 GND_19 30 GND_30 41 GND_41 49 PAD VCC_13 13 VCC_43 43 3V3_30 30

[1 uF 10V] IHP IHPD IHP IHPD 4 HPD_SRC VDD_14 14 VDD_23 23 VDD_24 24 VDD_37 37 VDD_48 48 VDD_19 19 GND_19 30 GND_30 41 GND_41 49 PAD VCC_13 13 VCC_43 43 3V3_30 30

ADD A0 IHP ADD A1 IHP

DCND K30 64K

[Optional I12400 Testing] [Pin I12400 Test 150]



COM Express Base Board - A6400-1000					
Project					
Title	First HDMI Retimer				
Size:	A3	Number:		Revision:	V1.2R1
Date:	19.10.2027	Time:	23.27.01	Sheet of:	
File:				Author:	
				Designer:	
				Checker:	
				Reviewer:	
				Approver:	