

Layer	Stack up	Type	Description	Base Thickness	Processed Thickness	Impedance ID
		Soldermask	LPI Soldermask			
1		Foil	1/2oz Copper Foil	0.600	1.800	1, 2, 3
		Pre-Preg	2113	3.900	3.720	
		Pre-Preg	2113	3.900	3.720	
2				1.200	1.200	
3		Core	008 1/1	8.000	8.000	
				1.200	1.200	4, 5, 6
		Pre-Preg	2113	3.900	3.154	
		Pre-Preg	1080	3.000	2.426	
4				1.200	1.200	
5		Core	008 1/1	8.000	8.000	
				1.200	1.200	
		Pre-Preg	2113	3.900	3.154	
		Pre-Preg	1080	3.000	2.426	
6				1.200	1.200	7, 8
7		Core	008 1/1	8.000	8.000	
				1.200	1.200	
		Pre-Preg	2113	3.900	3.493	
		Pre-Preg	1080	3.000	2.687	
8				1.200	1.200	
9		Core	008 1/1	8.000	8.000	
				1.200	1.200	
		Pre-Preg	2113	3.900	3.720	
		Pre-Preg	2113	3.900	3.720	
10		Foil	1/2oz Copper Foil	0.600	1.800	9
		Soldermask	LPI Soldermask			

83 + 8.3 / -8.3

Copper Thickness = 13.200 | Dielectric Thickness = 64.220 | Overall Processed Thickness = 77.420 |

Structure Image	Impedance ID	Structure Name	Impedance Signal Layer	Ref. Plane 1 in Layer	Ref. Plane 2 in Layer	Target Impedance	Calculated Impedance	Tol (+/- %)	Lower Trace Width	Trace Separation
	1	Coated Microstrip 1B	1	2	0	61.000	61.260	10.000	8.000	0.000
	2	Edge Coupled Coated Microstrip 1B	1	2	0	90.000	92.390	10.000	8.000	6.000

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Structure Image	Impedance ID	Structure Name	Impedance Signal Layer	Ref. Plane 1 in Layer	Ref. Plane 2 in Layer	Target Impedance	Calculated Impedance	Tol (+/- %)	Lower Trace Width	Trace Separation
	3	Edge Coupled Coated Microstrip 1B	1	2	0	100.000	101.670	10.000	6.000	6.000
	4	Edge Coupled Offset Stripline 1B1A	3	2	4	100.000	98.140	10.000	5.000	15.000
	5	Offset Stripline 1B1A	3	2	4	41.000	39.850	10.000	8.000	0.000
	6	Edge Coupled Offset Stripline 1B1A	3	2	4	90.000	90.520	10.000	6.000	15.000
	7	Offset Stripline 1B1A	6	5	7	41.000	39.850	10.000	8.000	0.000
	8	Edge Coupled Offset Stripline 1B1A	6	5	7	100.000	98.140	10.000	5.000	15.000
	9	Coated Microstrip 1B	10	9	0	50.000	50.840	10.000	12.000	0.000

Drill Image	1st Layer	2nd Layer	Drill Type
	1	10	Mechanical PTH

Notes

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