FPGA
(10AX115N2F
40I2SGE2)
POWER OFF

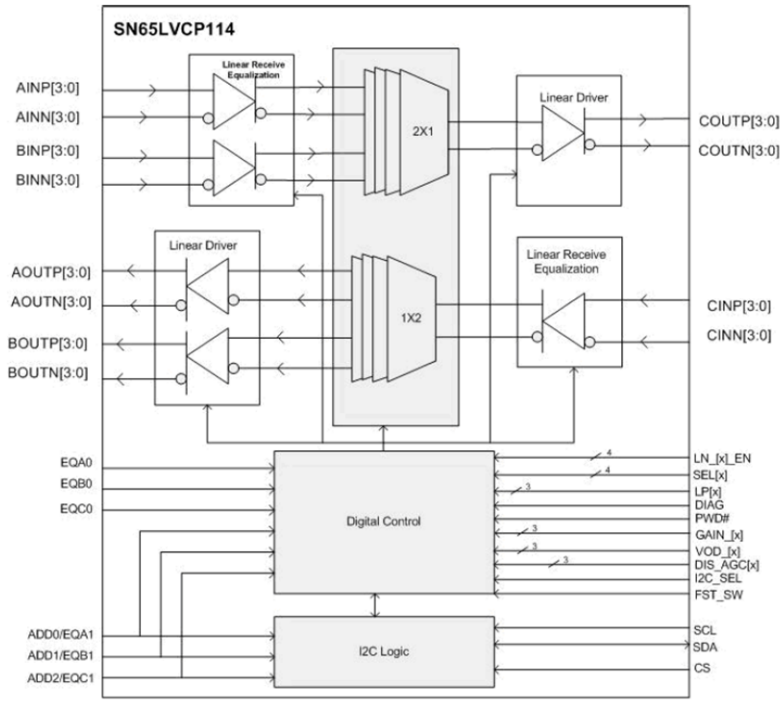
2
SFI

SFI
2

2
SFI

SFI 2

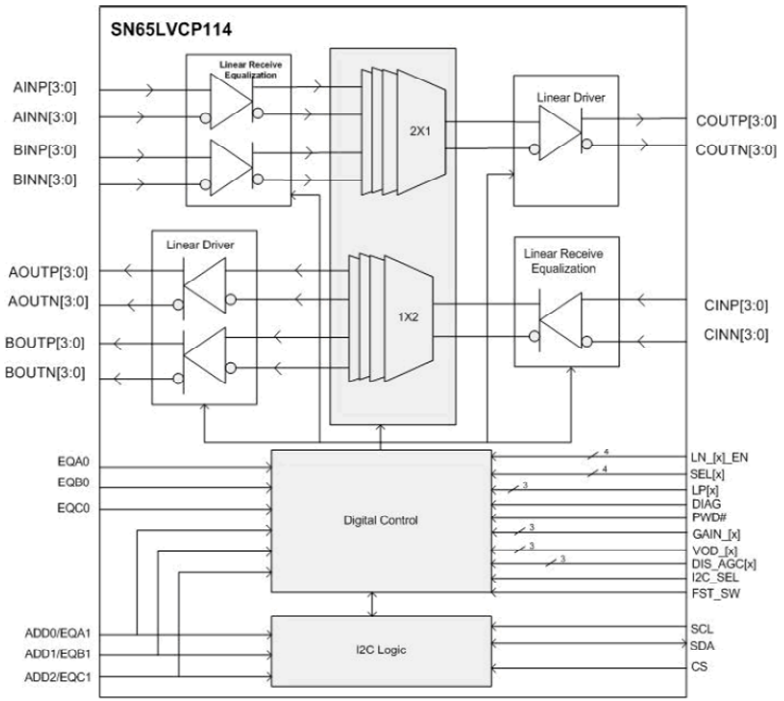
description of the SN65LVCP114 pin functions is provided in .



2
SFI
2

SFP+

description of the SN65LVCP114 pin functions is provided in .



2
SFI
2

SFP+