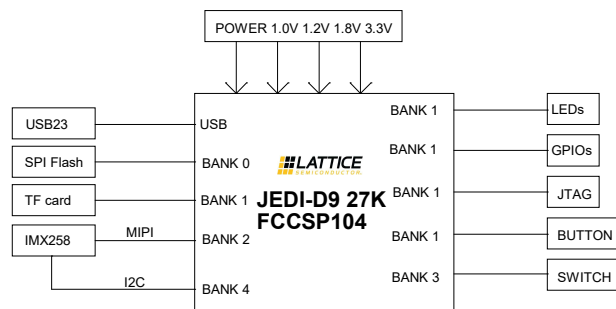


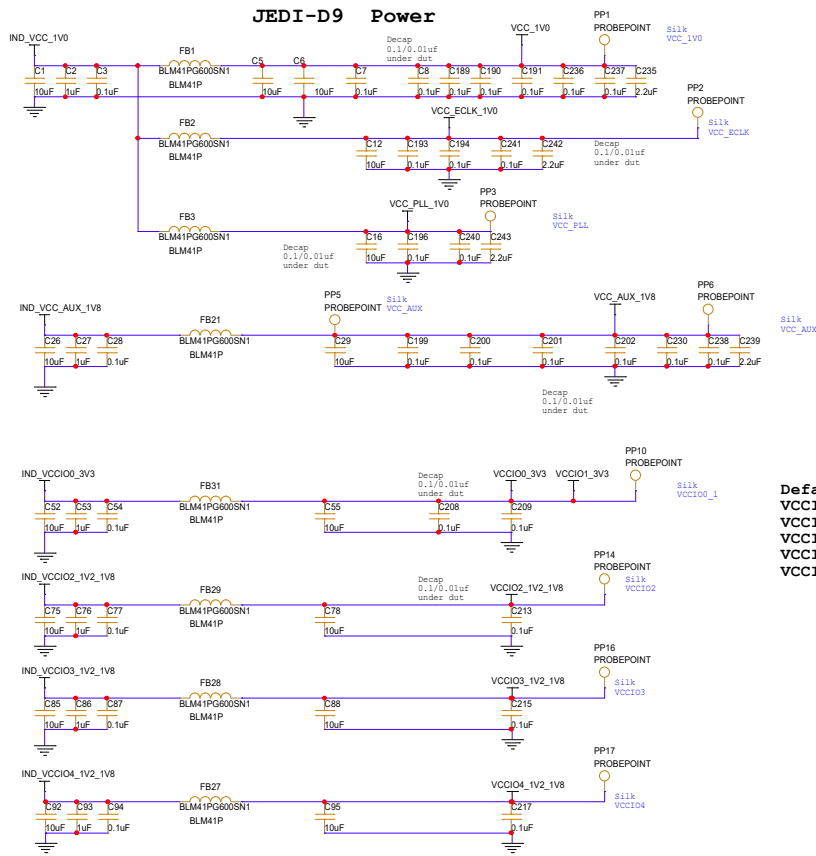
Jedi D9 System Board

Page	Title
1	Cover Page
2	Block Diagram
3	Power
4	Power Module
5	Config&LED&REF
6	JEDI-D9-Clock-Flash
7	USB23_SDIO
8	CMOS SENSOR
9	Redriver

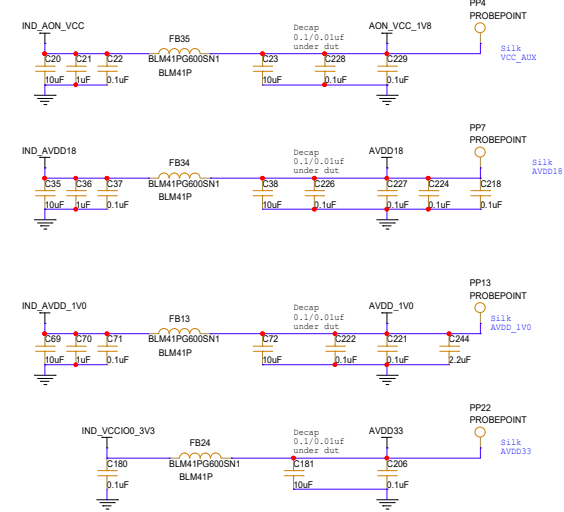


Power Generator

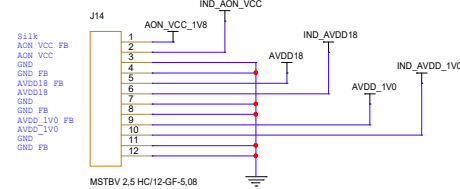
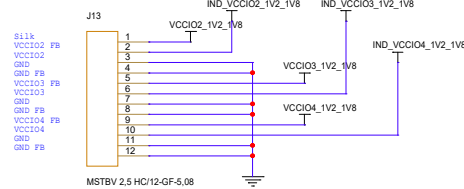
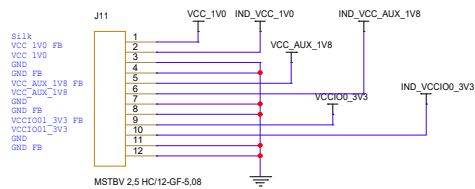
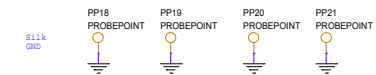
JEDI-D9 Power



USB Function Power



The Power PROBE POINT PLACE NEAR DUT



Power Source Connector & FeedBack sense

LATTICE Semiconductor Corporation	
Title: Jedi D9 System Board	
Size: C	Document Number: Power Generator
Date: Thursday, June 04, 2026	Rev: A
Sheet: 3 of 10	

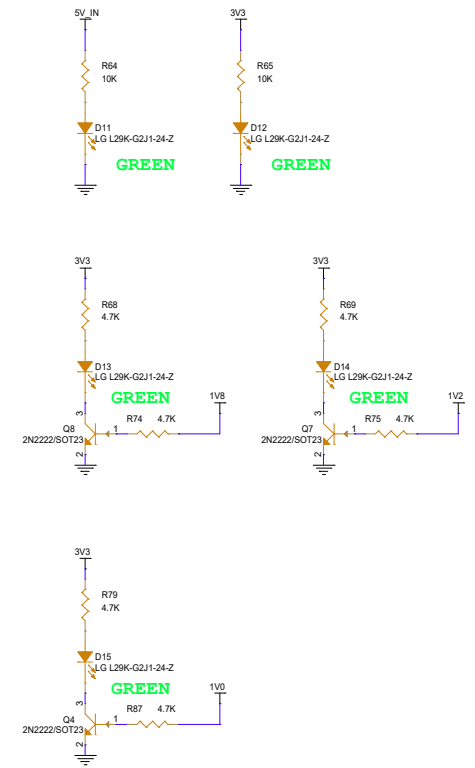
The image displays three circuit diagrams for the Power Jumper module, each showing a different power rail configuration. The diagrams are labeled with their respective power ratings: 3A, 0.8A, and 0.8A.

3A Configuration: This diagram shows the 3A power rail configuration. It includes a 5V IN input, a nanoSMDC200F converter, and a 3A output. The output is connected to a 3A power rail, which is then connected to the board components. The output voltage is 3V, and the output current is 3A.

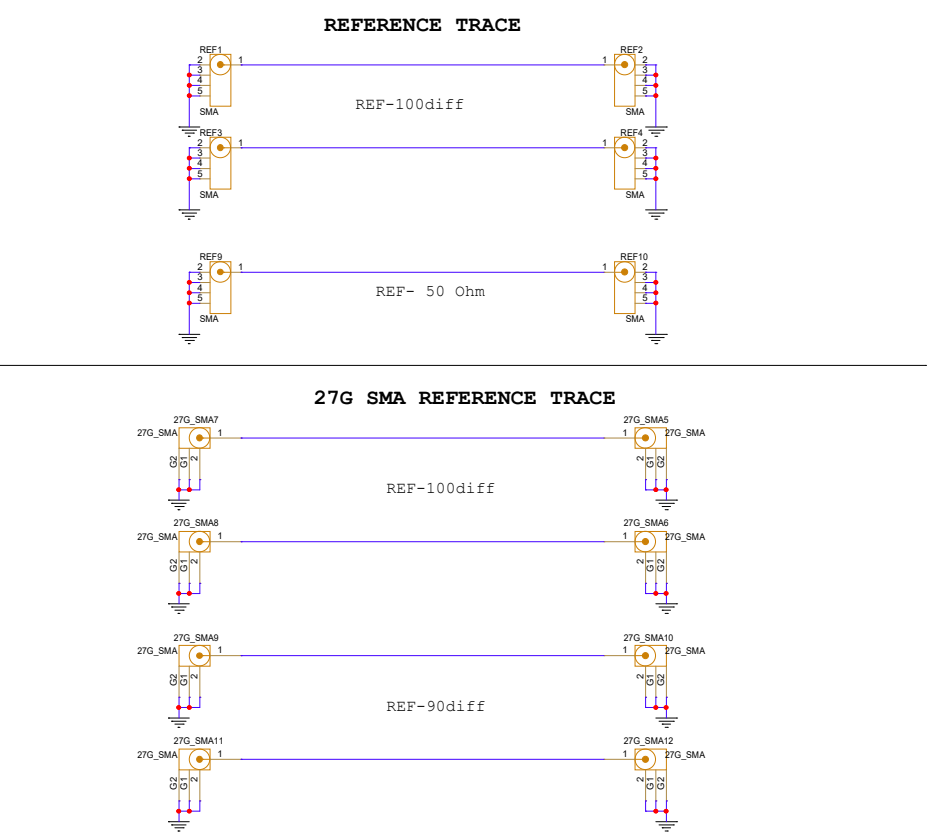
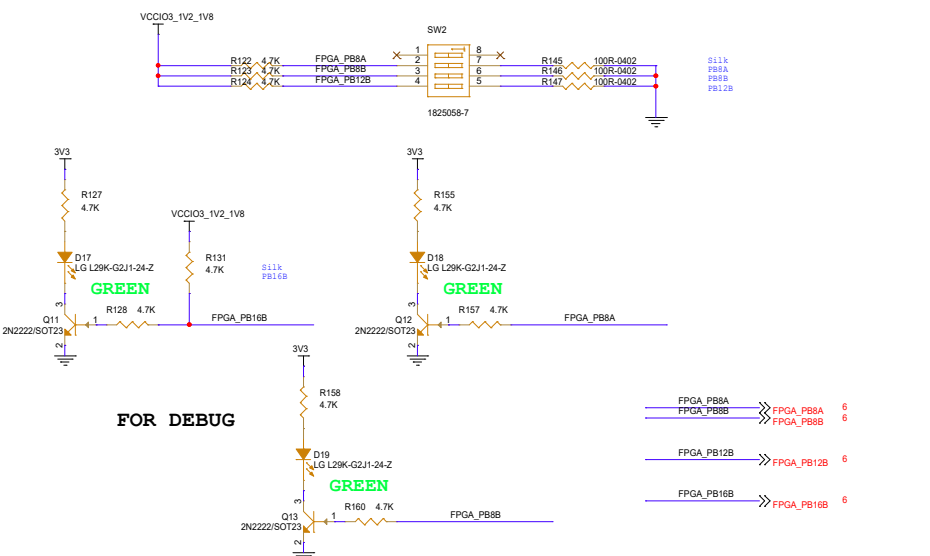
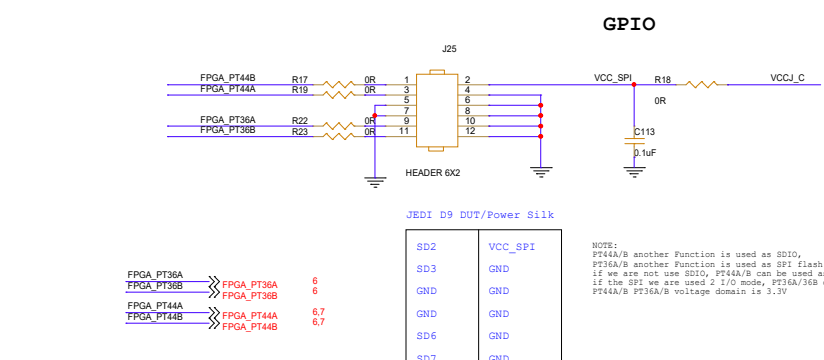
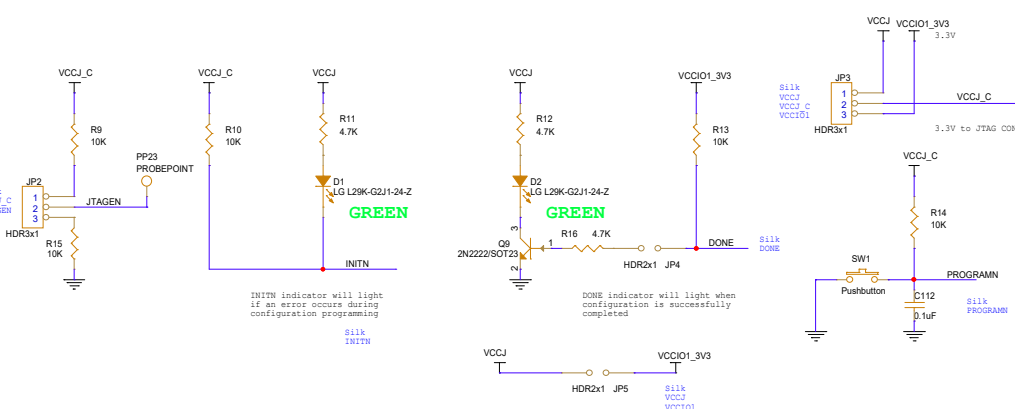
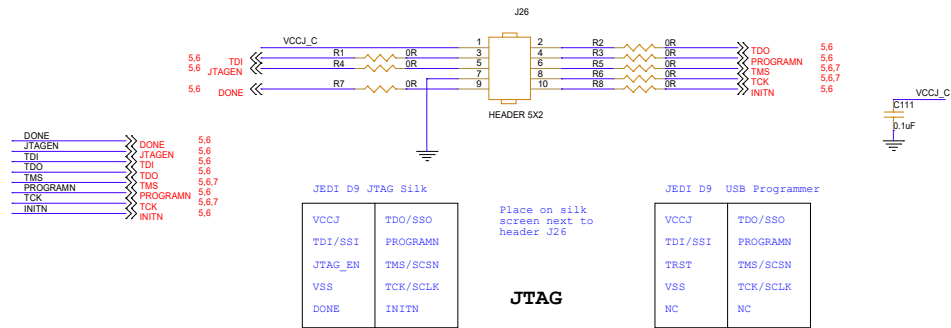
0.8A Configuration (Top): This diagram shows the 0.8A power rail configuration. It includes a 5V IN input, a nanoSMDC200F converter, and a 0.8A output. The output is connected to a 0.8A power rail, which is then connected to the board components. The output voltage is 0.8V, and the output current is 0.8A.

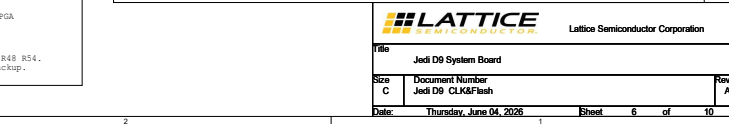
0.8A Configuration (Bottom): This diagram shows the 0.8A power rail configuration. It includes a 5V IN input, a nanoSMDC200F converter, and a 0.8A output. The output is connected to a 0.8A power rail, which is then connected to the board components. The output voltage is 0.8V, and the output current is 0.8A.

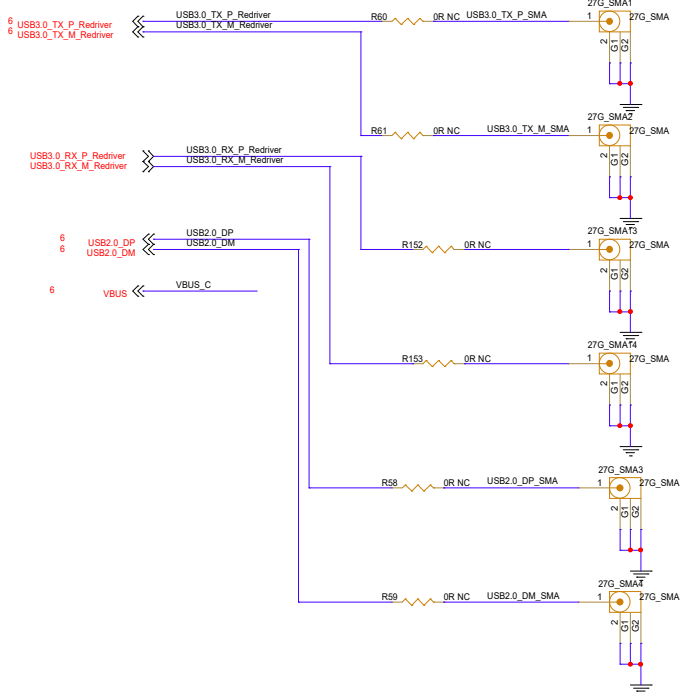
POWER JUMPER: The text "POWER JUMPER" is written in large, bold, black letters. Below it, the text "Short the Jumper, the Chips power is generate by on board DC/DC & LDO module." is written in a smaller font.



Config



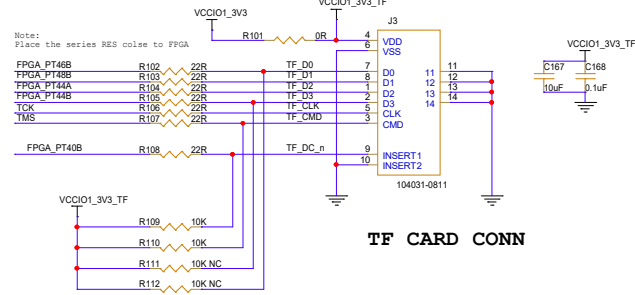
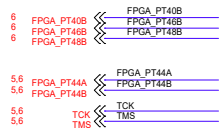
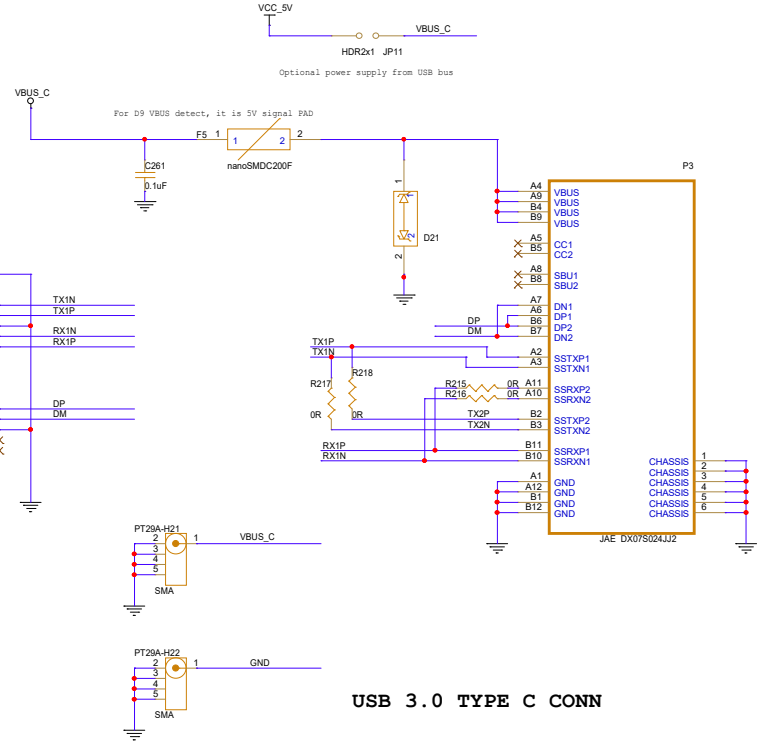
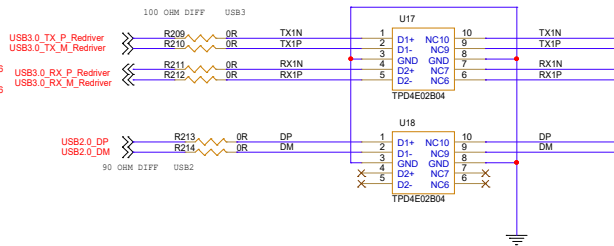




SMA CONN is for D9 PAD signal test

JEDI D9 USB23

NOTE:
1. Match length intra-pair within +/- 5mil,
2. USB3.0 Differential trace impedance should be 100 Ohm (+/-15%), USB2.0 Differential trace impedance should be 90 Ohm (+/-15%),
3. Route the trace on the Top or Bottom Layer, and with less 2 vias.
4. other traces to USB trace space should >25mil

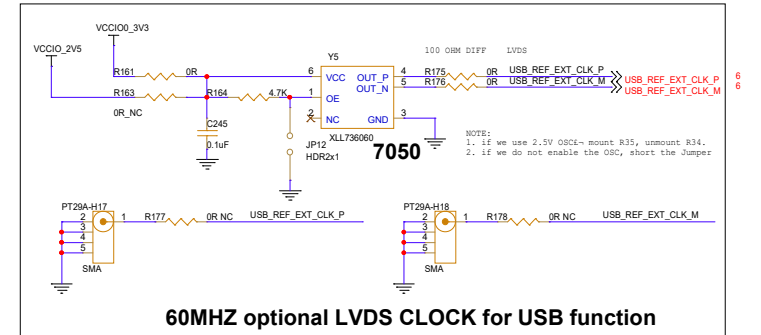


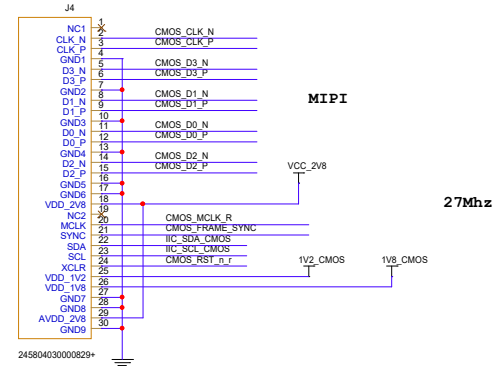
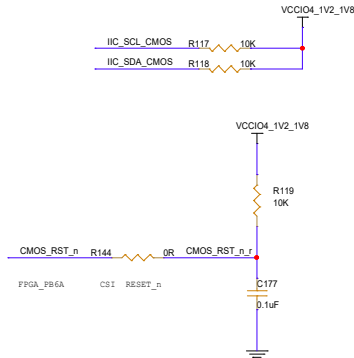
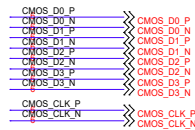
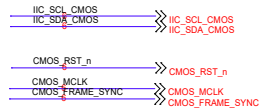
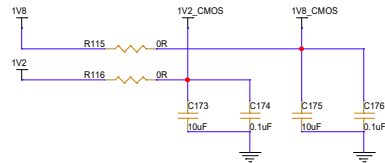
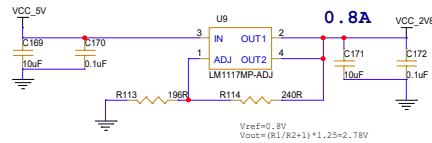
TF CARD CONN

JEDI D9 GPIO work as SDIO/SPI

NOTE:
1. Match length within Bus reference to TF_CLK +/- 100mil,
2. trace impedance should be 50 Ohm (+/-15%),

NO	D9 PAD	SD MODE	SPI MODE
1	PT44B	DATA0	CS
2	PT44B	DATA1	CS
3	PT40A	CMD	MOSI
4	N/A	VDD	VDD
5	PT45A	CLK	CLK
6	N/A	VSS	VSS
7	PT45B	DATA0	MISO
8	PT45B	DATA1	Nc





Sony IMX258 connector

NOTE:
1. Match MIPI signals length intra-pair within +/- 10mil; inter-pair within +/- 50mil tolerance
2. Differential impedance should be 100 Ohms and 50 Ohms as a single ended signal
3. All the power rails should be capable of carrying 1A current

Sony IMX258 IIC Slave Address:

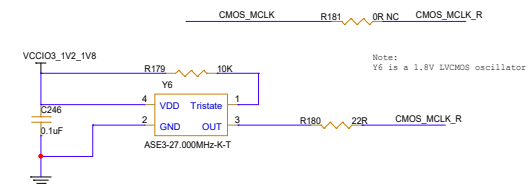
Write: 0011 0100

Read: 0011 0101

VCCIO2 ==1.2V

VCCIO3 ==1.8V

VCCIO4 ==1.8V



27MHz Clock for CMOS Sensor IMX258

