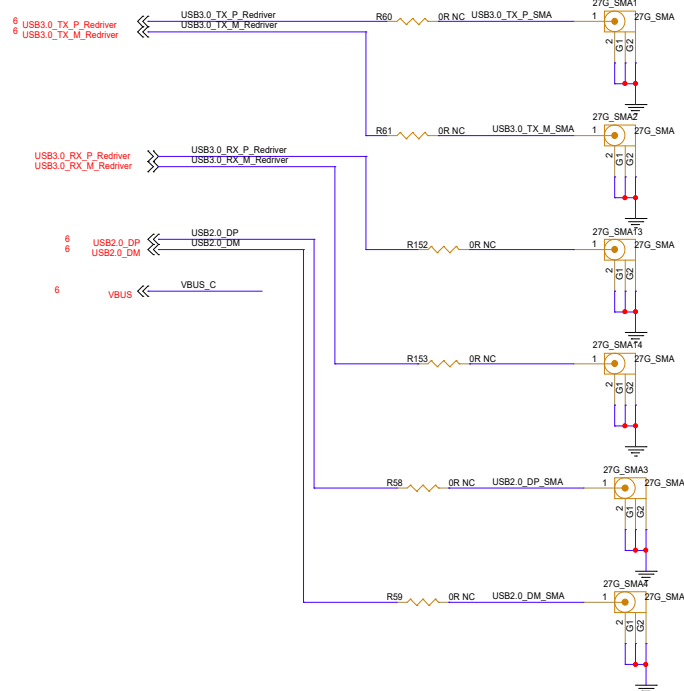
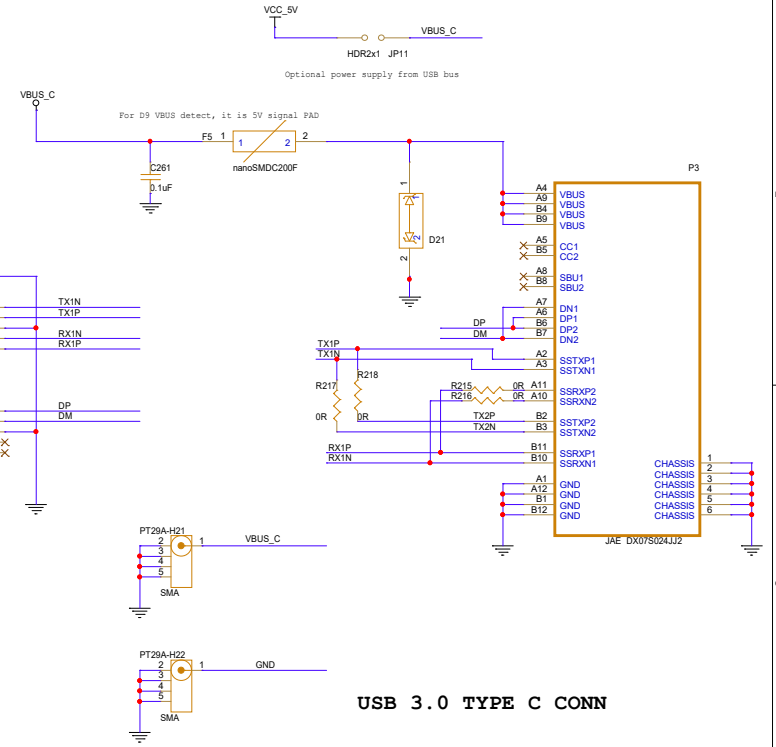
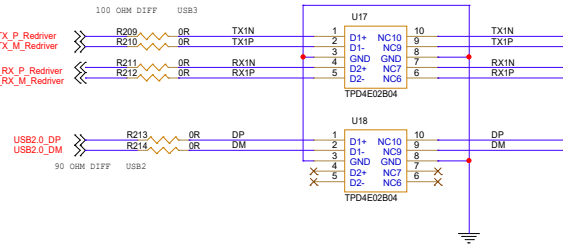




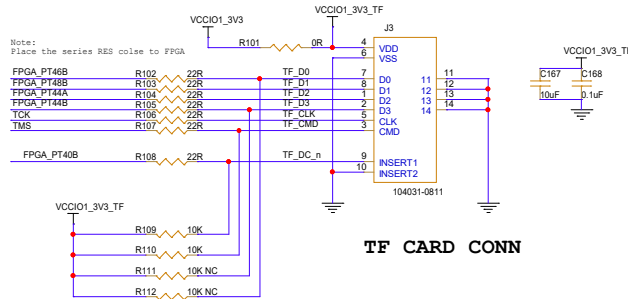
JEDI D9 USB23

NOTE:
1. Match length intra-pair within +/- 5mil.
2. USB3.0 Differential trace impedance should be 100 Ohm (+/-15%), USB2.0 Differential trace impedance should be 90 Ohm (+/-15%).
3. Route the trace on the Top or Bottom Layer, and with less 2 vias.
4. other traces to USB trace space should >25mil



JEDI D9 GPIO work as SDIO/SPI

NOTE:
1. Match length within Bus reference to TF CLK +/- 100mil.
2. trace impedance should be 50 Ohm (+/-15%),



NO	D9 PAD	SD MODE	SPI MODE
1	PT44A	DAT2	NC
2	PT44B	DAT3	CS
3	PT40A	CMD	MOSI
4	N/A	VDD	VDD
5	PT46A	CLK	CLK
6	N/A	VSS	VSS
7	PT46B	DAT0	MISO
8	PT48B	DAT1	NC

