# **LMH1297 Programming Guide** (SNAU204C)

I have something I would like to confirm in this document.

Regarding the following red frame part registers, I think that it will become the 4.3 ConfigIO Register Page 39 address 0x31 part.

Will value, Mask, and comment be the contents of P39? I'm sorry I do not understand.

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#### 3.1.1 EQ Mode for SDI\_IO+ to OUT0± and SDI\_OUT+ (All Retimed Data)

COMMAND	REGISTER	VALUE	MASK	//COMMENTS	
RAW	FF	00	07	//Select Share Register Page	
RAW	FE	02	02	//Override EQ/CD_SEL pin setting with value in Reg 0xFE[0]	
RAW	FE	00	01	//Select SDI_IO as input (EQ Mode)	
RAW	FE	20	20	//Override SDI_OUT_SEL pin setting with value in Reg 0xFE[4]	
RAW	FE	00	10	//Enable SDI_OUT as output	
RAW	FF	05	07	//Select ConfigIO Register Page	
RAW	31	40	40	//Override OUT0_SEL power-down control with value in Reg 0x31[3]	
RAW	31	00	20	//Enable OUT0 as output	
RAR	02	40	40	//Read back Energy Detect for SDI_IO	
RAW	FF	04	07	//Select CTLE/CDR Register Page	
RAW	3F	08	08	//Override OUT_CTRL pin settings with value in Reg 0x1C[3:2]	
RAW	1C	08	0C	//Enable retimed data on both SDI_OUT and OUT0	

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#### 3.15.3.2 OUT0 Power Down (EQ Mode)

In EQ Mode, OUT0 is enabled regardless of the settings applied to the OUT0\_SEL pin. Therefore, OUT0 must be powered down instead by setting the power-down override in the ConfigIO Register Page.

	COMMAND	REGISTER	VALUE	MASK	//COMMENTS	
	RAW	FF	05	07	//Select ConfigIO Register Page	
Γ	RAW	31	20	20	//Override OUT0 Power-Down control in EQ Mode	
ı	RAW	31	10	10	//Force Power-Down OUT0	

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### 4.3 ConfigIO Register Page

0x31	OUTO De- Emphasis Control	7	Reserved		RW	Reserved
		6	tx0_dem_ov		RW	OUT0 De-Emphasis Override Control 0 = De-emphasis for OUT0 determined by HOST_EQ0 pin 1 = Override De-emphasis settings for OUT0. De-emphasis settings for OUT0 are controlled by Reg 0x31[2:0]
		5	tx0_PD_ov	0x01	RW	OUTO Power-Down Override Control (EQ Mode) 0 = Disable OUTO Power-Down Override Control 1 = Enable OUTO Power-Down Override Control by value in Reg 0x31[4] Note: Share Register 0xFE[3:2] takes OUTO Power-Down priority. Only use this override when powering down OUTO in EQ Mode
		4	tx0_PD		RW	0 = Normal Operation 1 = Power-Down OUT0 if Reg 0x31[5] = 1
		3	Reserved		RW	Reserved
		2:0	tx0_dem		RW	De-emphasis settings for OUT0 if Reg 0x31[6] = 1