

A

B

C

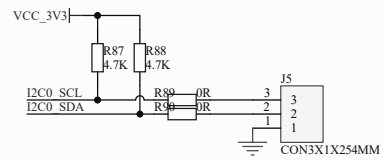
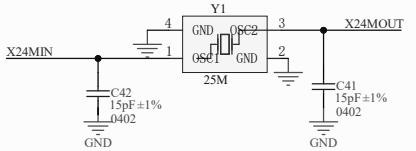
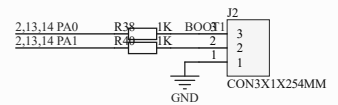
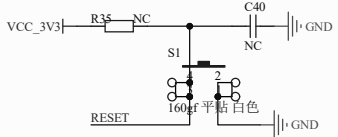
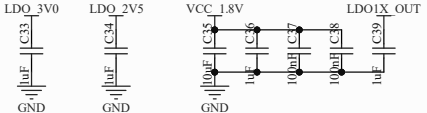
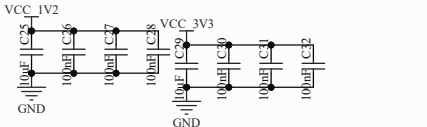
D

A

B

C

D



2.13.14 PA0  
2.13.14 PA1

OLED VDDIO EN

2.5 PB0  
2.5 PB1  
2.5 PB2  
2.5 PB3  
2.5 PB4  
2.5 PB5

I2C0 SCL  
I2C0 SDA

UART TX3  
UART RX3  
GPIO4  
GPIO3  
GPIO2  
GPIO1

MIPI0 D0-  
MIPI0 D0+  
MIPI0 D1-  
MIPI0 D1+  
MIPI0 CLK-  
MIPI0 CLK+  
MIPI0 D2-  
MIPI0 D2+  
MIPI0 D3-  
MIPI0 D3+

RESET  
X24MOUT  
X24MIN

U8

**UART0/ADC/AMIC/I2C/TP**  
GPA\_P0/PSADC0/I2C0\_SCL/UART0\_TX/AMIC\_IN/IR\_TX  
GPA\_P1/PSADC1/I2C0\_SDA/UART0\_RX/AMIC\_BIAS/IR\_RX  
GPA\_P7/PSADC7/I2C1\_SDA/UART1\_CTS  
GPA\_P8/RTP\_XP/PSADC8/I2C2\_SCL/UART2\_TX/JTAG\_DO  
GPA\_P9/RTP\_YP/PSADC9/I2C2\_SDA/UART2\_RX/JTAG\_DI  
GPA\_P10/RTP\_XN/PSADC10/I2C3\_SCL/UART2\_RTS/JTAG\_MS  
GPA\_P11/RTP\_YN/PSADC11/I2C3\_SDA/UART2\_CTS/JTAG\_CK/PSADC\_TRIG

**SPI0/SDC0**  
GPB\_P0/SDC0\_CMD/SPI0\_HOLD/I2C1\_SCL  
GPB\_P1/SDC0\_CLK/SPI0\_WP/I2C1\_SDA  
GPB\_P2/SDC0\_D3/SPI0\_CS  
GPB\_P3/SDC0\_D0/SPI0\_MISO  
GPB\_P4/SDC0\_D1/SPI0\_MOSI  
GPB\_P5/SDC0\_D2/SPI0\_CLK

**SDC1/LCD/LVDS/MIPI/SPI1~3**  
GPC\_P0/SDC1\_D1/LCD\_D5/SPI2\_CLK/UART1\_TX/JTAG\_MS/PWM0\_A  
GPC\_P1/SDC1\_D0/LCD\_D4/SPI2\_CS/UART1\_RX/JTAG\_DI/PWM0\_B  
GPC\_P2/SDC1\_CLK/LCD\_D3/SPI2\_MOSI/UART1\_RTS/UART0\_TX/PWM1\_A  
GPC\_P3/SDC1\_CMD/LCD\_D2/SPI2\_MISO/UART2\_TX/JTAG\_DO/PWM1\_B  
GPC\_P4/SDC1\_D3/LCD\_D1/UART2\_RX/UART0\_RX/PWM2\_A  
GPC\_P5/SDC1\_D2/LCD\_D0/UART2\_RTS/UART3\_TX/JTAG\_CK/PWM2\_B  
GPC\_P6/SDC1\_DET/CLK\_OUT0/DE\_TE/UART3\_RX/PWM3\_A

GPD\_P6/LCD\_D6/SPI3\_MISO/I2C0\_SCL/UART1\_TX  
GPD\_P7/LCD\_D7/SPI2\_MISO/I2C0\_SDA/UART1\_RX  
GPD\_P8/LCD\_D8/LVDS1\_D0N/SPI1\_HOLD/UART2\_TX  
GPD\_P9/LCD\_D9/LVDS1\_D0P/SPI1\_WP/UART2\_RX  
GPD\_P10/LCD\_D10/LVDS1\_D1N/SPI1\_CS/UART3\_TX  
GPD\_P11/LCD\_D11/LVDS1\_D1P/SPI1\_MISO/UART3\_RX  
GPD\_P12/LCD\_D12/LVDS1\_D2N/SPI1\_MOSI  
GPD\_P13/LCD\_D13/LVDS1\_D2P/SPI1\_CLK  
GPD\_P14/LCD\_D14/LVDS1\_CKN/SPI3\_CLK  
GPD\_P15/LCD\_D15/LVDS1\_CKP/SPI3\_CS  
GPD\_P16/LCD\_D16/LVDS1\_D3N/SPI3\_MOSI  
GPD\_P17/LCD\_D17/LVDS1\_D3P/SPI3\_MISO  
GPD\_P18/LCD\_D18/LVDS0\_D0N/DSI\_D0N/I2C1\_SCL  
GPD\_P19/LCD\_D19/LVDS0\_D0P/DSI\_D0P/I2C1\_SDA  
GPD\_P20/LCD\_D20/LVDS0\_D1N/DSI\_D1N  
GPD\_P21/LCD\_D21/LVDS0\_D1P/DSI\_D1P/CLK\_OUT0  
GPD\_P22/LCD\_D22/LVDS0\_D2N/DSI\_D2N/CLK\_IN3\_SCL  
GPD\_P23/LCD\_D23/LVDS0\_D2P/DSI\_D2P/CLK\_IN3\_SDA  
GPD\_P24/LCD\_DCLK/LVDS0\_CKN/DSI\_D2N/SPI1\_CLK  
GPD\_P25/LCD\_HS/LVDS0\_CKP/DSI\_D2P/SPI1\_CS  
GPD\_P26/LCD\_VS/LVDS0\_D3N/DSI\_D3N/PWM3\_A/SPI1\_MOSI  
GPD\_P27/LCD\_DE/LVDS0\_D3P/DSI\_D3P/PWM3\_B/SPI1\_MISO/RTC\_32K

**SYSTEM**  
RESETN  
PLL\_XO  
PLL\_XI

D215BB

**DVP/I2S/PWM**  
GPE\_P0/DVP\_D0/I2C0\_SCL/PWM0\_A  
GPE\_P1/DVP\_D1/I2C0\_SDA/PWM0\_B  
GPE\_P2/DVP\_D2/PWM1\_A  
GPE\_P3/DVP\_D3/PWM1\_B  
GPE\_P4/DVP\_D4/PWM2\_A  
GPE\_P5/DVP\_D5/PWM2\_B  
GPE\_P6/DSPK0/DVP\_D6  
GPE\_P7/DSPK1/DVP\_D7  
GPE\_P8/I2S0\_MCLK/DVP\_CK  
GPE\_P9/I2S0\_BCLK/DVP\_HS  
GPE\_P10/I2S0\_LRCK/DVP\_VS/DSPK0/CLK\_OUT2  
GPE\_P11/I2S0\_DOUT/I2S0\_DIN/DSPK1/CLK\_OUT1  
GPE\_P14/SPI3\_MOSI/UART3\_TX  
GPE\_P15/SPI3\_MISO/UART3\_RX  
GPE\_P16/SPI0\_CLK/I2C3\_SCL  
GPE\_P17/SPI0\_CS/I2C3\_SDA  
GPE\_P18/SPI0\_MOSI/PWM3\_A  
GPE\_P19/SPI0\_MISO/PWM3\_B

**USB**  
PU0/USB0\_DM/UART0\_RX/UART1\_RX  
PU1/USB0\_DP/UART0\_TX/UART1\_TX

**Speaker/DMIC**

GPB\_P0/SPI2\_CLK  
GPB\_P1/SPI2\_CS  
GPF\_P14/DSPK0/DMIC\_D0  
GPF\_P15/DE/TE/DSPK1/DMIC\_CLK

**RTC**

RTC\_IO  
VCOIN\_RTC  
RTC\_XO  
RTC\_XI

**Power**

VCC33\_I00  
VCC33\_I01  
VCC33\_I01  
VCC33\_I00  
VDD\_SYS  
VDD\_SYS  
VDD\_SYS  
VDD\_SYS  
VDD\_SYS  
VCC\_DRAM  
VCC\_DRAM  
VCC\_DRAM  
LDO1X\_OUT  
LDO25  
VCC30\_ANA  
GND

| Title |                                    |           |
|-------|------------------------------------|-----------|
| Size  | Number                             | Revision  |
| A3    |                                    |           |
| Date: | 7/10/2026                          | Sheet of  |
| File: | C:\Users\16477\Desktop\D215.SchDoc | Drawn By: |

