

ENET Test Report

Overall result: Fail

DUT: CM_NEW
Comment:
Time of session start: 10/29/2019 10:23:09
Operator: CS
Temperature: 25° C
Standard in use: ENET

Session ID: 76, Continuation #: 1:

Time of run: 2019/10/29 10:23:46
Configuration in use: 10BASE-T All tests
Limits in use: Default
Oscilloscope Name: LCRY4751N40116 Model: WAVERUNNER9404M-MS
Oscilloscope Serial #: LCRY4751N40116
Computer: C0038283
Oscilloscope firmware version: 8.9.0.3 (Build 263624)
QualiPHY core version: 8.9.0.1 (Build 1000001)

QualiPHY script version: 8.9.0.1
Stylesheet version: 1.2.0.7

Summary Table


[Hide Table]

Pass #	Test	Measurement	Current Value	Test Criteria
✓	1 14.3.1.2.1	Peak Differential Output Voltage	2.43 V	2.20 V < x < 2.80 V
✓	1 14.3.1.2.3	8BT Output Timing Jitter 100 ohm without TPM Internal MAU	1.569 ns	x <= 22.000 ns
✓	1 14.3.1.2.3	8.5BT Output Timing Jitter 100 ohm without TPM Internal MAU	1.279 ns	x <= 22.000 ns
✓	1 14.3.1.2.1	TP_IDL 100 ohm without TPM (Head)	AllPass	match
✓	1 14.3.1.2.1	TP_IDL 100 ohm without TPM (Tail)	AllPass	match
✓	1 14.3.1.2.1	Link Pulse 100 ohm without TPM (Head)	AllPass	match
✓	1 14.3.1.2.1	Link Pulse 100 ohm without TPM (Tail)	AllPass	match
✓	1 14.3.1.2.1	Differential Output Voltage Harmonics	43.3 dB	x >= 27.0 dB
✓	1 14.3.1.2.1	TP_IDL Load 1 without TPM (Head)	AllPass	match
✓	1 14.3.1.2.1	TP_IDL Load 1 without TPM (Tail)	AllPass	match
✓	1 14.3.1.2.1	Link Pulse Load 1 without TPM (Head)	AllPass	match
✓	1 14.3.1.2.1	Link Pulse Load 1 without TPM (Tail)	AllPass	match
✓	1 14.3.1.2.1	TP_IDL Load 2 without TPM (Head)	AllPass	match
✓	1 14.3.1.2.1	TP_IDL Load 2 without TPM (Tail)	AllPass	match
✓	1 14.3.1.2.1	Link Pulse Load 2 without TPM (Head)	AllPass	match
✓	1 14.3.1.2.1	Link Pulse Load 2 without TPM (Tail)	AllPass	match
✓	1 14.3.1.2.1	Template - Internal MAU Normal	AllPass	match
✓	1 14.3.1.2.1	Template - Internal MAU Inverted	AllPass	match
✓	1 14.3.1.2.1	TP_IDL 100 ohm with TPM (Head)	AllPass	match
✓	1 14.3.1.2.1	TP_IDL 100 ohm with TPM (Tail)	AllPass	match
✓	1 14.3.1.2.3	8BT Output Timing Jitter 100 ohm with TPM Internal MAU	4.563 ns	x <= 22.000 ns
✓	1 14.3.1.2.3	8.5BT Output Timing Jitter 100 ohm with TPM Internal MAU	5.859 ns	x <= 22.000 ns
✓	1 14.3.1.2.1	Link Pulse 100 ohm with TPM (Head)	AllPass	match
✓	1 14.3.1.2.1	Link Pulse 100 ohm with TPM (Tail)	AllPass	match
✓	1 14.3.1.2.1	TP_IDL Load 1 with TPM (Head)	AllPass	match
✓	1 14.3.1.2.1	TP_IDL Load 1 with TPM (Tail)	AllPass	match
✓	1 14.3.1.2.1	Link Pulse Load 1 with TPM (Head)	AllPass	match
✓	1 14.3.1.2.1	Link Pulse Load 1 with TPM (Tail)	AllPass	match
✗	1 14.3.1.2.1	TP_IDL Load 2 with TPM (Head)	AllFail	match
✓	1 14.3.1.2.1	TP_IDL Load 2 with TPM (Tail)	AllPass	match
✓	1 14.3.1.2.1	Link Pulse Load 2 with TPM (Head)	AllPass	match
✓	1 14.3.1.2.1	Link Pulse Load 2 with TPM (Tail)	AllPass	match
✓	1 14.3.1.2.5	Common mode output voltage	32.3 mV	x <= 50.0 mV

Details


Test 14.3.1.2.1 - Differential output voltage

[\[Up\]](#)

 Pass	Measurement: Peak Differential Output Voltage	
	Current Value: 2.43 V	Test Criteria: $2.20\text{ V} < x < 2.80\text{ V}$
	Timestamp: 10/29/2019 10:24:14	Limit Name: 10BT-DOV


Test 14.3.1.2.3 - Output timing jitter 8 BT

[\[Up\]](#)

 Pass	Measurement: 8BT Output Timing Jitter 100 ohm without TPM Internal MAU	
	Current Value: 1.569 ns	Test Criteria: $x \leq 22.000\text{ ns}$
	Timestamp: 10/29/2019 10:24:17	Limit Name: 10BT-8BT-Int-Jitter


Test 14.3.1.2.3 - Output timing jitter 8.5 BT

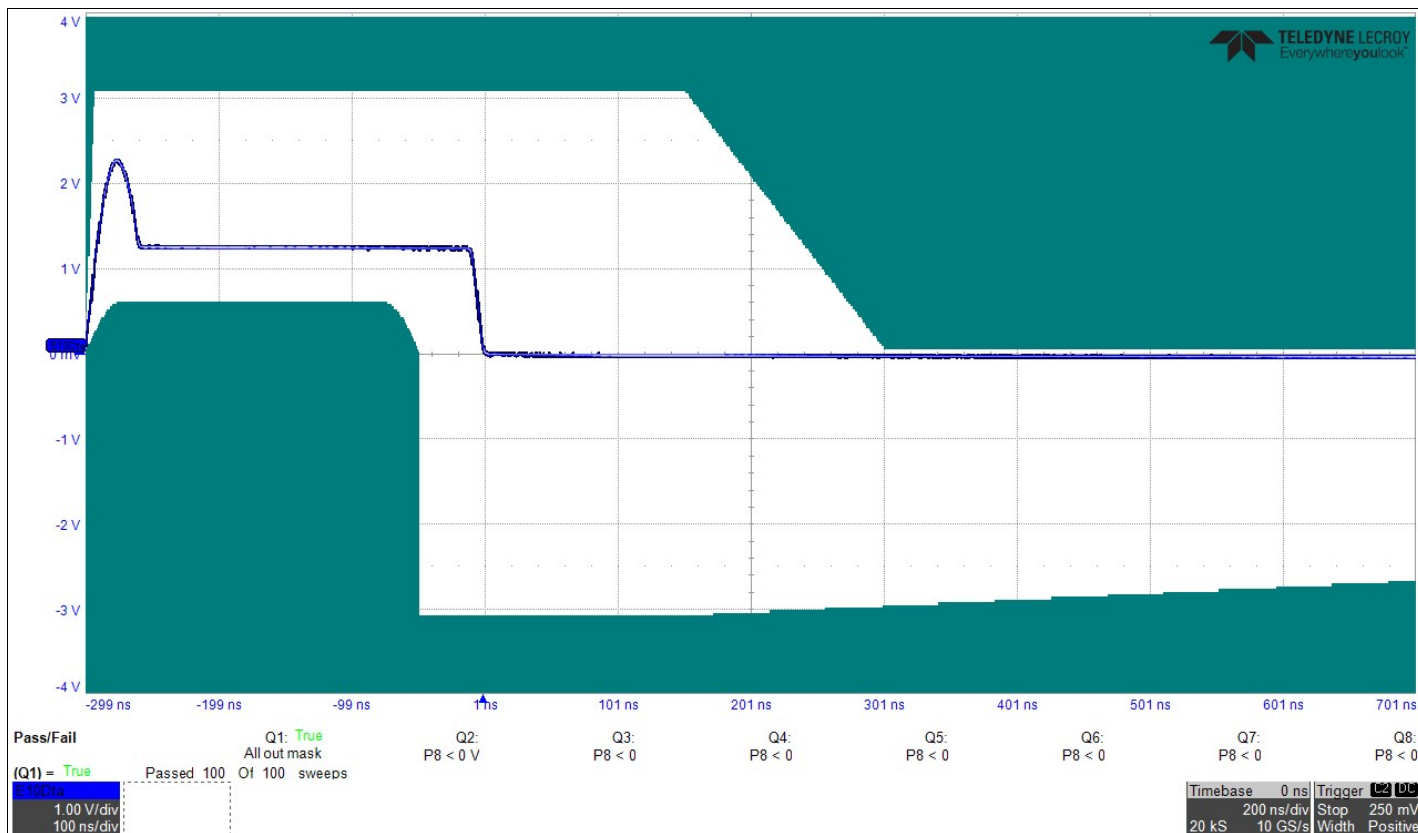
[\[Up\]](#)

 Pass	Measurement: 8.5BT Output Timing Jitter 100 ohm without TPM Internal MAU	
	Current Value: 1.279 ns	Test Criteria: $x \leq 22.000\text{ ns}$
	Timestamp: 10/29/2019 10:24:18	Limit Name: 10BT-85BT-Int-Jitter

Test 14.3.1.2.1 - TP_IDL Head

[\[Up\]](#)

 Pass	Measurement: TP_IDL 100 ohm without TPM (Head)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:24:23	Limit Name: Mask-Test




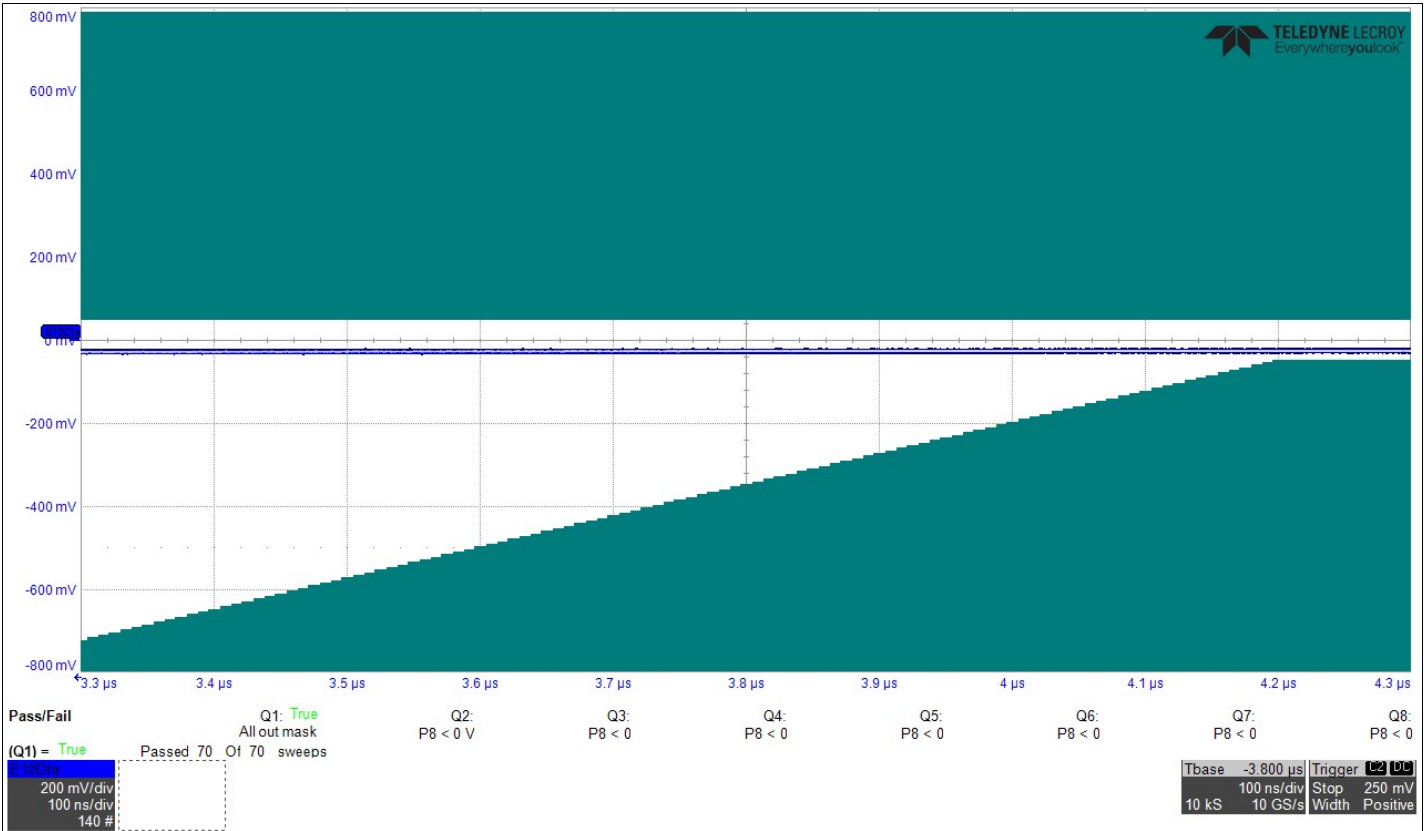
TP_IDL Head mask - 100 Ohm without twisted pair model

Timestamp: 10/29/2019 10:24:23

Test 14.3.1.2.1 - TP_IDL Tail

[Up]


 Pass	Measurement: TP_IDL 100 ohm without TPM (Tail)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:24:26	Limit Name: Mask-Test

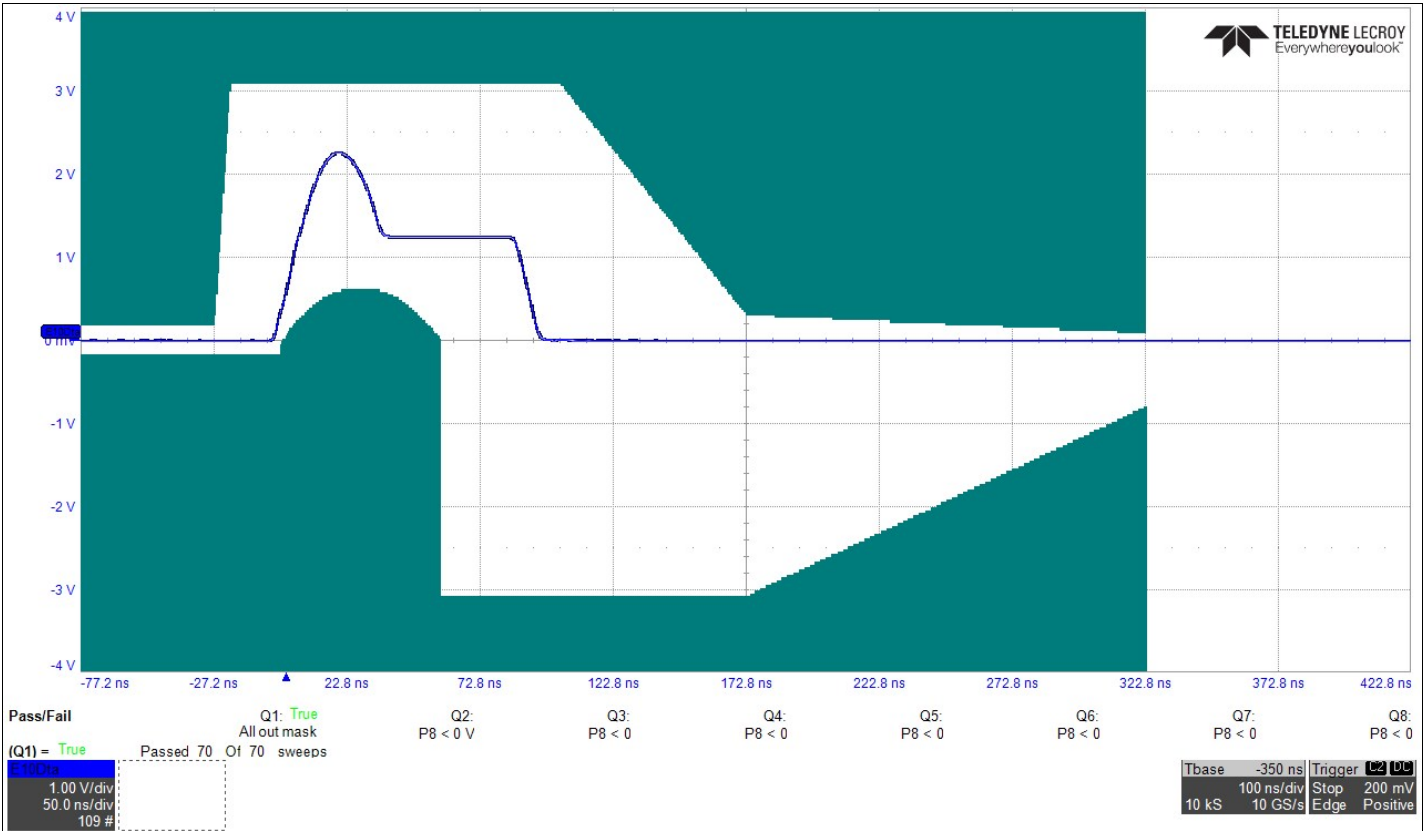


TP_IDL Tail mask - 100 Ohm without twisted pair model
 Timestamp: 10/29/2019 10:24:26

Test 14.3.1.2.1 - Link Test Pulse Head

[\[Up\]](#)


 Pass	Measurement: Link Pulse 100 ohm without TPM (Head)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:24:41	Limit Name: Mask-Test

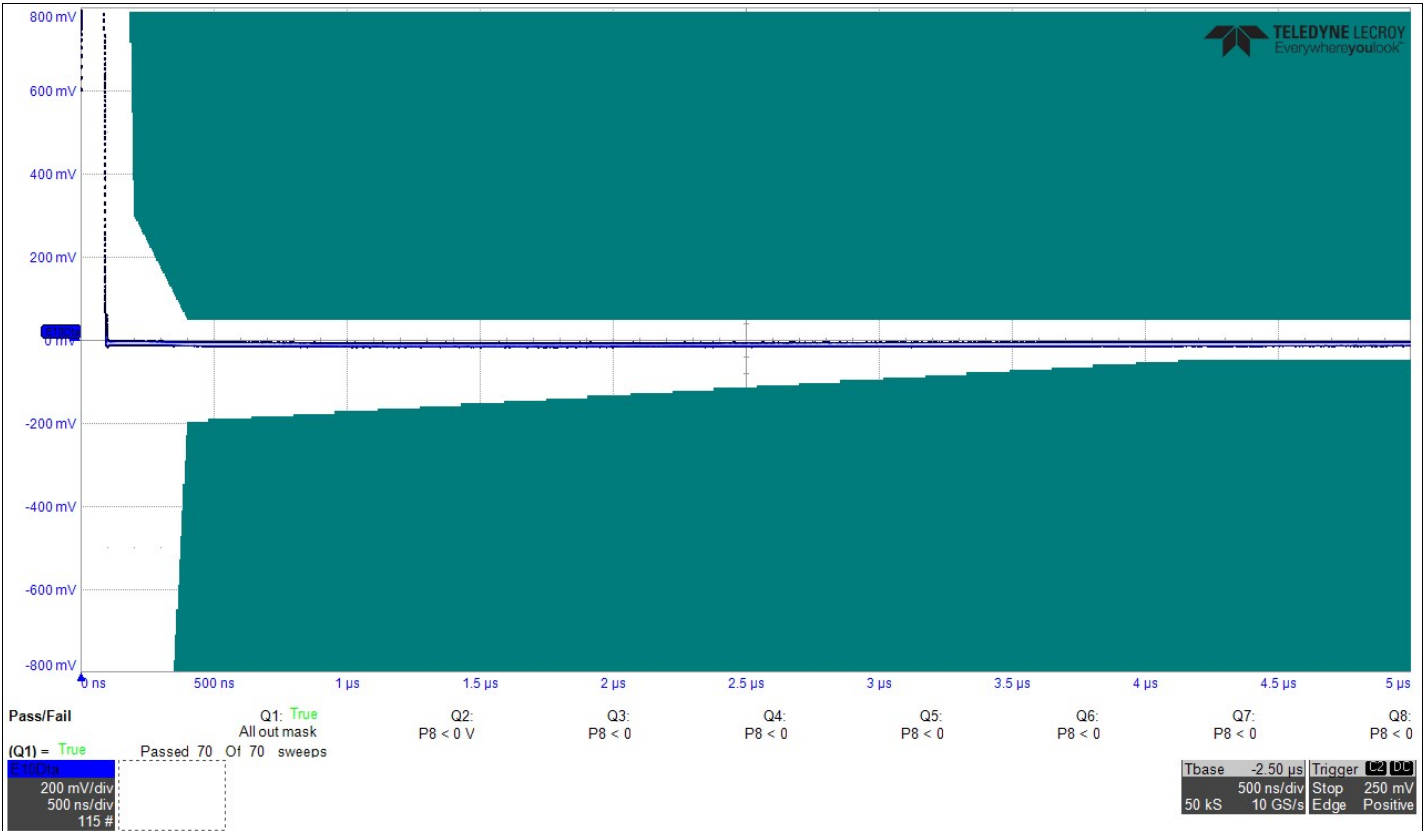


Link Test Pulse, Head mask - 100 ohm without TPM
Timestamp: 10/29/2019 10:24:41

Test 14.3.1.2.1 - Link Test Pulse Tail

[\[Up\]](#)


 Pass	Measurement: Link Pulse 100 ohm without TPM (Tail)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:24:44	Limit Name: Mask-Test



Link Test Pulse, Tail mask - 100 ohm without TPM
 Timestamp: 10/29/2019 10:24:45


Test 14.3.1.2.1 - Differential output voltage Harmonics

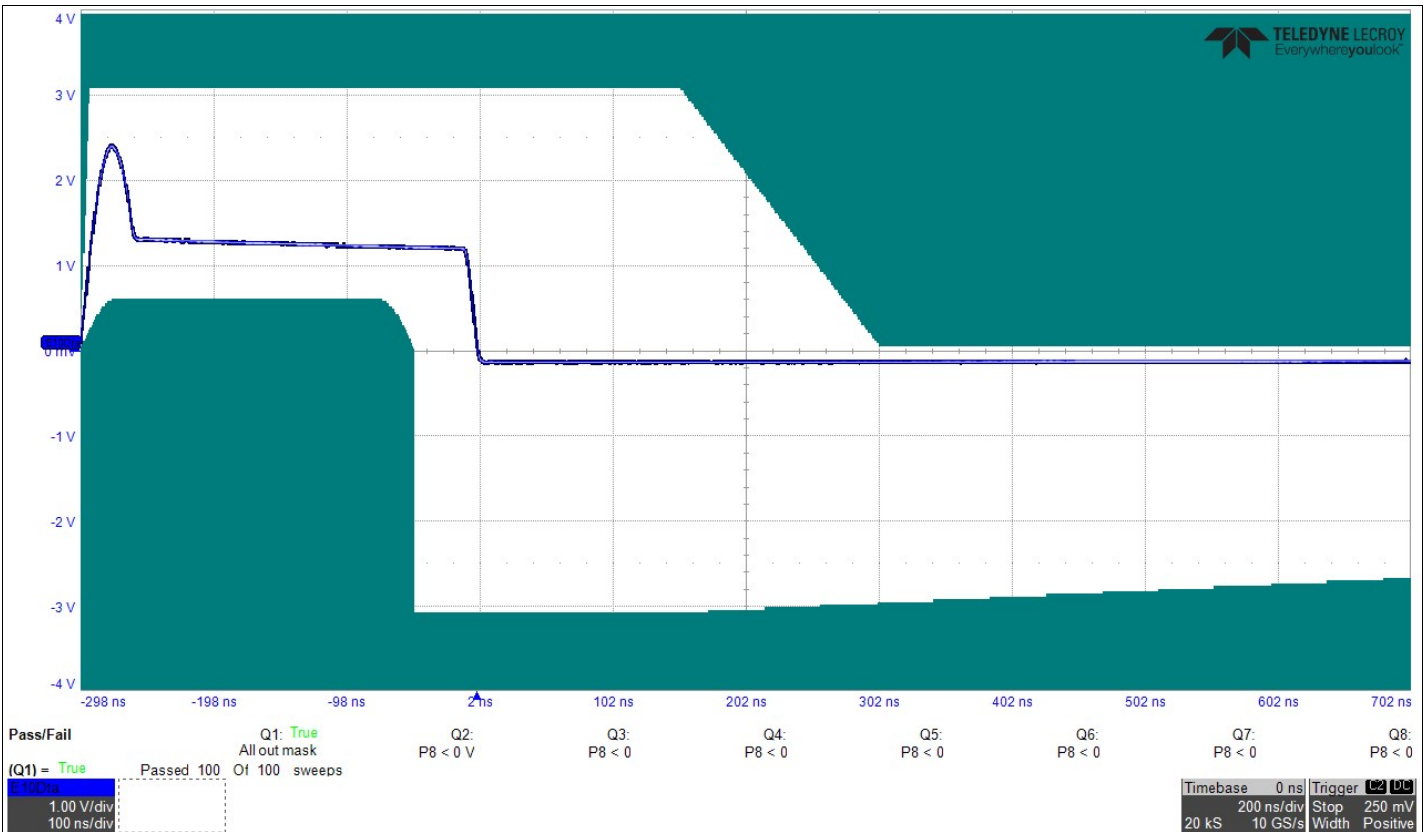
[\[Up\]](#)

 Pass	Measurement: Differential Output Voltage Harmonics	
	Current Value: 43.3 dB	Test Criteria: $x \geq 27.0$ dB
	Timestamp: 10/29/2019 10:25:11	Limit Name: 10BT-Harmonics

Test 14.3.1.2.1 - TP_IDL Head

[\[Up\]](#)

 Pass	Measurement: TP_IDL Load 1 without TPM (Head)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:25:33	Limit Name: Mask-Test




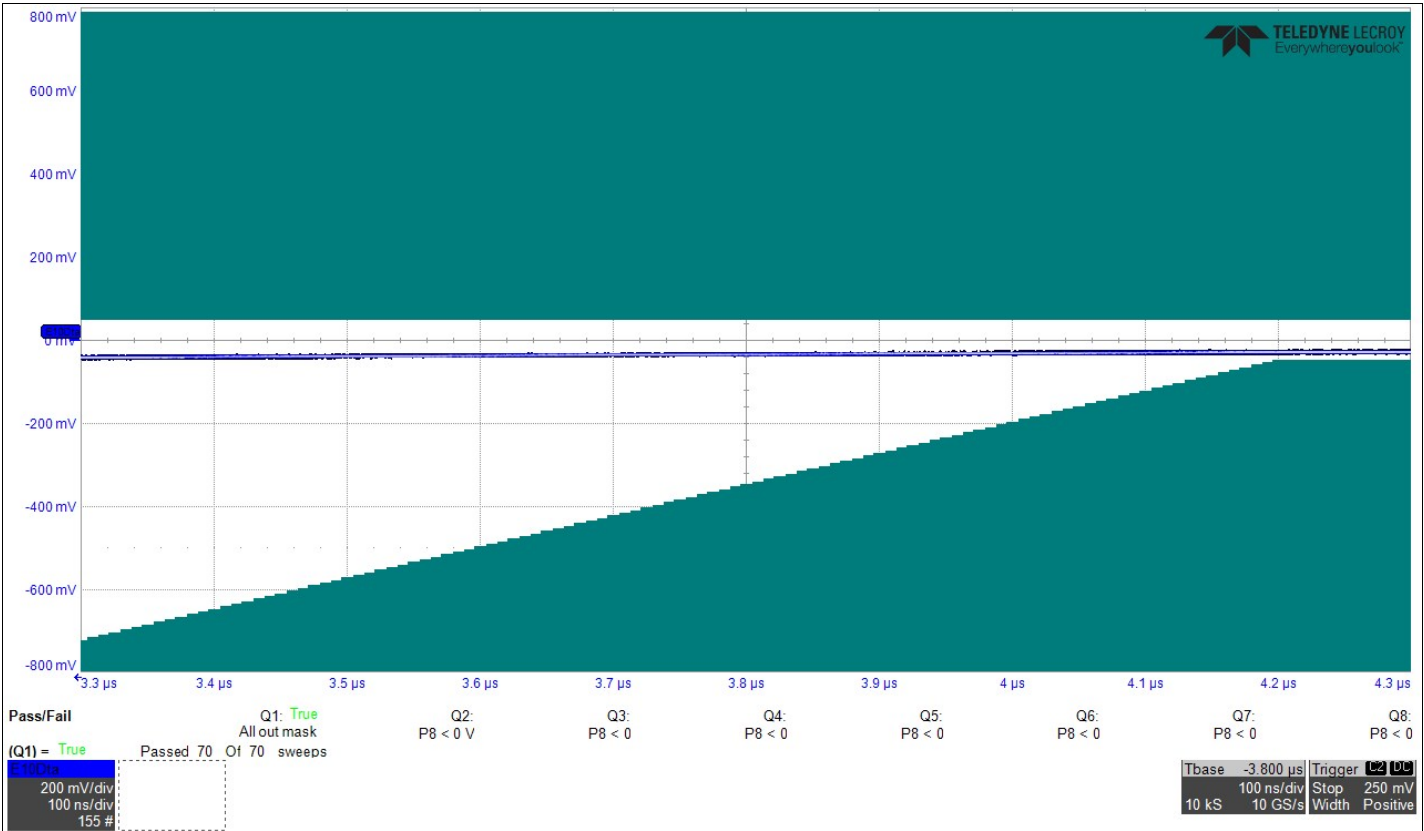
TP_IDL Head mask - Load 1 without TPM

Timestamp: 10/29/2019 10:25:34

Test 14.3.1.2.1 - TP_IDL Tail

[\[Up\]](#)


 Pass	Measurement: TP_IDL Load 1 without TPM (Tail)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:25:37	Limit Name: Mask-Test

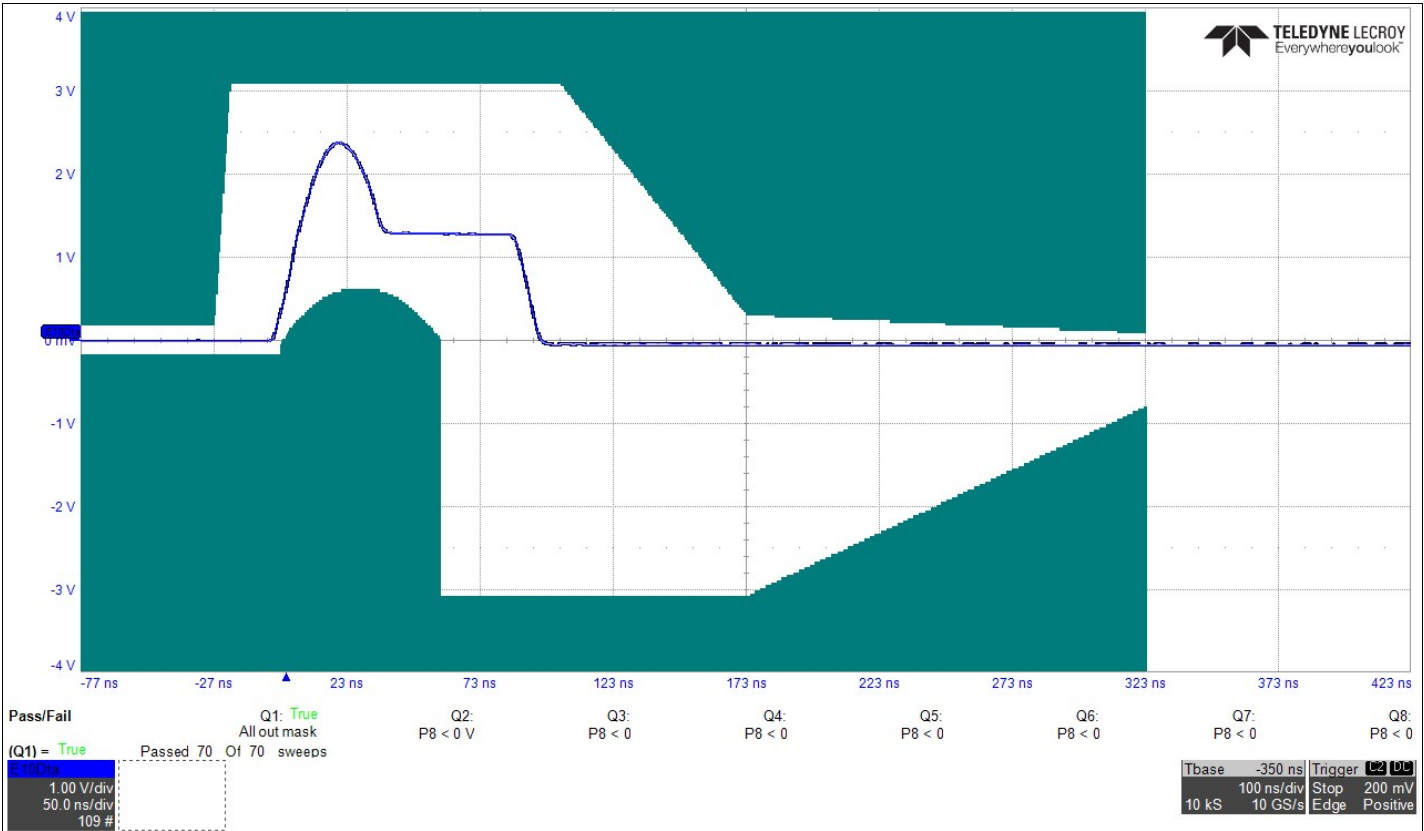


TP_IDL Tail mask - Load 1, without TPM
 Timestamp: 10/29/2019 10:25:37

Test 14.3.1.2.1 - Link Test Pulse Head

[\[Up\]](#)


 Pass	Measurement: Link Pulse Load 1 without TPM (Head)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:25:49	Limit Name: Mask-Test

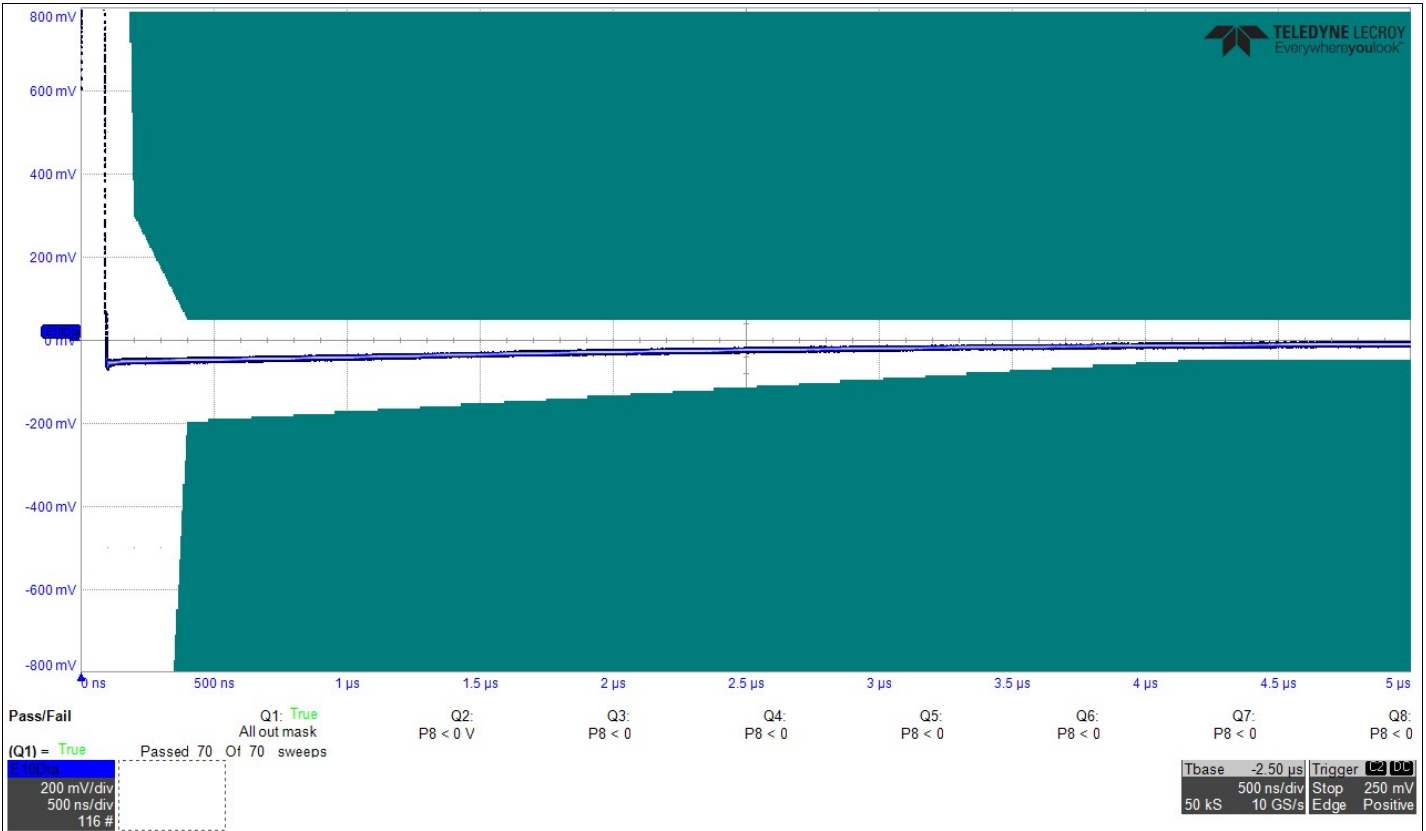


Link Test Pulse, Head Mask - Load 1, without TPM
Timestamp: 10/29/2019 10:25:49

Test 14.3.1.2.1 - Link Test Pulse Tail

[Up]


 Pass	Measurement: Link Pulse Load 1 without TPM (Tail)		
	Current Value: AllPass	Test Criteria: match	
	Timestamp: 10/29/2019 10:25:53	Limit Name: Mask-Test	

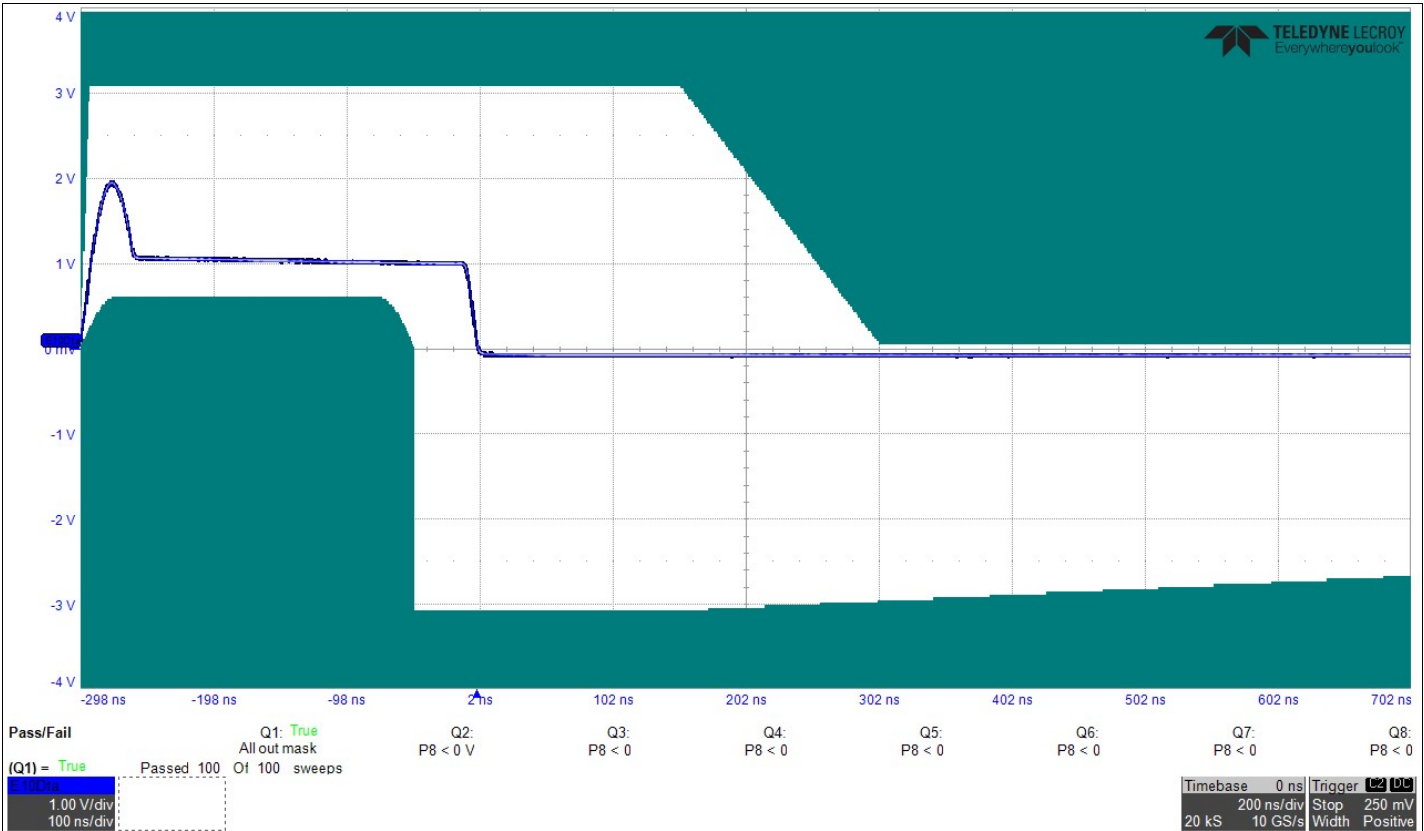


Link Test Pulse, Tail Mask - Load 1, without TPM
Timestamp: 10/29/2019 10:25:53

Test 14.3.1.2.1 - TP_IDL Head

[Up]


 Pass	Measurement: TP_IDL Load 2 without TPM (Head)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:26:16	Limit Name: Mask-Test

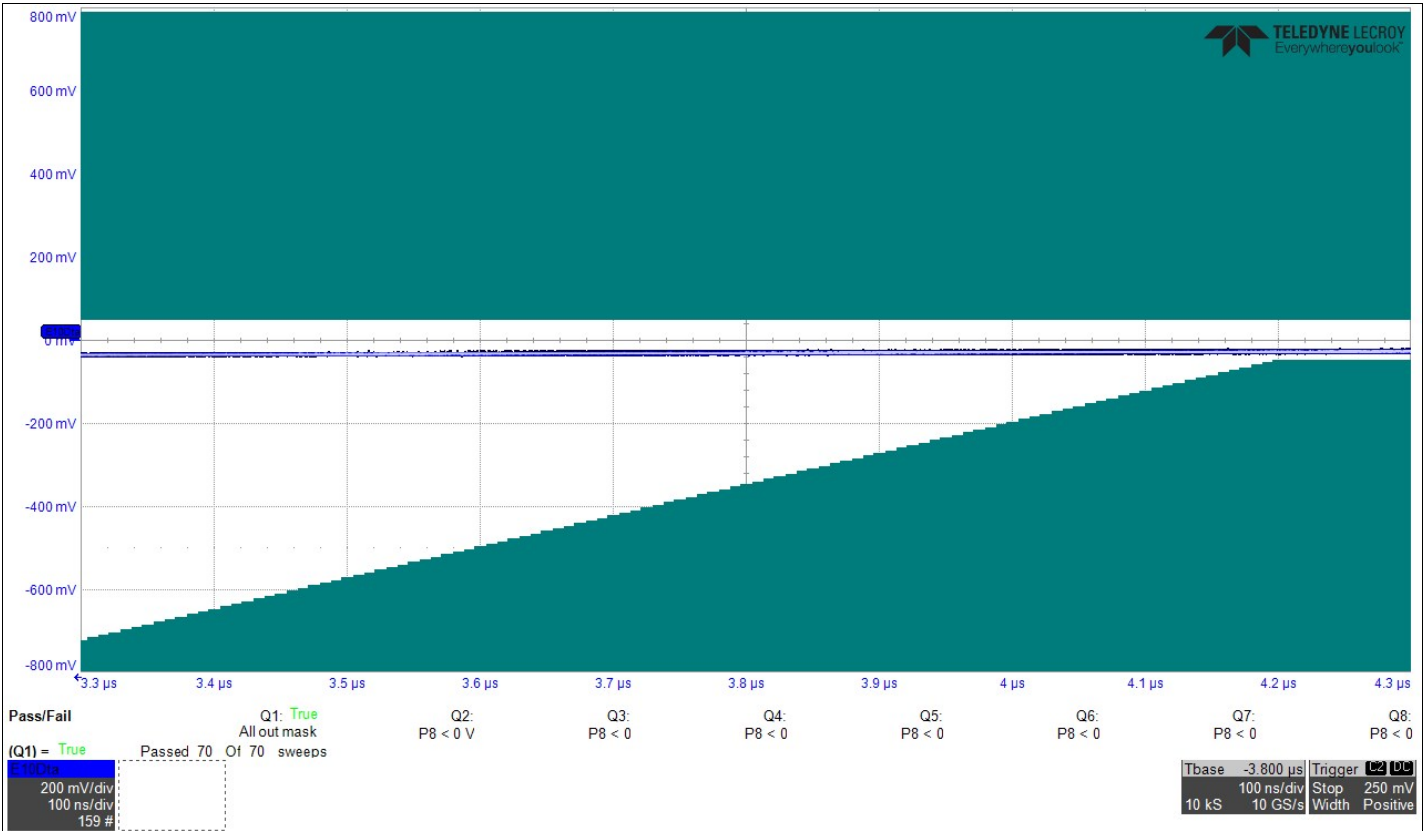


TP_IDL Head mask - Load 2, without TPM
Timestamp: 10/29/2019 10:26:16

Test 14.3.1.2.1 - TP_IDL Tail

[Up]


 Pass	Measurement: TP_IDL Load 2 without TPM (Tail)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:26:20	Limit Name: Mask-Test

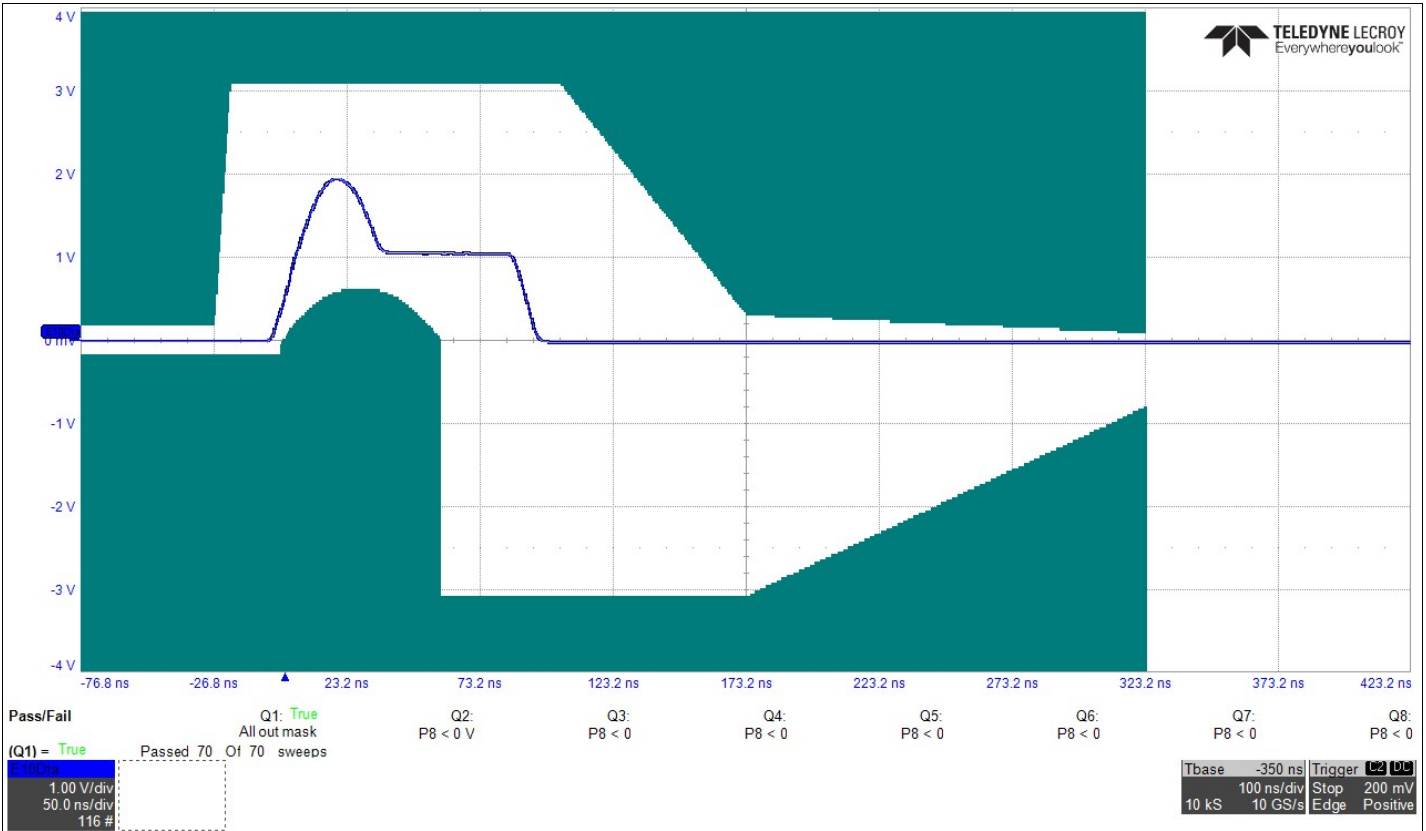


TP_IDL Tail mask - Load 2, without TPM
 Timestamp: 10/29/2019 10:26:20

Test 14.3.1.2.1 - Link Test Pulse Head

[\[Up\]](#)


 Pass	Measurement: Link Pulse Load 2 without TPM (Head)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:26:42	Limit Name: Mask-Test

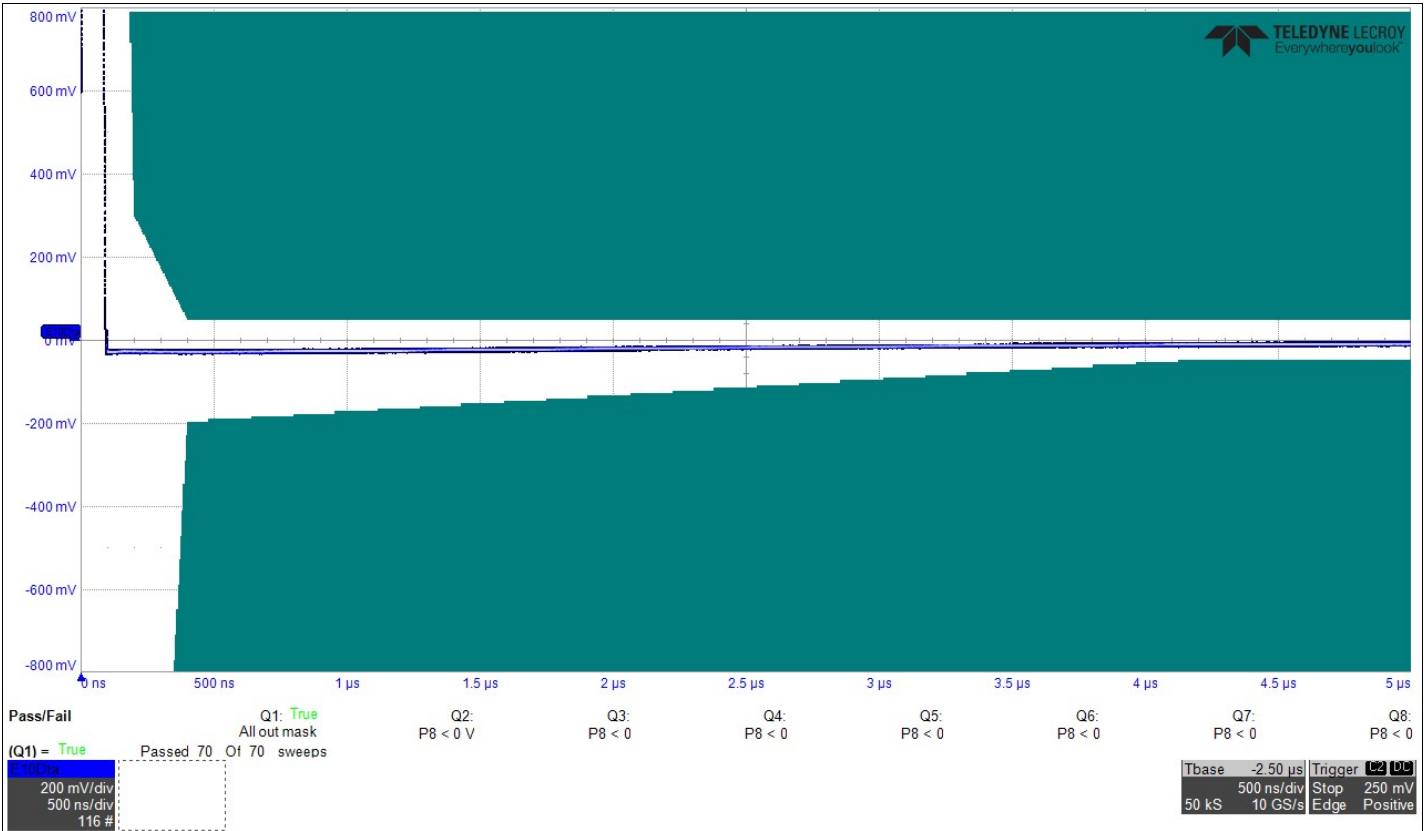


Link Test Pulse, Head Mask - Load 2, without TPM
Timestamp: 10/29/2019 10:26:43

Test 14.3.1.2.1 - Link Test Pulse Tail

[\[Up\]](#)


 Pass	Measurement: Link Pulse Load 2 without TPM (Tail)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:26:46	Limit Name: Mask-Test

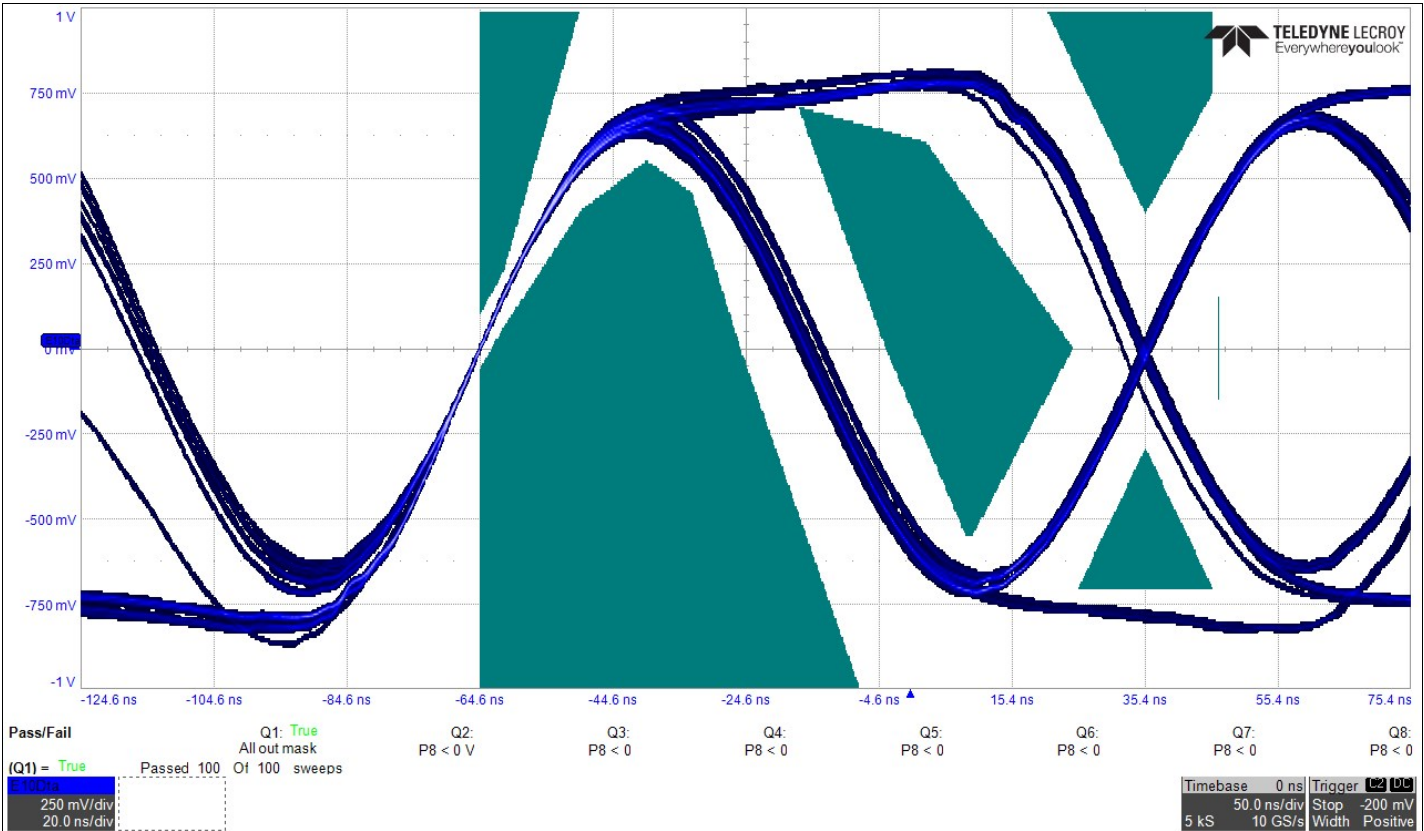


Link Test Pulse, Tail Mask - Load 2, without TPM
 Timestamp: 10/29/2019 10:26:47

Test 14.3.1.2.1 - DOV Internal MAU Normal

[Up]


 Pass	Measurement: Template - Internal MAU Normal	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:27:21	Limit Name: Mask-Test

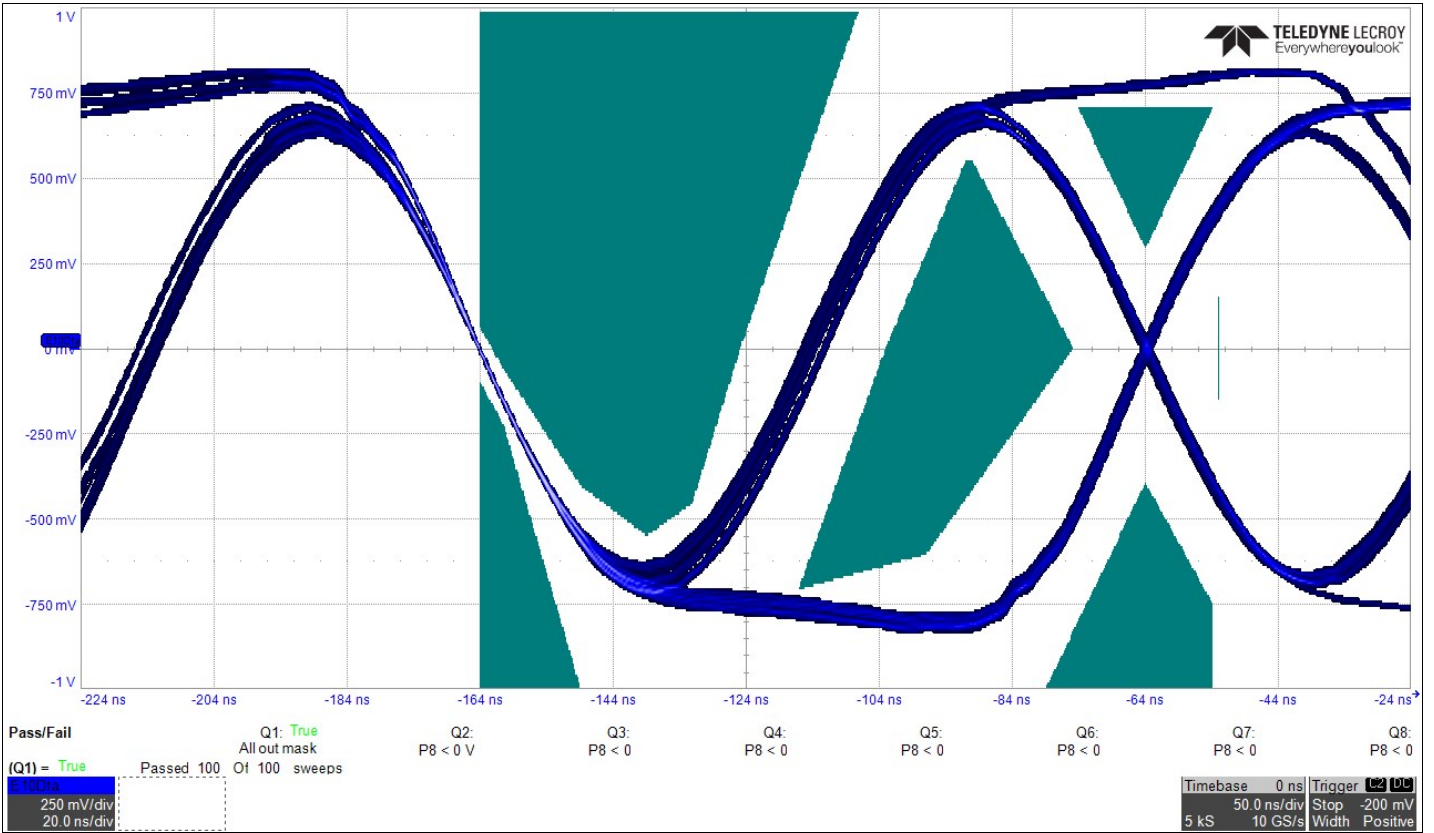


DOV Template Internal MAU
Timestamp: 10/29/2019 10:27:21

Test 14.3.1.2.1 - DOV Internal MAU Inverted

[\[Up\]](#)


 Pass	Measurement: Template - Internal MAU Inverted	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:27:28	Limit Name: Mask-Test

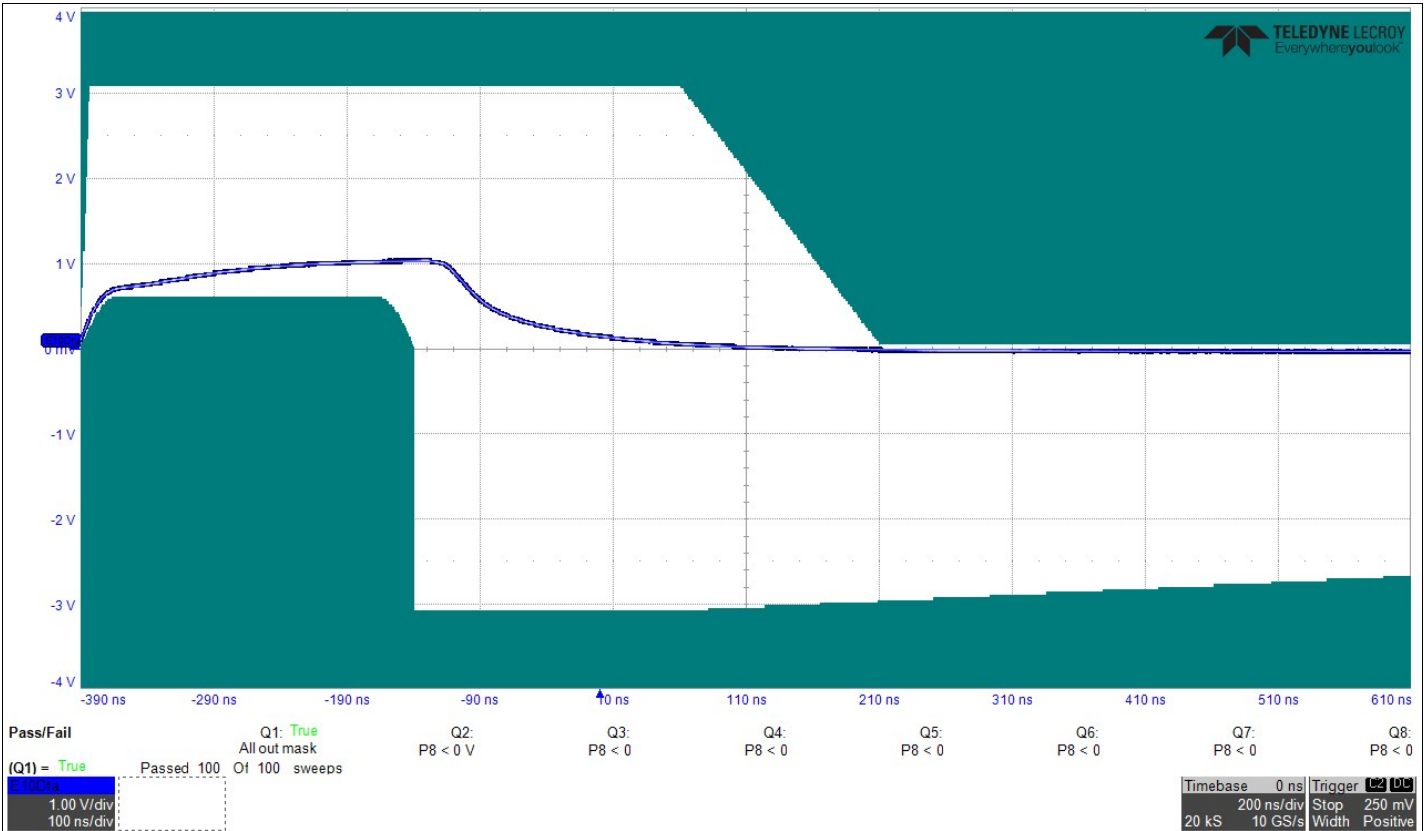


DOV Template Internal MAU, Inverted
 Timestamp: 10/29/2019 10:27:29

Test 14.3.1.2.1 - DOV TP_IDL Head

[Up]


 Pass	Measurement: TP_IDL 100 ohm with TPM (Head)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:27:35	Limit Name: Mask-Test

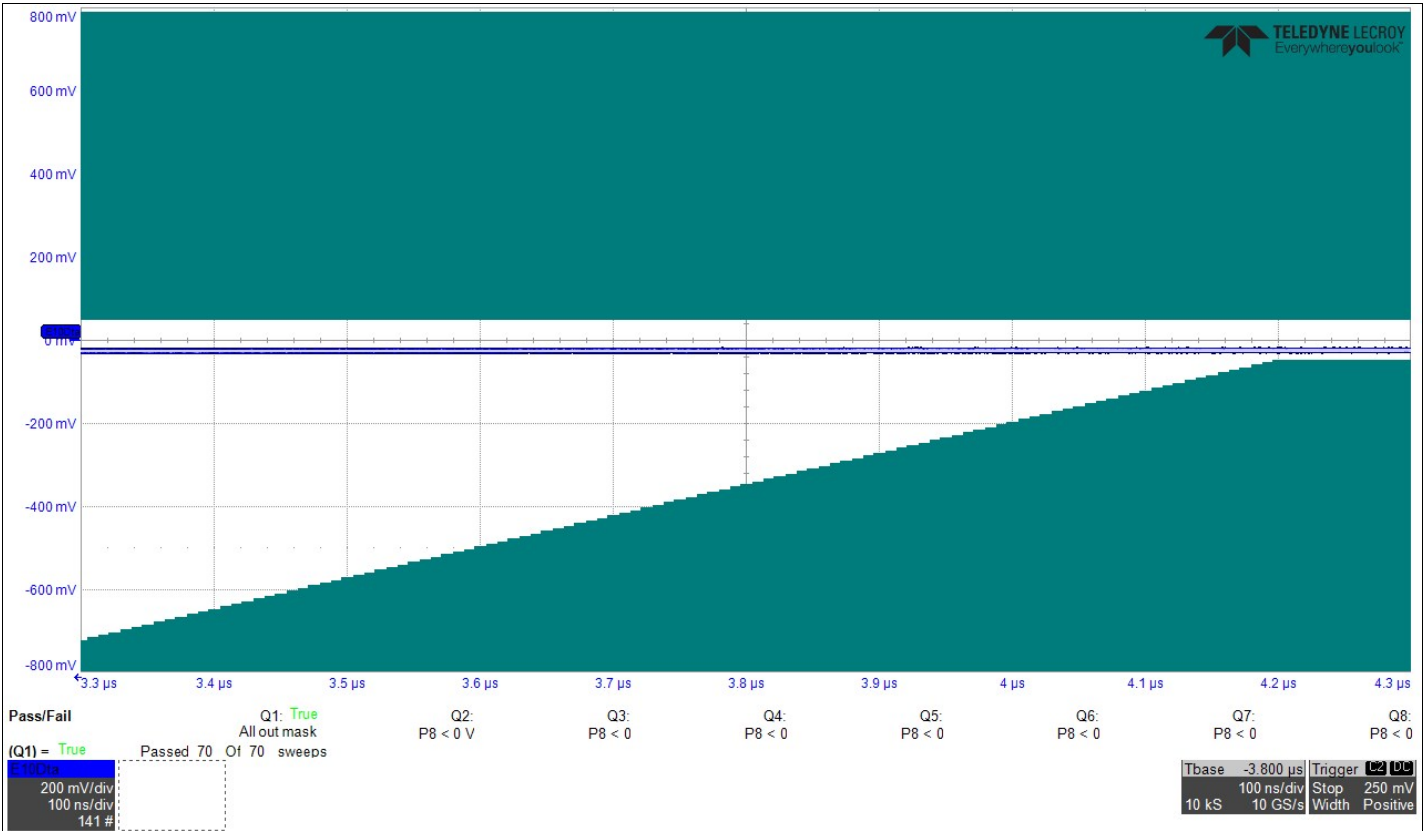


TP_IDL Head Mask, 100 Ohm load with TPM
Timestamp: 10/29/2019 10:27:35

Test 14.3.1.2.1 - DOV TP_IDL Tail

[Up]


 Pass	Measurement: TP_IDL 100 ohm with TPM (Tail)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:27:38	Limit Name: Mask-Test



TP_IDL Tail Mask, 100 Ohm load with TPM
Timestamp: 10/29/2019 10:27:39


Test 14.3.1.2.3 - Output Timing Jitter 8 BT

[\[Up\]](#)

 Pass	Measurement: 8BT Output Timing Jitter 100 ohm with TPM Internal MAU	
	Current Value: 4.563 ns	Test Criteria: x <= 22.000 ns
	Timestamp: 10/29/2019 10:27:43	Limit Name: 10BT-8BT-Int-Jitter


Test 14.3.1.2.3 - Output Timing Jitter 8.5 BT

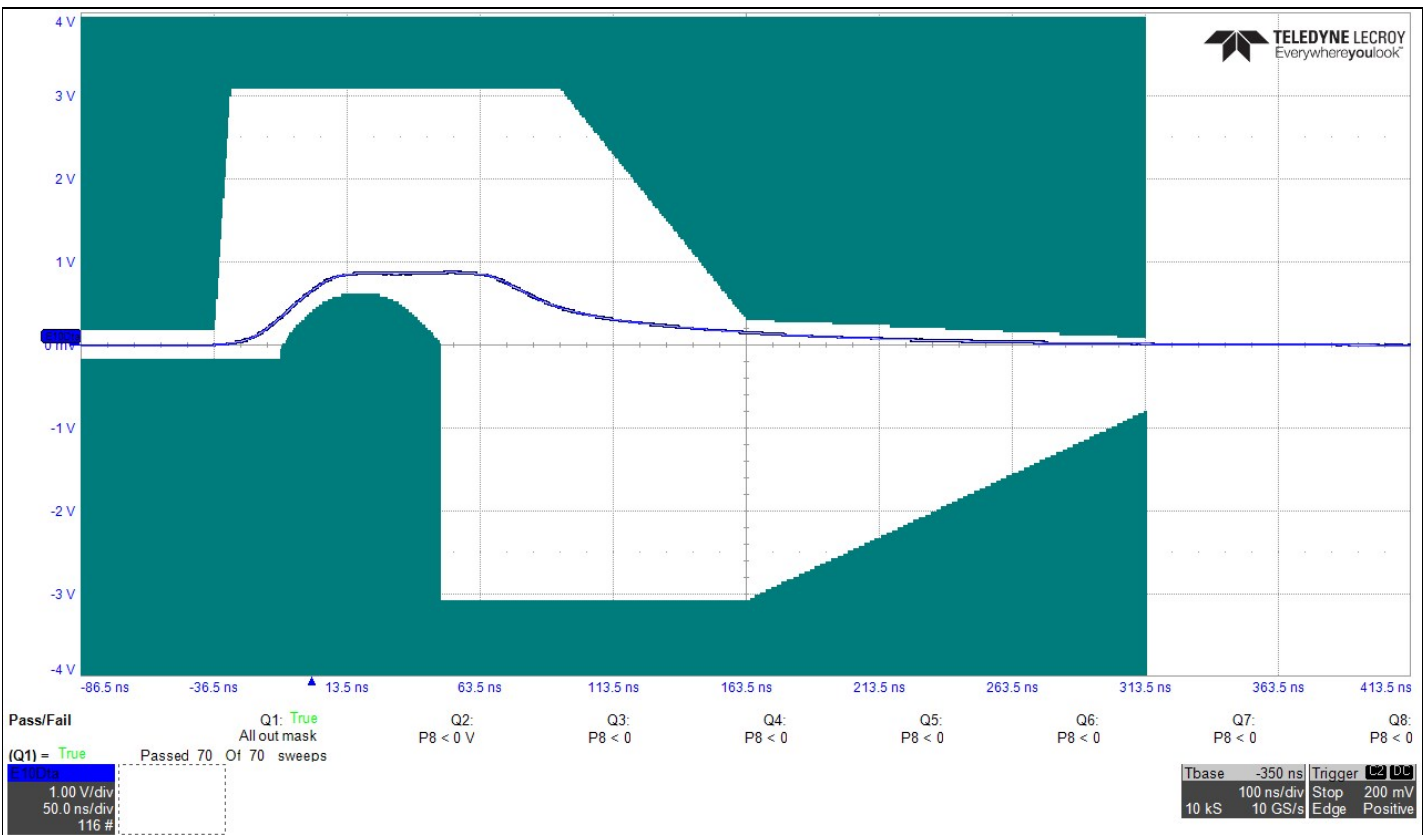
[\[Up\]](#)

 Pass	Measurement: 8.5BT Output Timing Jitter 100 ohm with TPM Internal MAU	
	Current Value: 5.859 ns	Test Criteria: x <= 22.000 ns
	Timestamp: 10/29/2019 10:27:44	Limit Name: 10BT-85BT-Int-Jitter

Test 14.3.1.2.1 - DOV Link Test Pulse Head

[\[Up\]](#)

 Pass	Measurement: Link Pulse 100 ohm with TPM (Head)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:27:56	Limit Name: Mask-Test




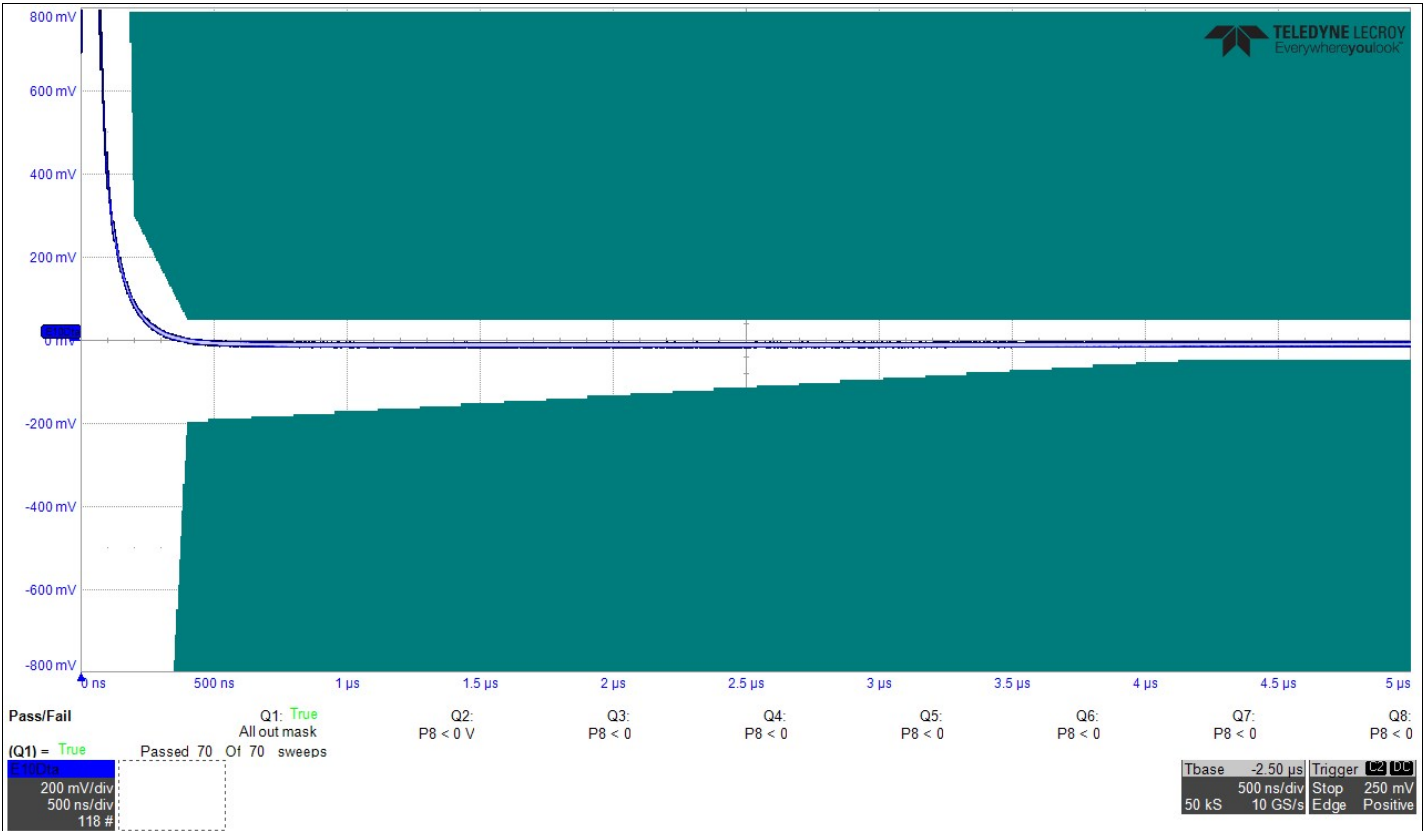
Link Test Pulse Head mask - 100 Ohm load with TPM

Timestamp: 10/29/2019 10:27:56

Test 14.3.1.2.1 - DOV Link Test Pulse Tail

[Up]


 Pass	Measurement: Link Pulse 100 ohm with TPM (Tail)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:28:00	Limit Name: Mask-Test

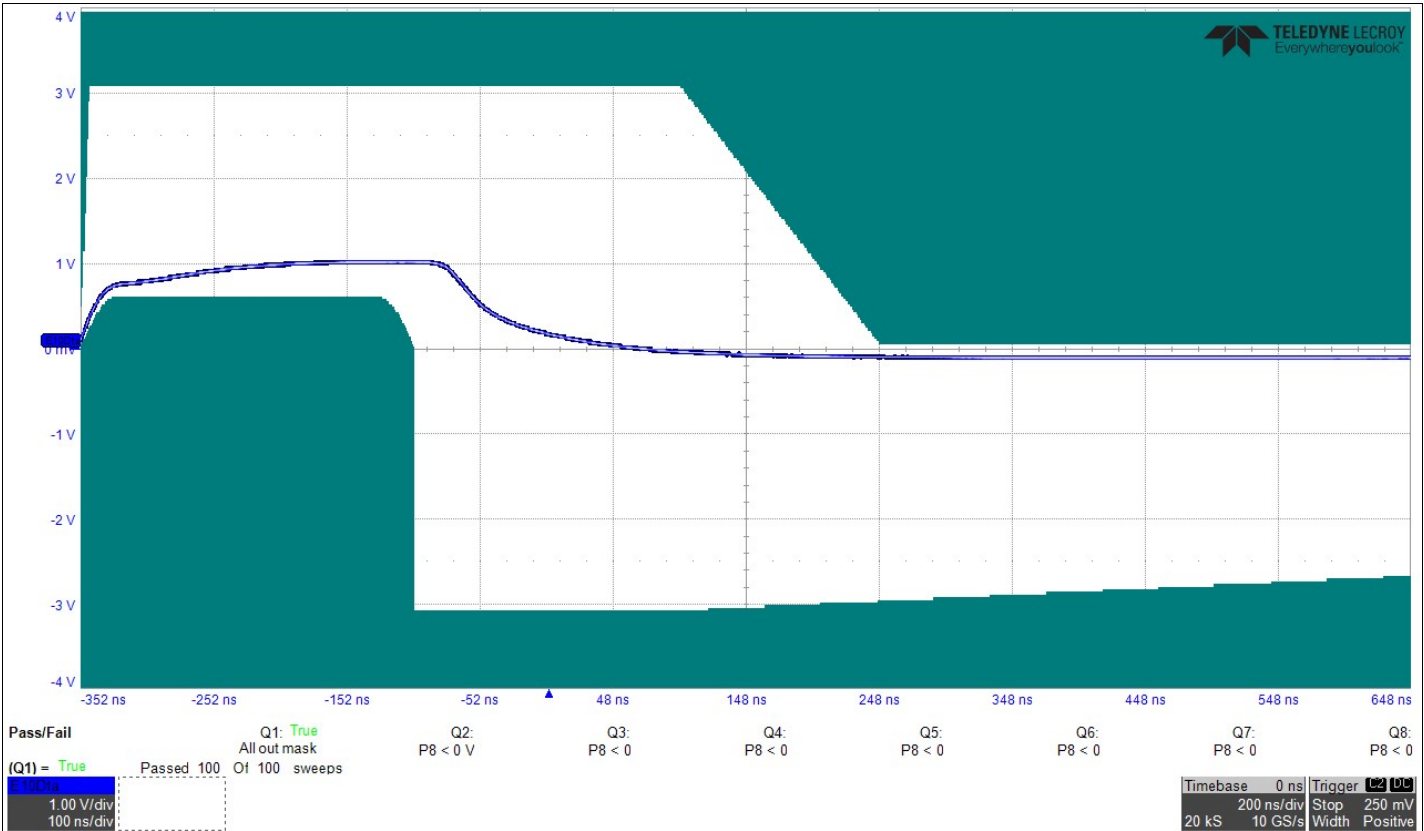


Link Test Pulse Tail mask - 100 Ohm load with TPM
Timestamp: 10/29/2019 10:28:01

Test 14.3.1.2.1 - DOV TP_IDL Head

[Up]


 Pass	Measurement: TP_IDL Load 1 with TPM (Head)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:28:21	Limit Name: Mask-Test

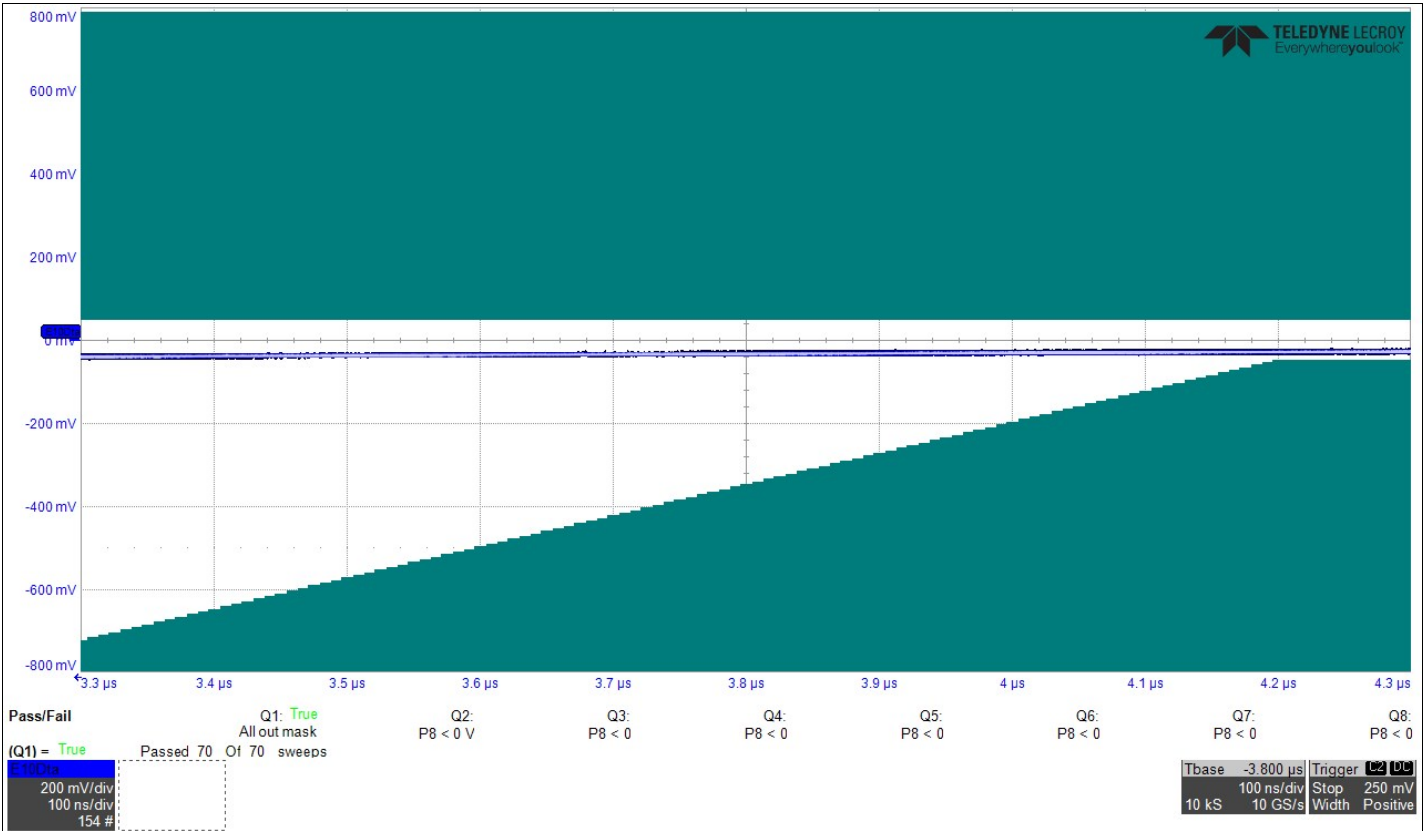


TP_IDL Head mask - Load 1, with TPM
Timestamp: 10/29/2019 10:28:22

Test 14.3.1.2.1 - DOV TP_IDL Tail

[Up]


 Pass	Measurement: TP_IDL Load 1 with TPM (Tail)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:28:25	Limit Name: Mask-Test

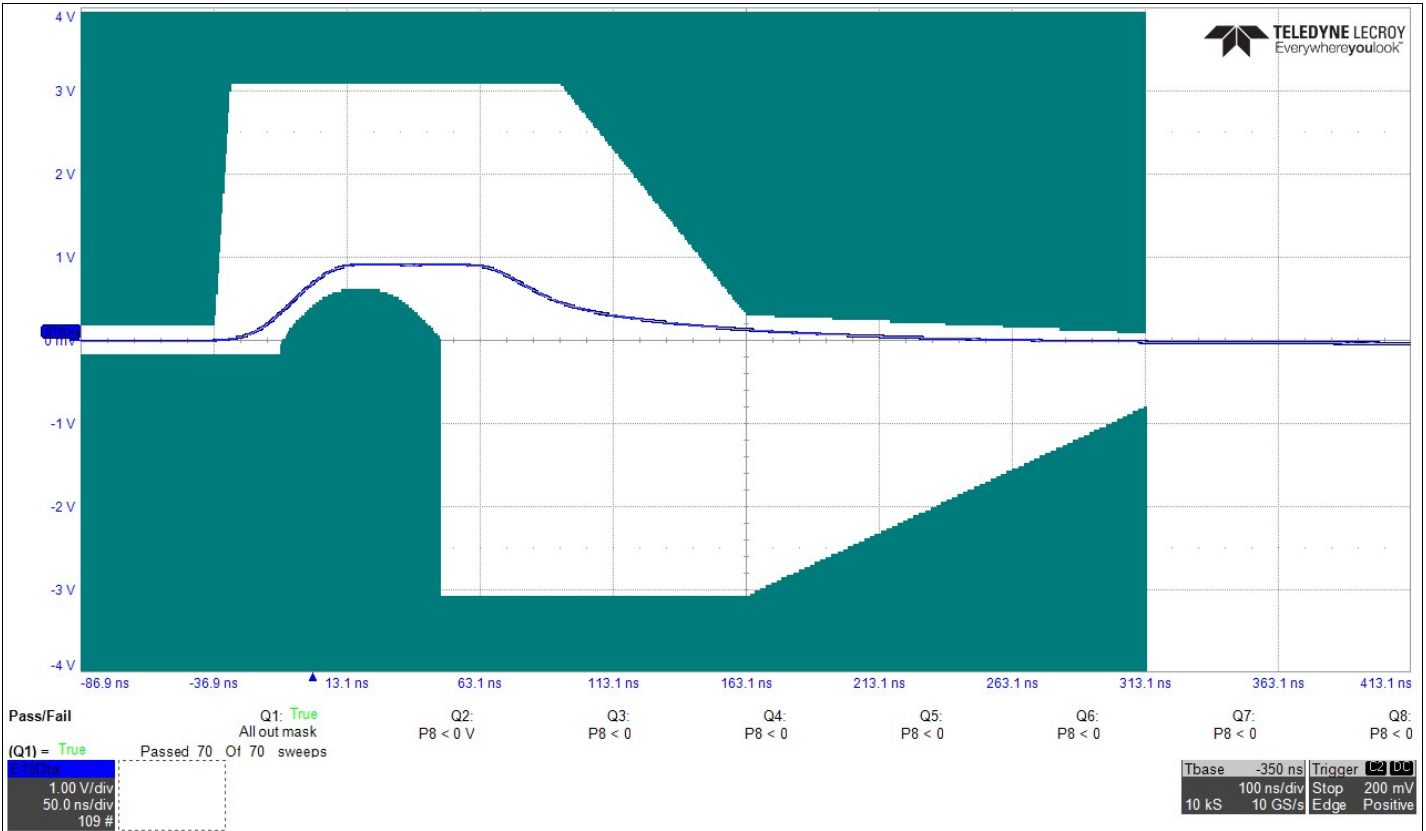


TP_IDL Tail mask - Load 1, with TPM
 Timestamp: 10/29/2019 10:28:25

Test 14.3.1.2.1 - DOV Link Test Pulse Head

[\[Up\]](#)


 Pass	Measurement: Link Pulse Load 1 with TPM (Head)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:28:43	Limit Name: Mask-Test

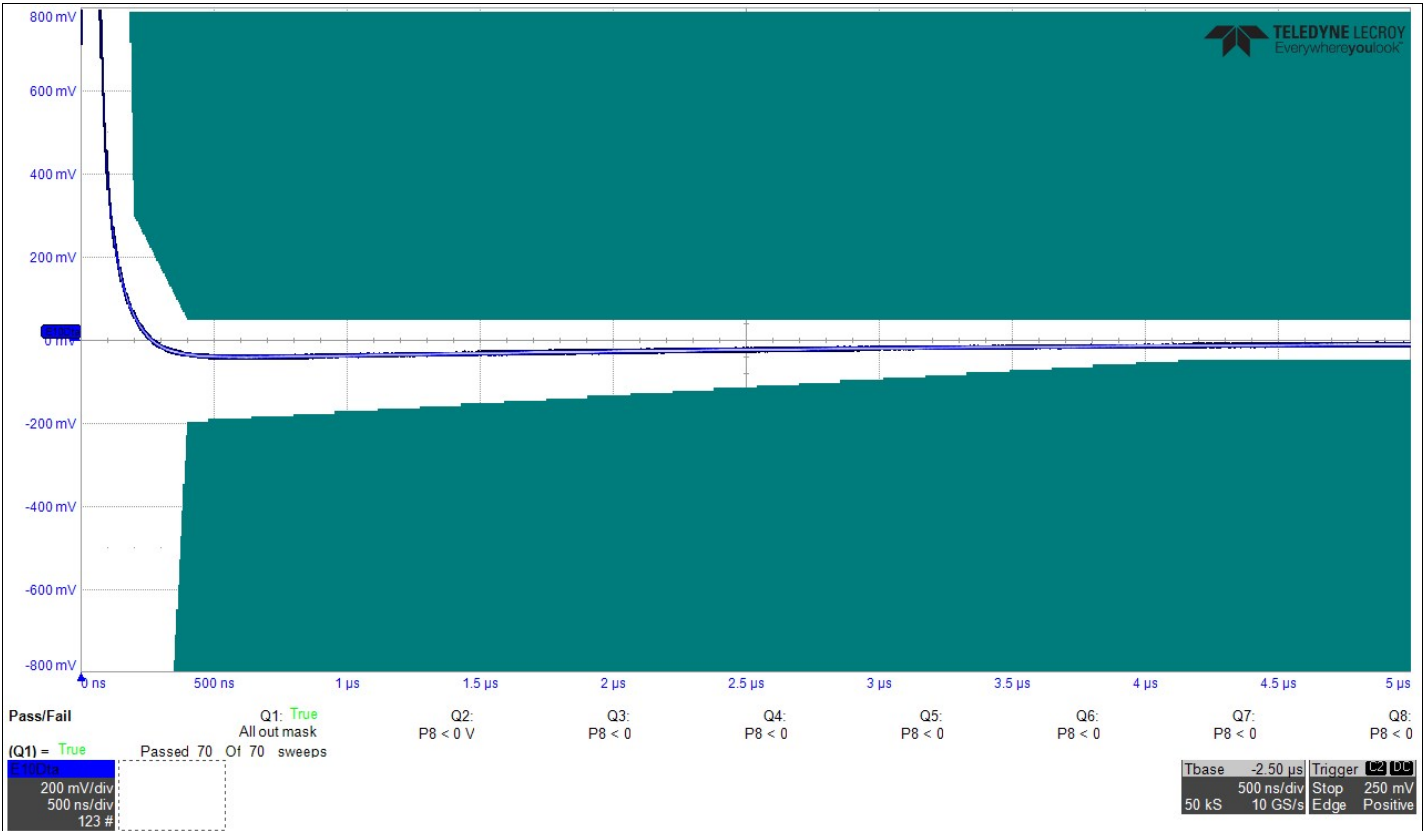


Link Test Pulse Head mask - Load 1, with TPM
Timestamp: 10/29/2019 10:28:43

Test 14.3.1.2.1 - DOV Link Test Pulse Tail

[\[Up\]](#)


 Pass	Measurement: Link Pulse Load 1 with TPM (Tail)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:28:47	Limit Name: Mask-Test

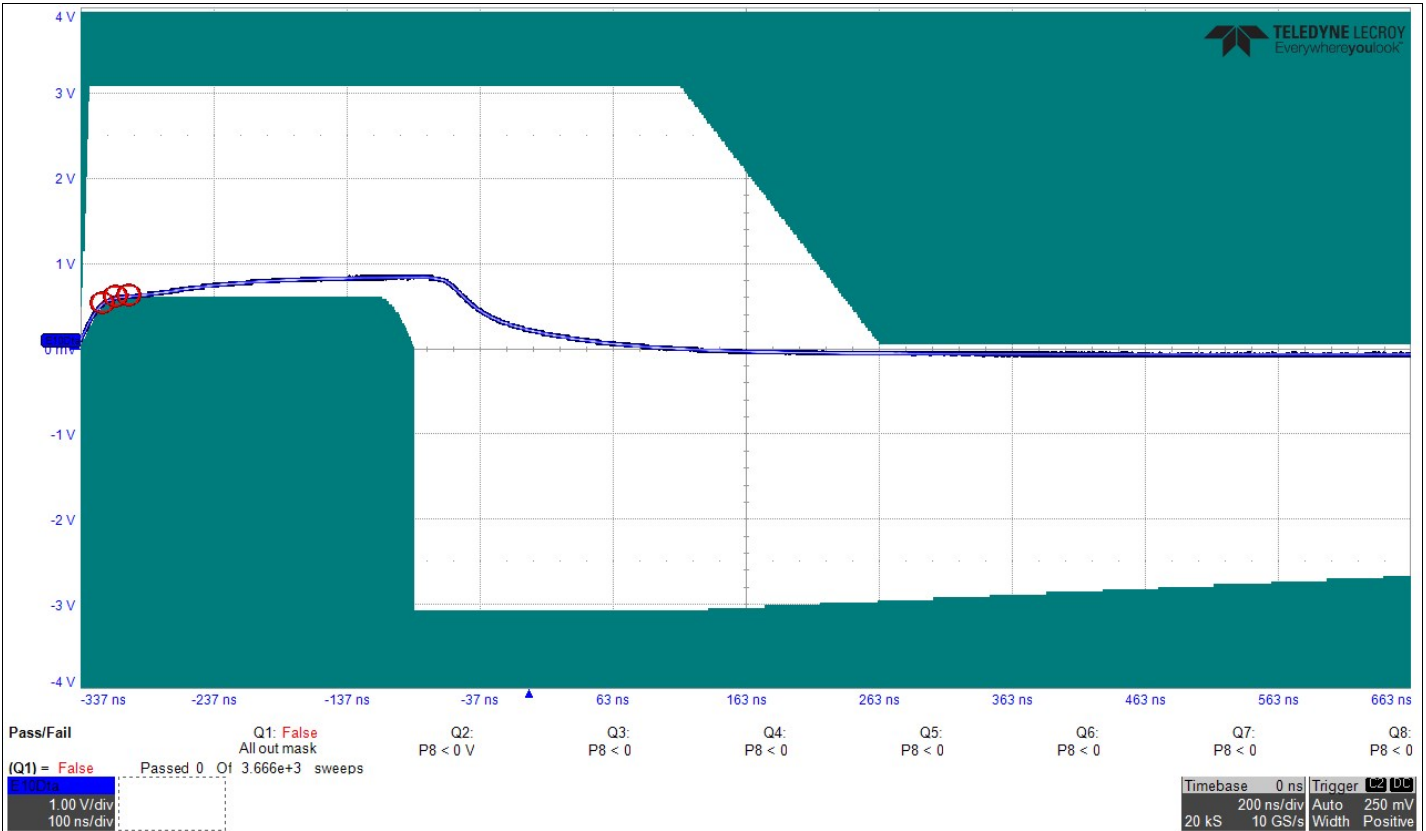


Link Test Pulse Tail mask - Load 1, with TPM
 Timestamp: 10/29/2019 10:28:47

Test 14.3.1.2.1 - DOV TP_IDL Head

[Up]


	Measurement: TP_IDL Load 2 with TPM (Head)	
	Current Value: AllFail	Test Criteria: match
	Timestamp: 10/29/2019 10:29:08	Limit Name: Mask-Test

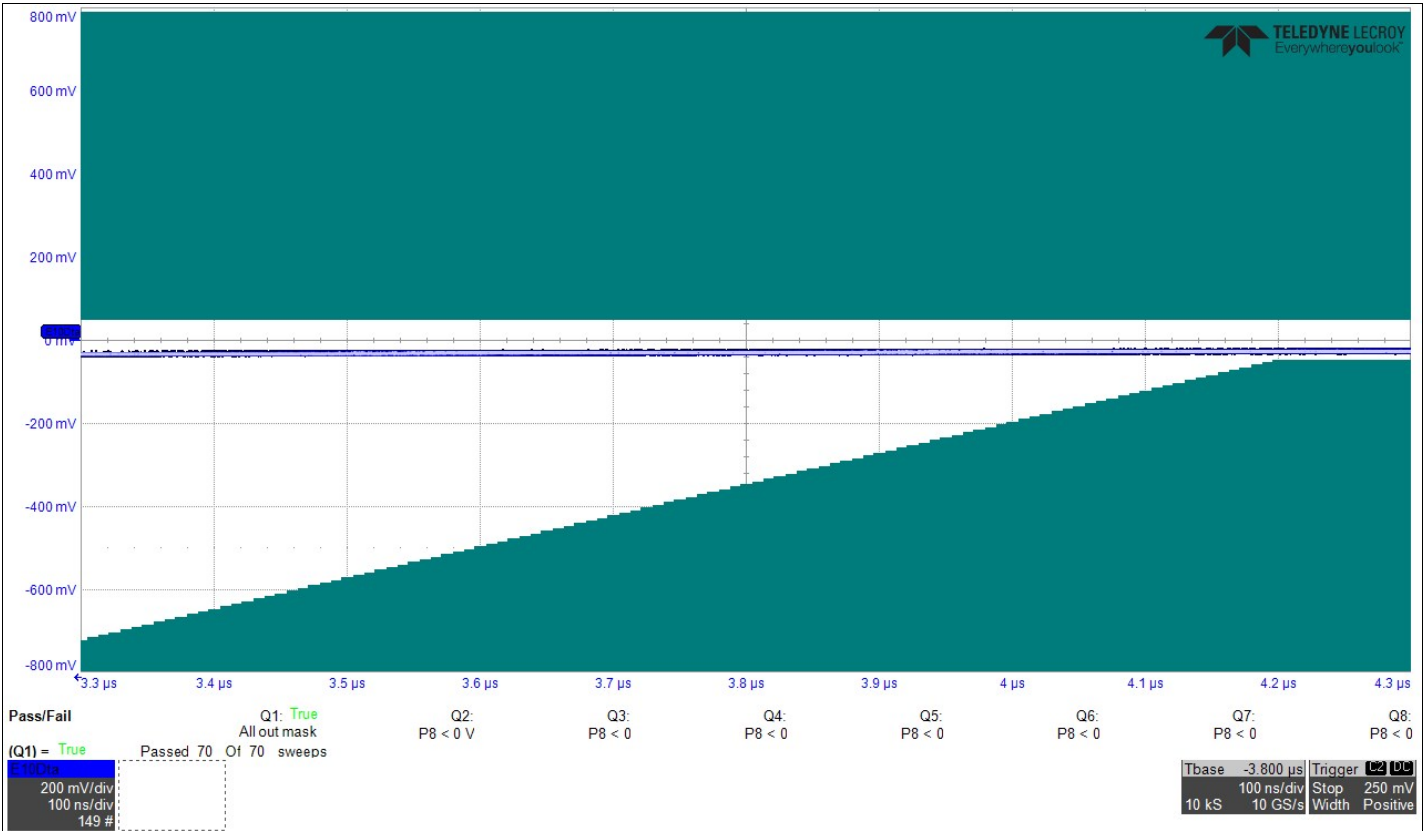


TP_IDL Head mask - Load 2, with TPM
Timestamp: 10/29/2019 10:30:17

Test 14.3.1.2.1 - DOV TP_IDL Tail

[Up]


 Pass	Measurement: TP_IDL Load 2 with TPM (Tail)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:30:20	Limit Name: Mask-Test

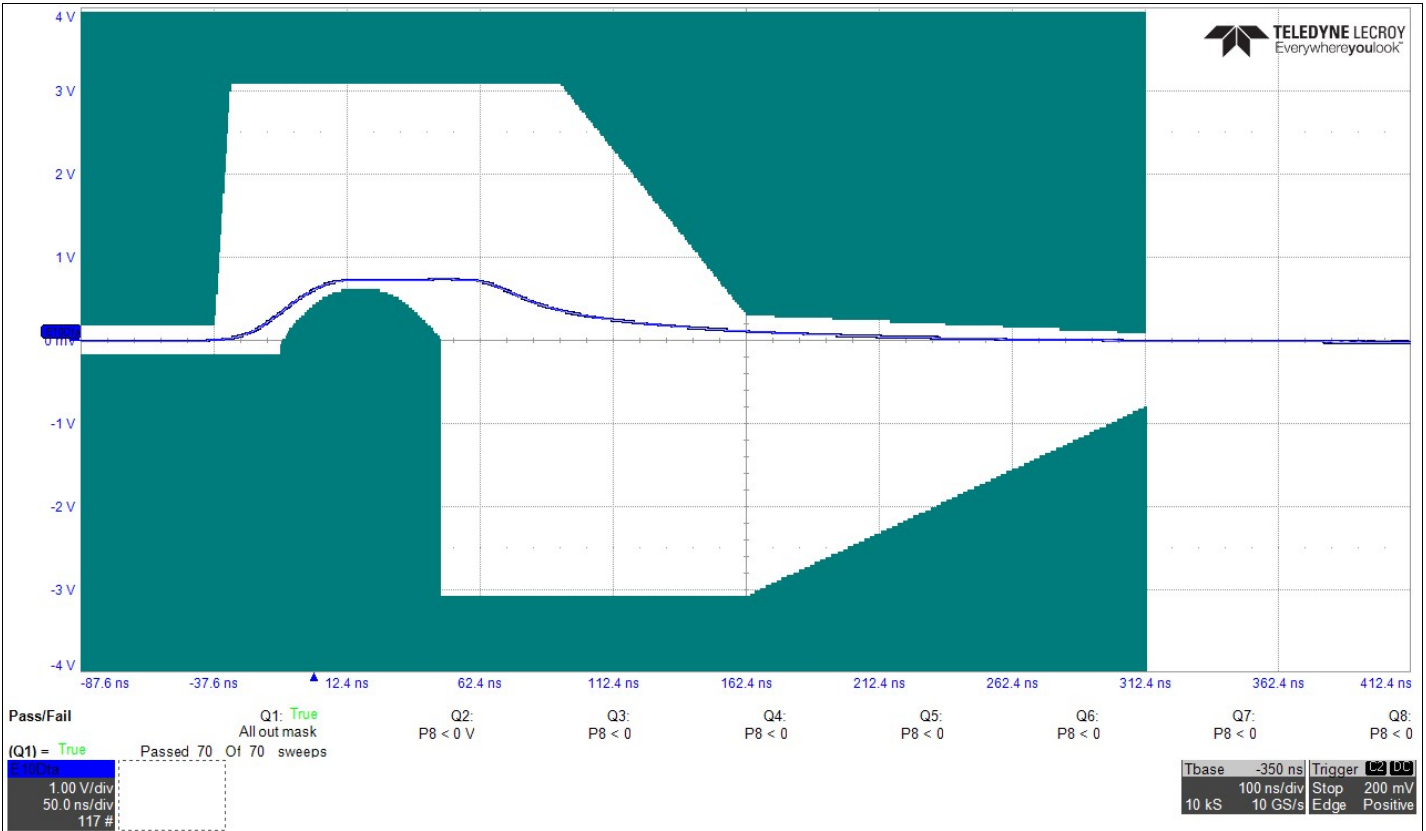


TP_IDL Tail mask - Load 2, with TPM
 Timestamp: 10/29/2019 10:30:21

Test 14.3.1.2.1 - DOV Link Test Pulse Head

[\[Up\]](#)


 Pass	Measurement: Link Pulse Load 2 with TPM (Head)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:30:38	Limit Name: Mask-Test

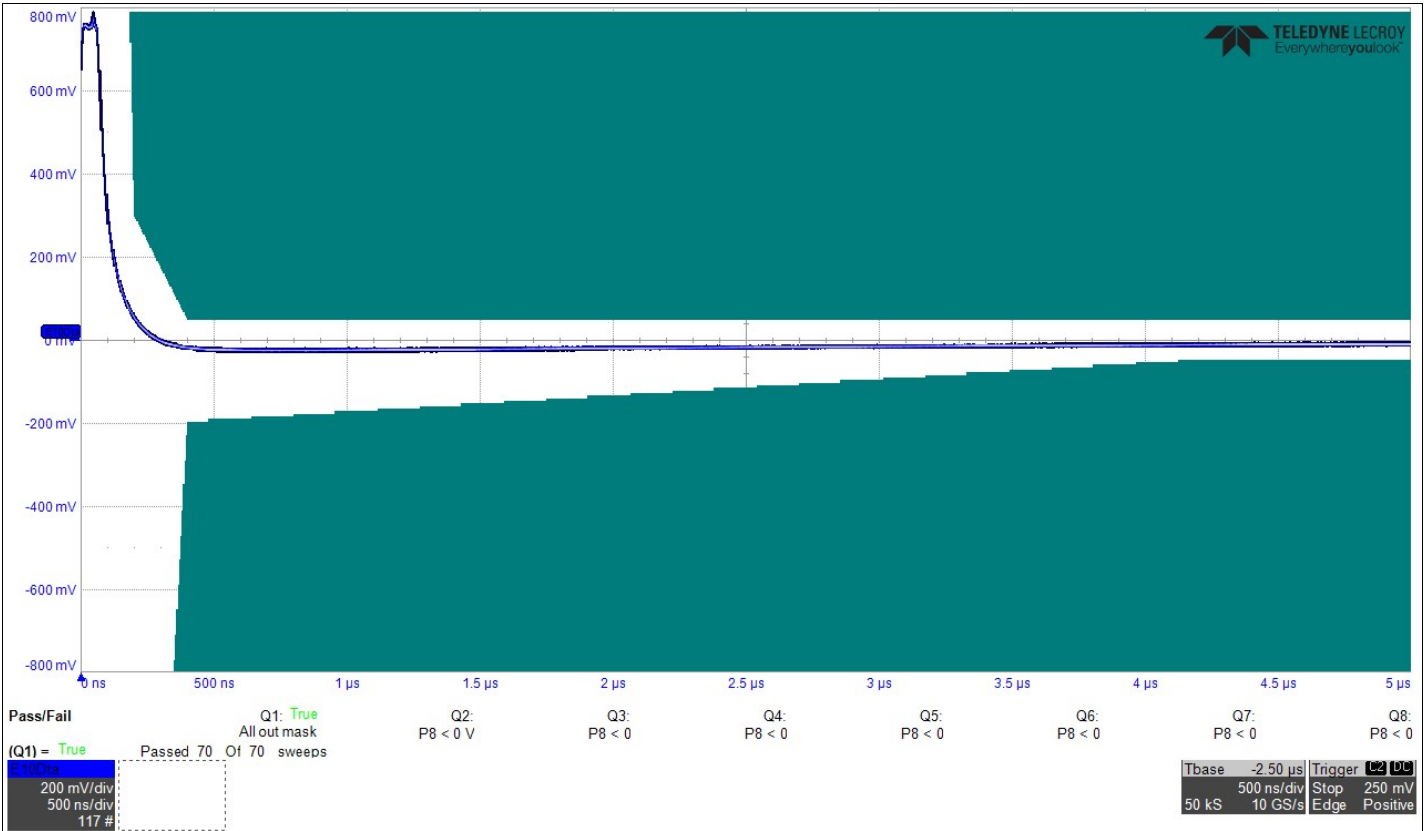


Link Test Pulse Head mask - Load 2, with TPM
 Timestamp: 10/29/2019 10:30:38

Test 14.3.1.2.1 - DOV Link Test Pulse Tail

[\[Up\]](#)


 Pass	Measurement: Link Pulse Load 2 with TPM (Tail)	
	Current Value: AllPass	Test Criteria: match
	Timestamp: 10/29/2019 10:30:42	Limit Name: Mask-Test



Link Test Pulse Tail mask - Load 2, with TPM
Timestamp: 10/29/2019 10:30:42

Test 14.3.1.2.5 - Common-mode output voltage

[\[Up\]](#)

 Pass	Measurement: Common mode output voltage	
	Current Value: 32.3 mV	Test Criteria: $x \leq 50.0$ mV
	Timestamp: 10/29/2019 10:31:11	Limit Name: Common-mode-voltage

--- End of report ---