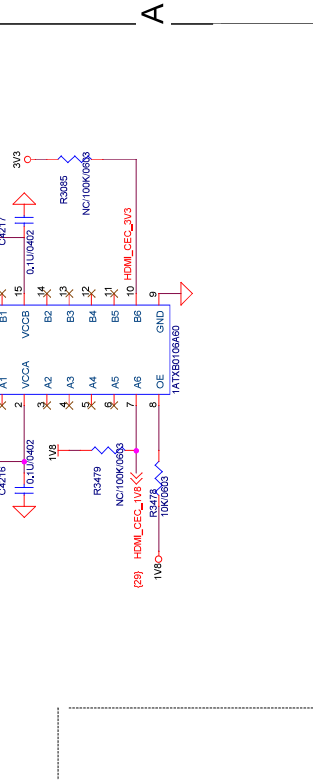
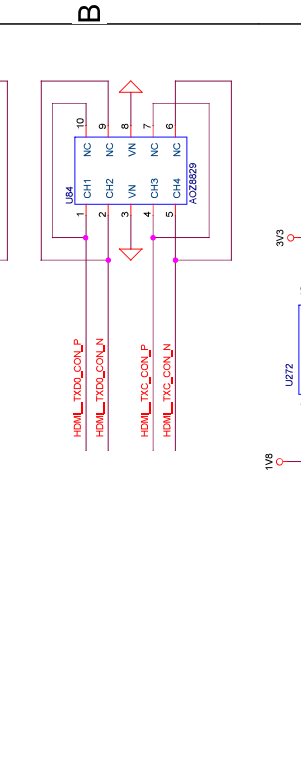
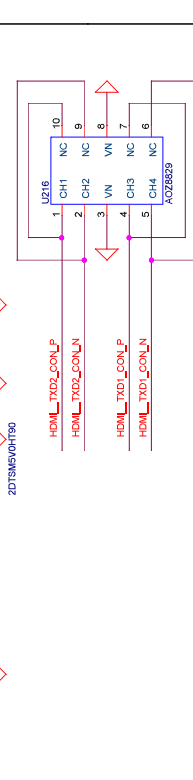
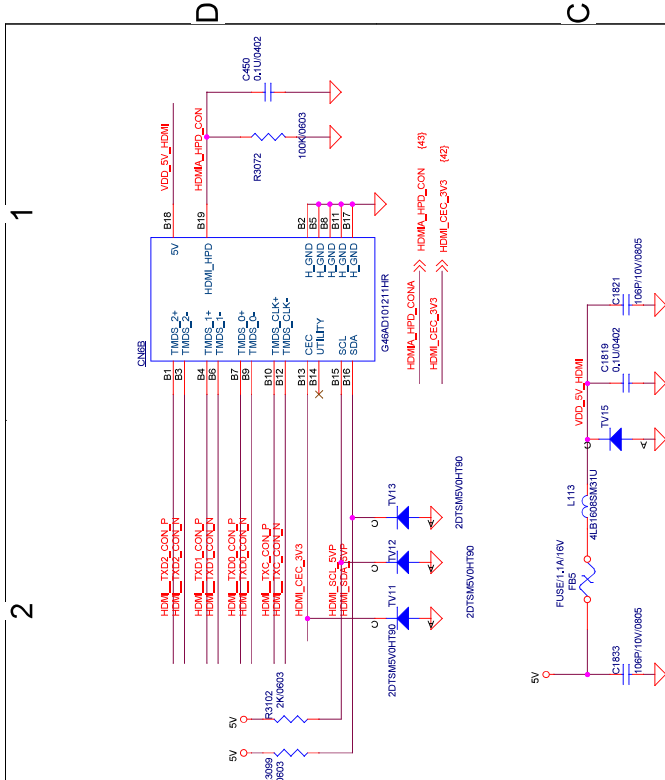
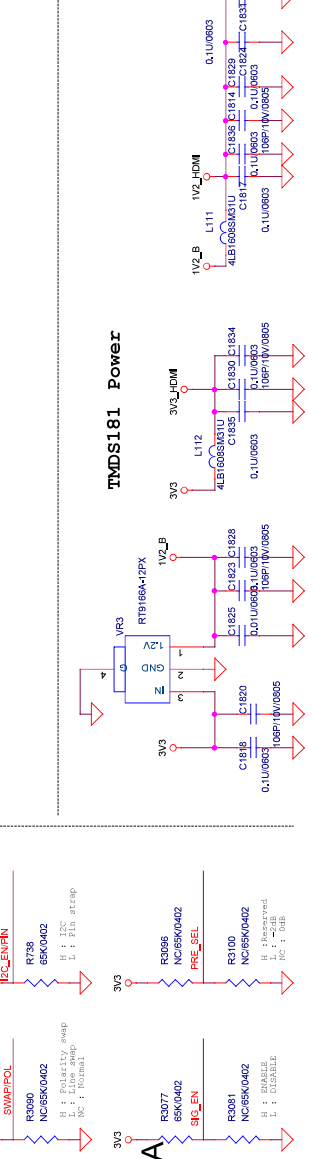
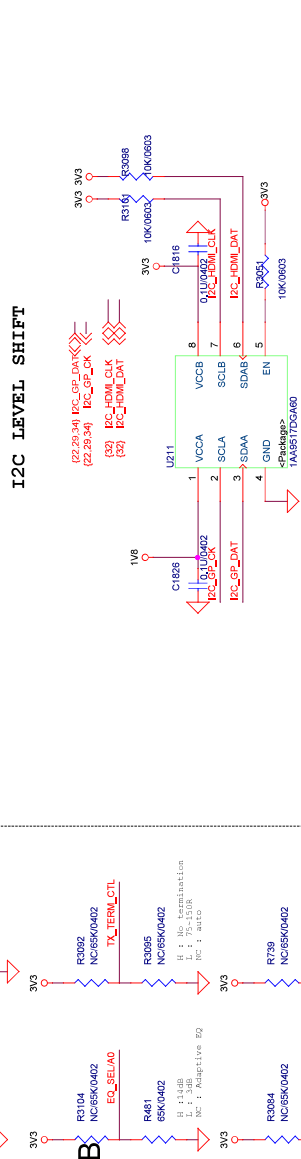
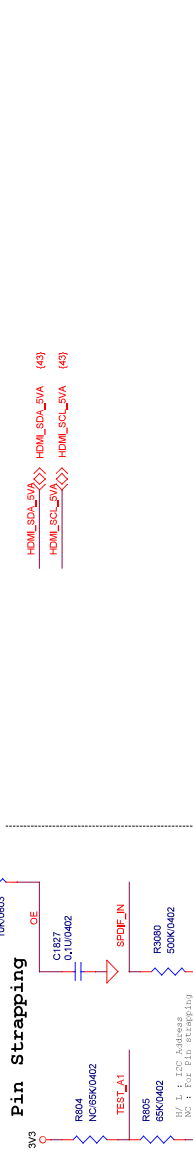
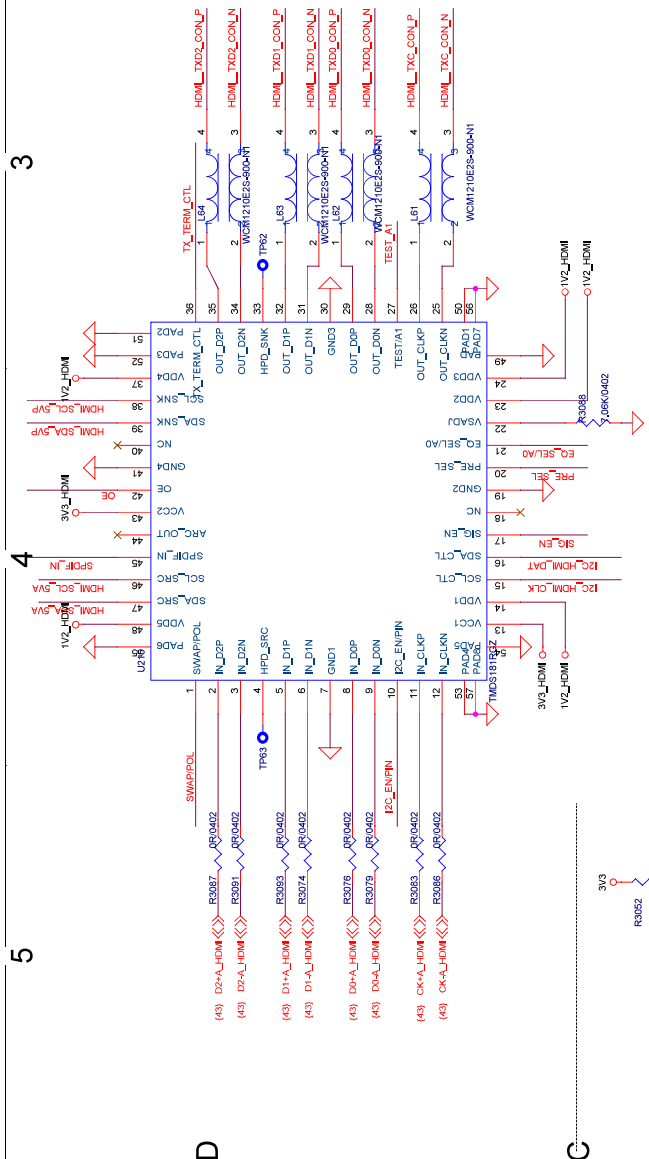




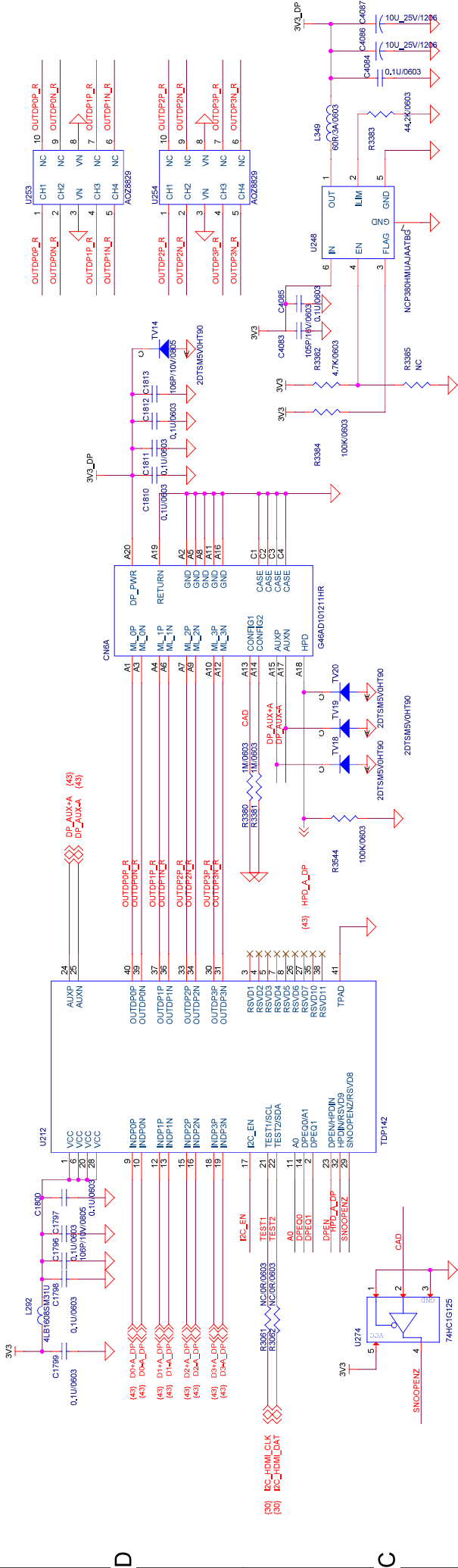
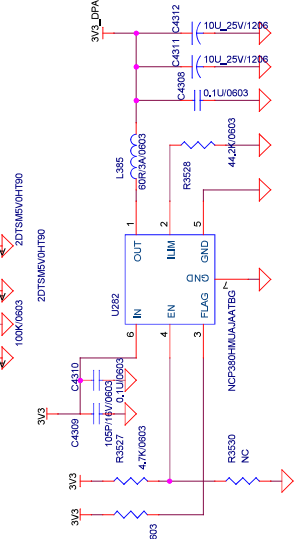
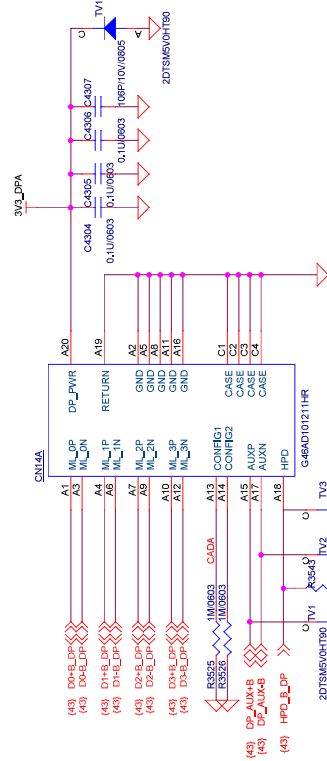
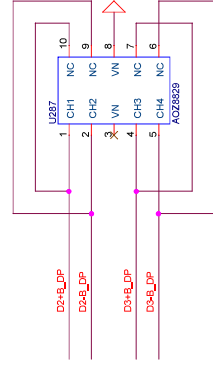
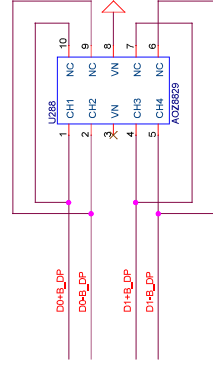
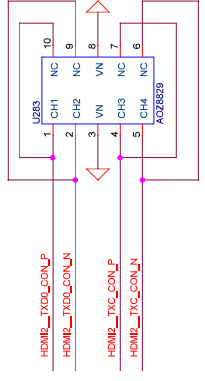
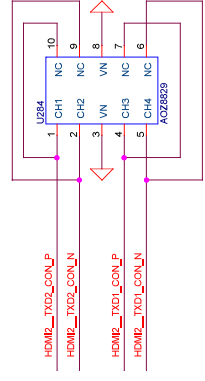
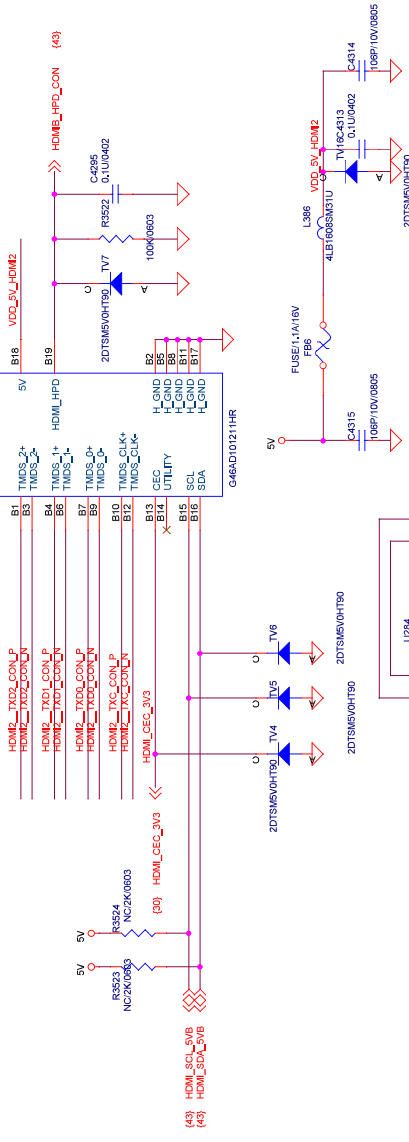
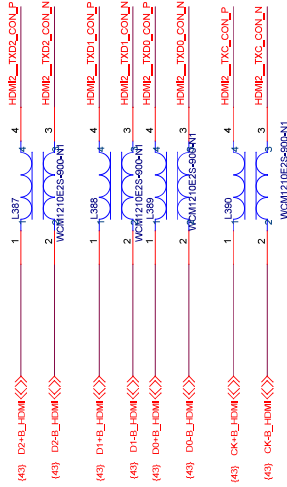


Table 2. TDP142 Receiver Equalization GPIO Control

Equalization Setting #	ALL DISPLAYPORT LANES									
	DPEQ1 PIN LEVEL					DPEQ0 PIN LEVEL				
0	0	0	0	0	0	0	0	0	0	EO GAIN at 4.05 GHz (dB)
1	0	0	0	0	0	R	R	R	R	3.3
2	0	0	0	0	0	F	F	F	F	4.9
3	0	0	0	0	0	1	1	1	1	8.5
4	R	R	R	R	R	0	0	0	0	7.5
5	R	R	R	R	R	R	R	R	R	8.6
6	R	R	R	R	R	F	F	F	F	9.5
7	R	R	R	R	R	1	1	1	1	10.4
8	F	F	F	F	F	0	0	0	0	11.1
9	F	F	F	F	F	R	R	R	R	11.7
10	F	F	F	F	F	F	F	F	F	12.3
11	F	F	F	F	F	1	1	1	1	12.8
12	1	1	1	1	1	0	0	0	0	13.2
13	1	1	1	1	1	R	R	R	R	13.6
14	1	1	1	1	1	F	F	F	F	14.0
15	1	1	1	1	1	1	1	1	1	14.4

Table 1. 4-Level Control Pin Settings

LEVEL	SETTINGS
0	Option 1: Tie 1 kΩ 5% to GND. Option 2: Tie directly to GND.
R	Tie 20 kΩ 5% to GND.
F	Float (leave pin open)
1	Option 1: Tie 1 kΩ 5% to V _{CC} . Option 2: Tie directly to V _{CC} .



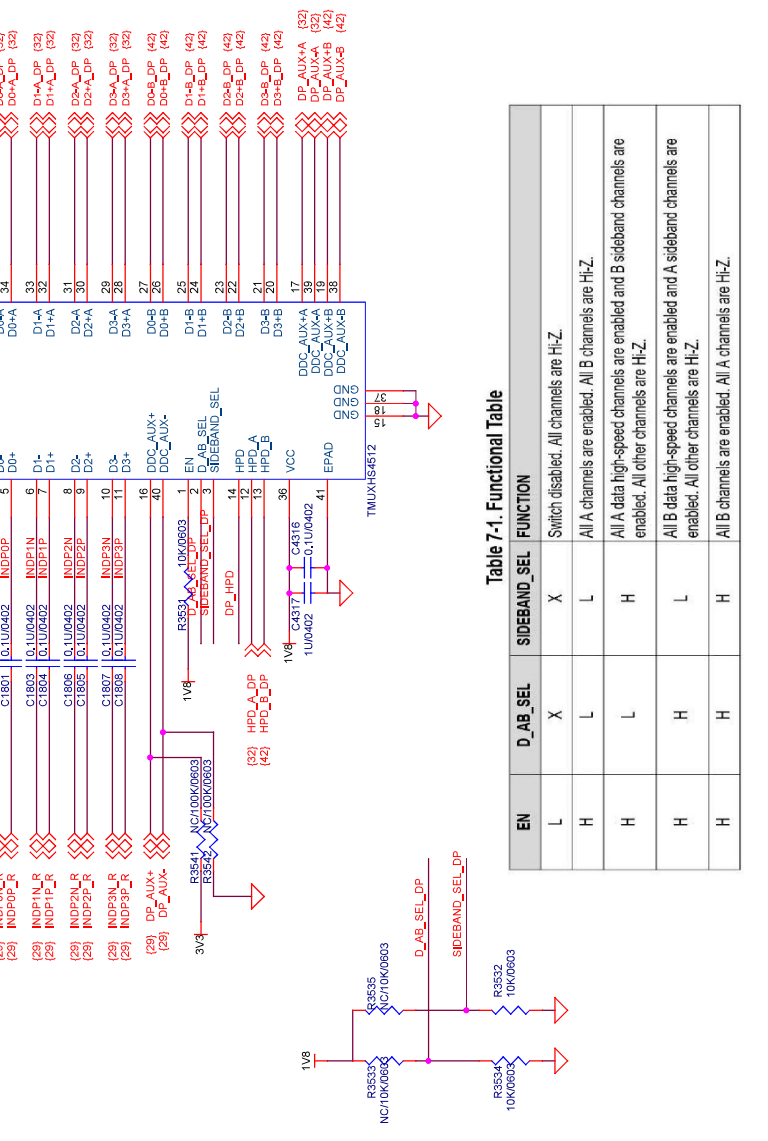
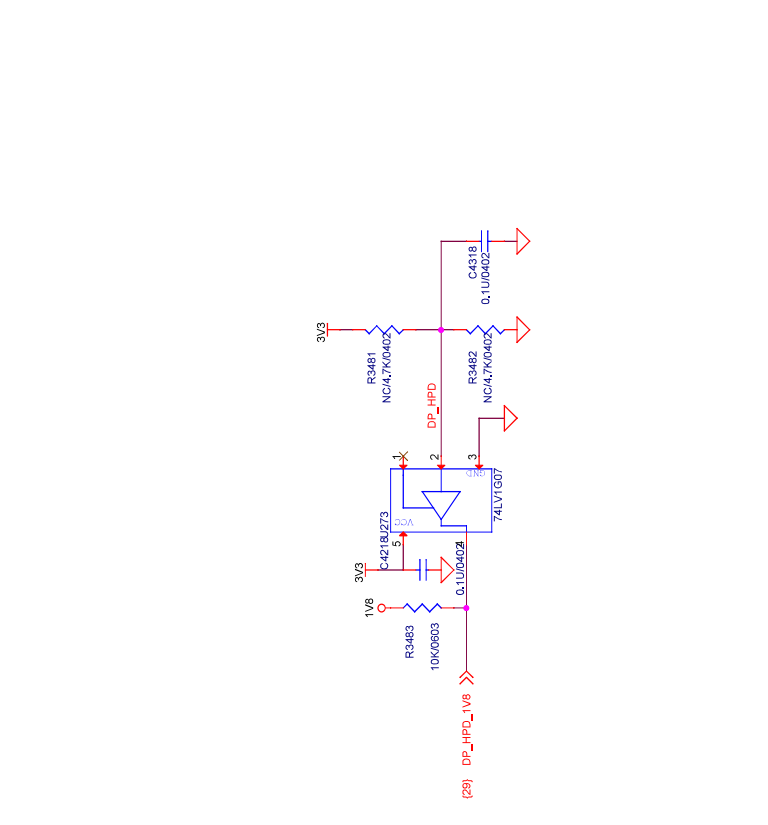
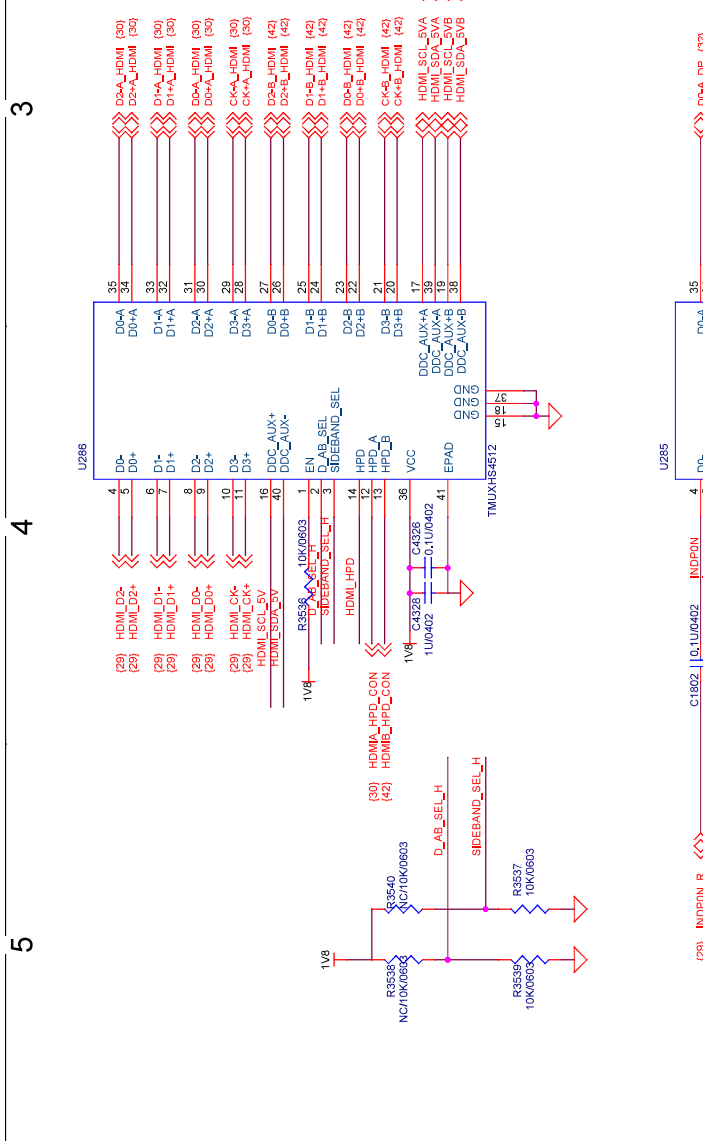
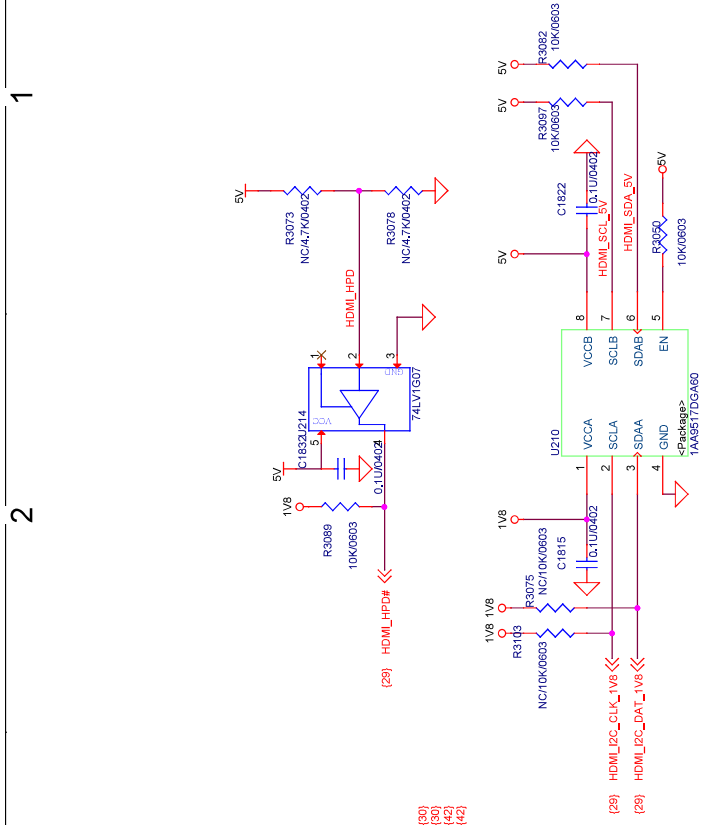


Table 7-1. Functional Table

EN	D_AB_SEL	SIDEBAND_SEL	FUNCTION
L	X	X	Switch disabled. All channels are Hi-Z.
H	L	L	All A channels are enabled. All B channels are Hi-Z.
H	L	H	All A data high-speed channels are enabled and B sideband channels are enabled. All other channels are Hi-Z.
H	H	L	All B data high-speed channels are enabled and A sideband channels are enabled. All other channels are Hi-Z.
H	H	H	All B channels are enabled. All A channels are Hi-Z.