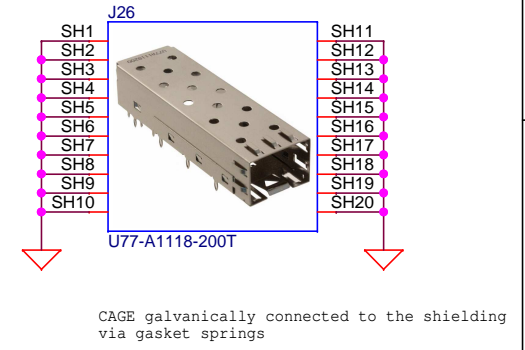
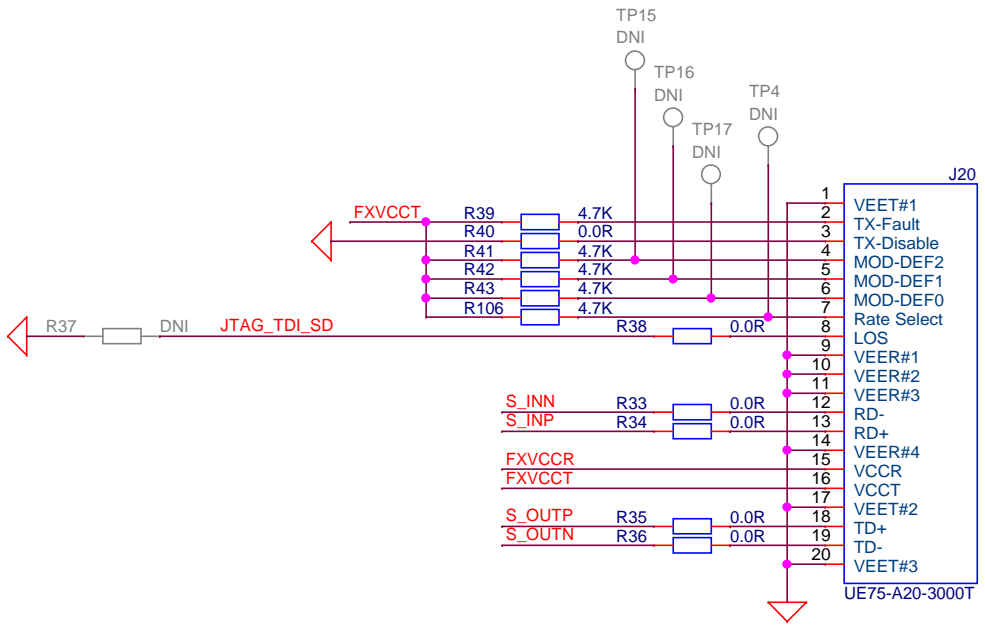


4 level strap config
PHY ADDR 0 - for MDIO interface
RXD_0, RXD_1 = 00 (OPEN)

2 level strap config
RGMII-T0-1000BASE-X
JTAG_TDO - HIGH
RXD2 - LOW (OPEN)
RXD3 - LOW (OPEN)
LED1 - LOW - autonegotiation enabled
RX_CLK - LOW (OPEN) Not used in this mode

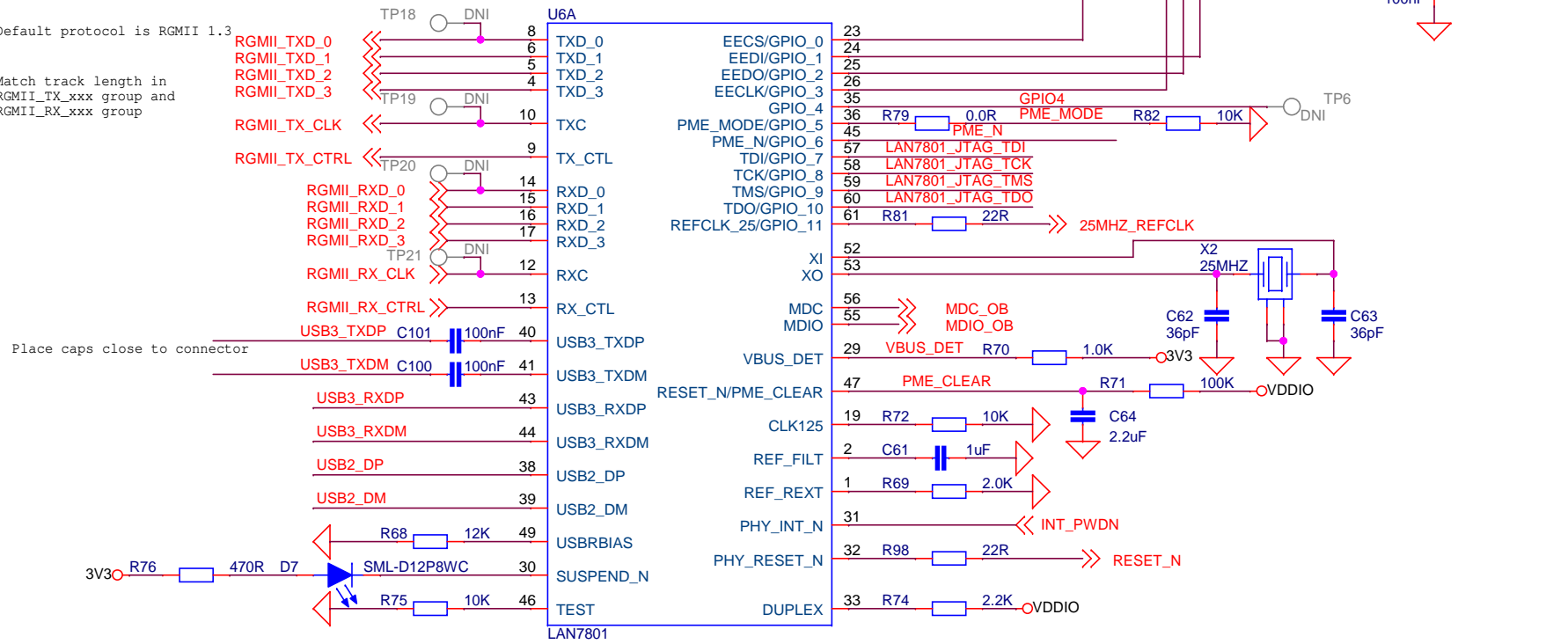
Note: The other side must be set to auto-negotiation, 1000BASE-FX available



USB 3.1 Gen 1 to 10/100/1000 Ethernet Controller

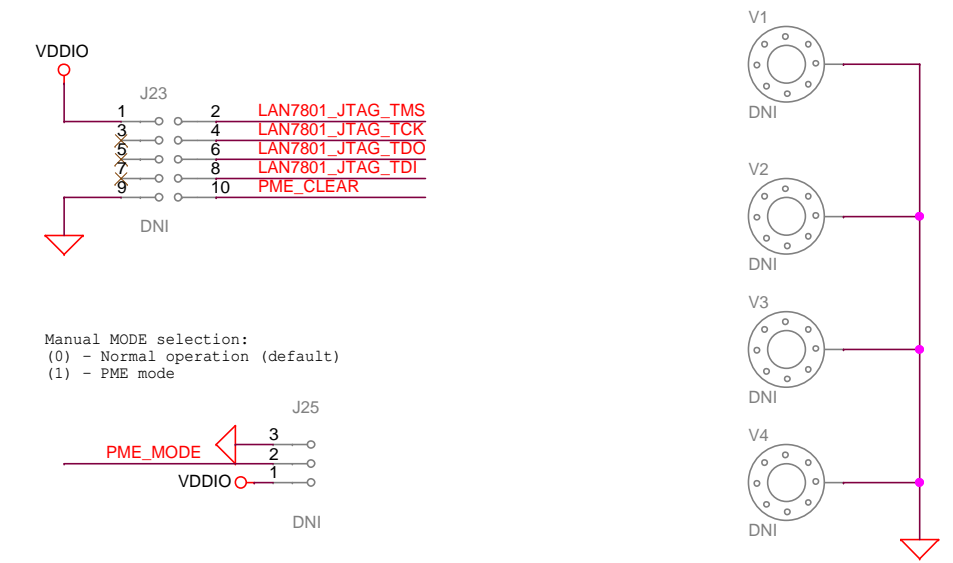
for PCB layout application notes
see LAN7801 datasheet

Default protocol is RGMII 1.3
Match track length in RGMII_TX_XXX group and RGMII_RX_XXX group



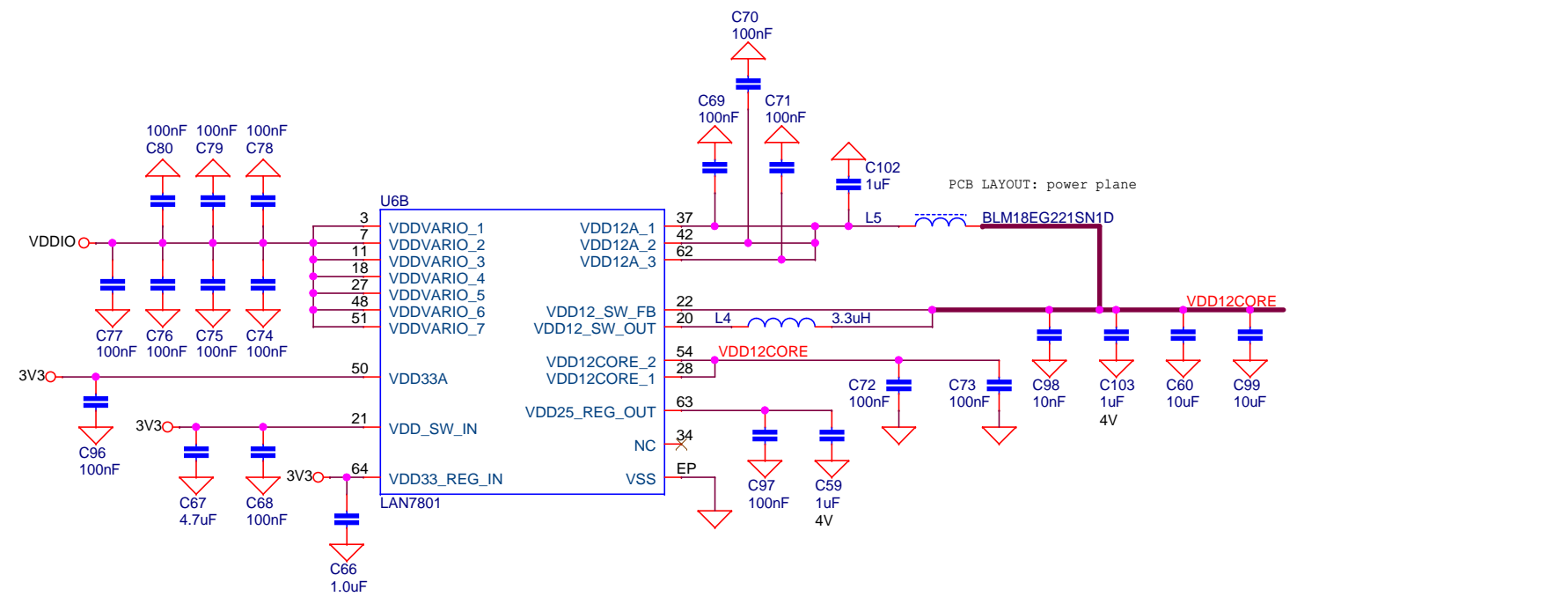
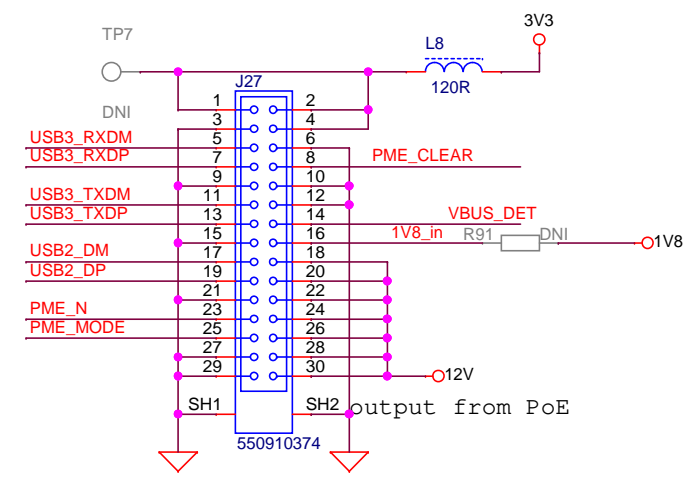
Place caps close to connector

keep r=2.5mm route keepout!



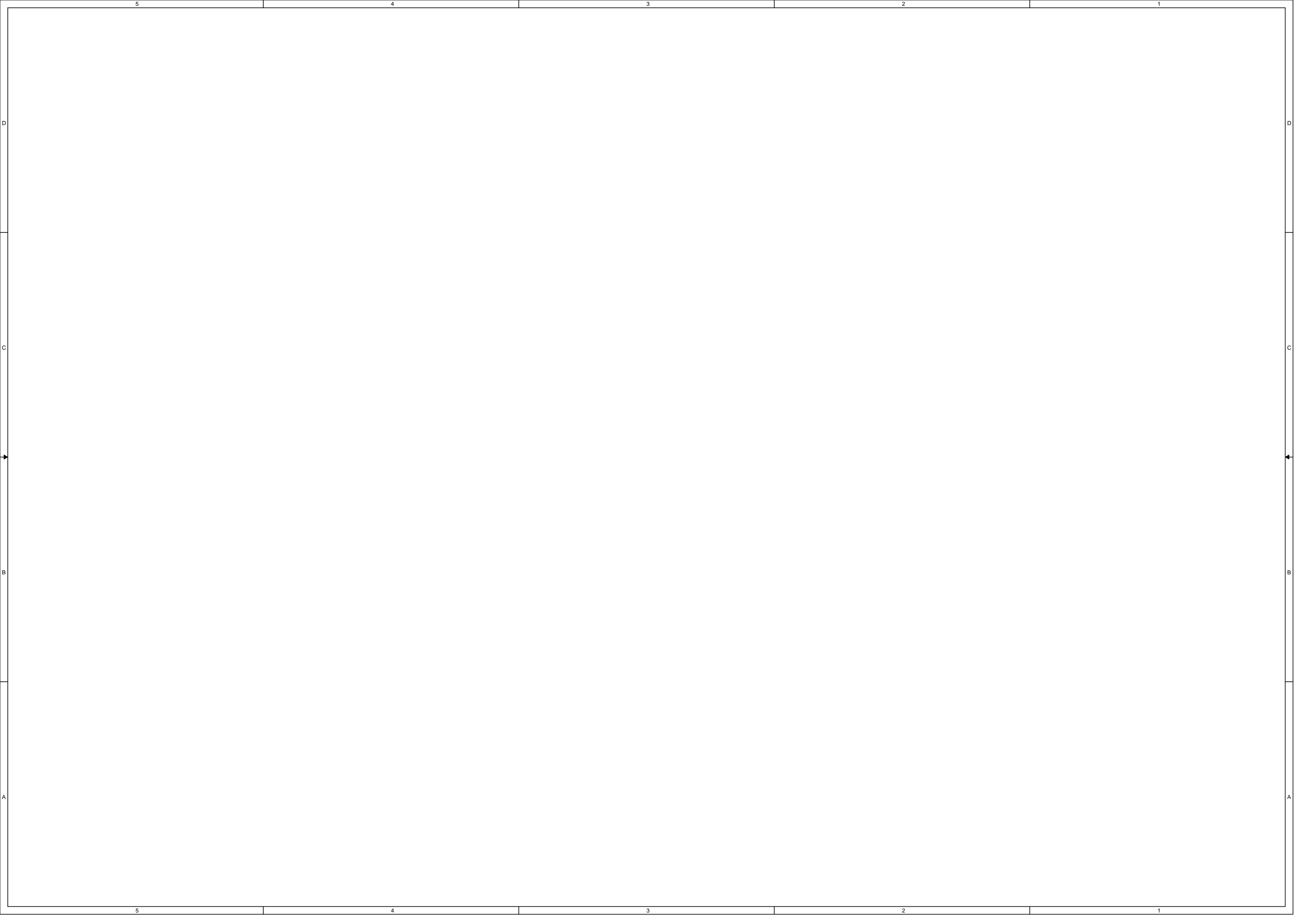
Manual MODE selection:
(0) - Normal operation (default)
(1) - PME mode

CPU board connector

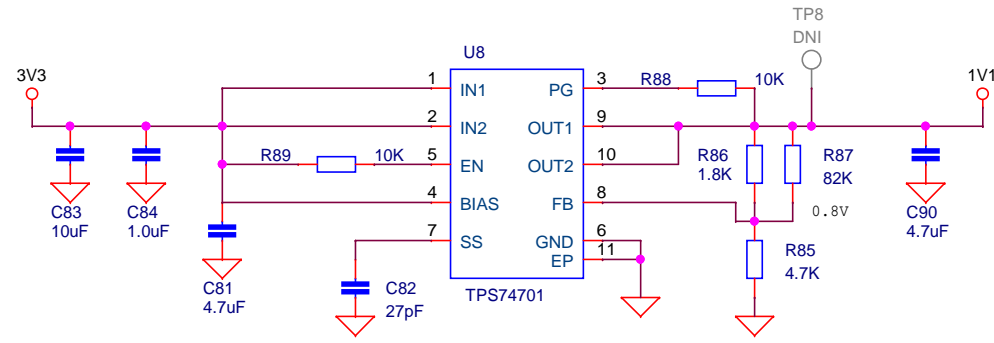


PCB LAYOUT: power plane

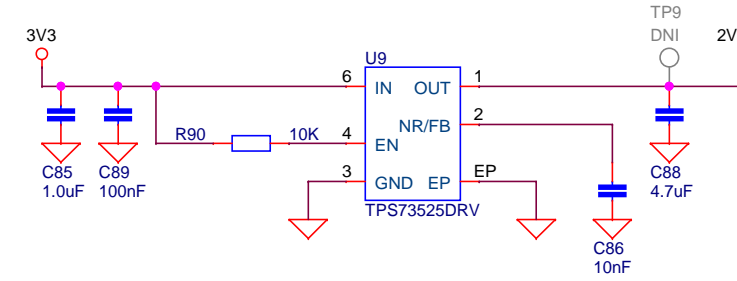
output from PoE



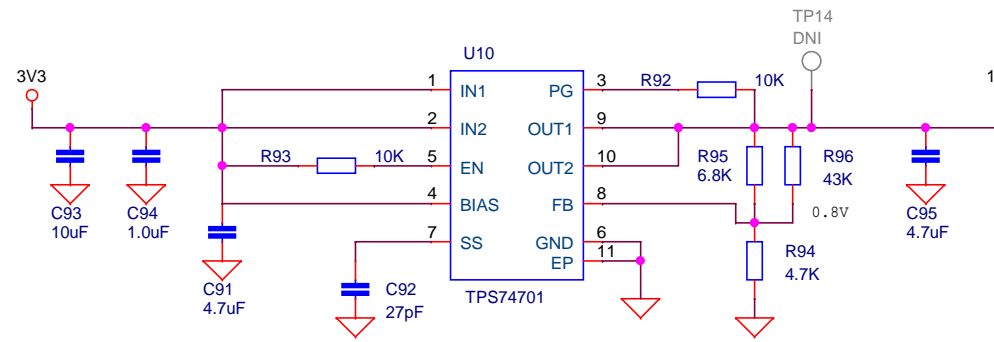
LDO 1.1V



LDO 2.5V



LDO 1.8V



Buck 1.8V (option)

