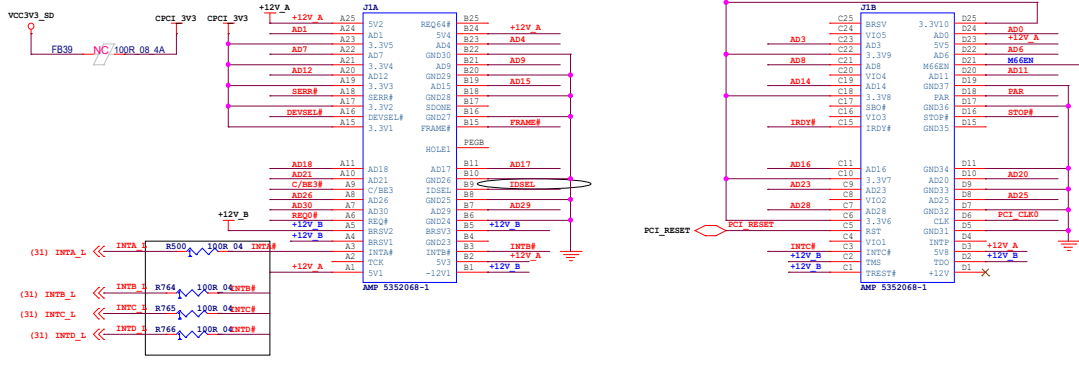
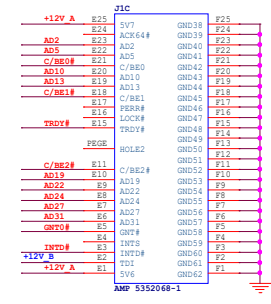


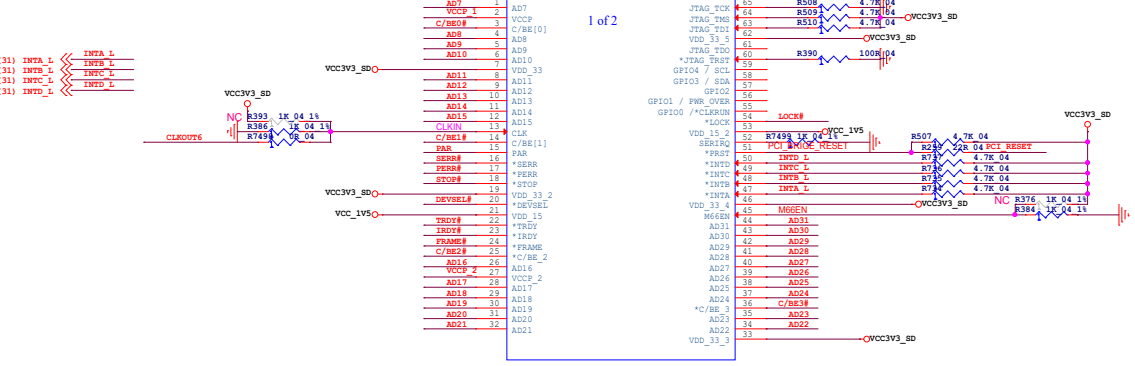
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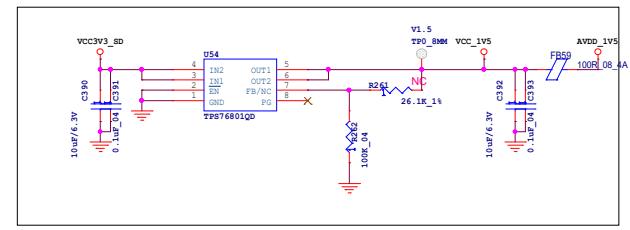
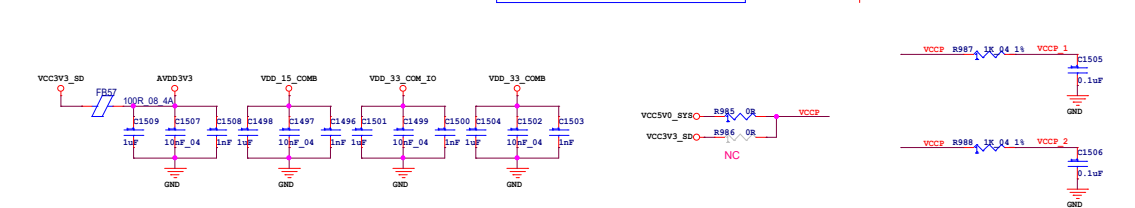
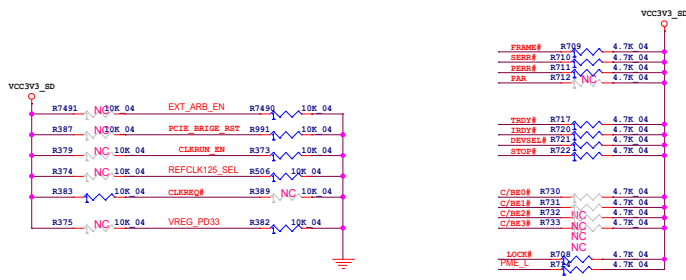
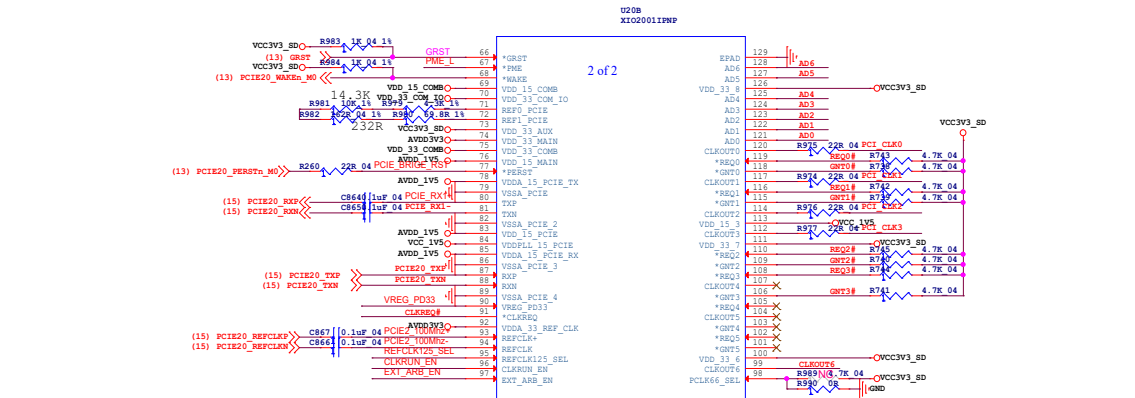
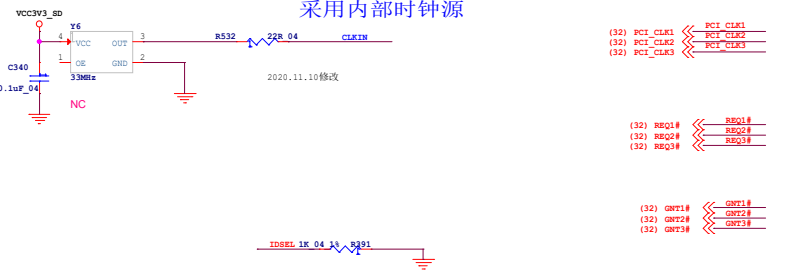
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PCIE TO PCI BRIDGE



采用内部时钟源



Title	<Title>	Rev	<RevCode>
Size	Document Number		
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Date	Friday, March 27, 2026	Sheet	31 of 37