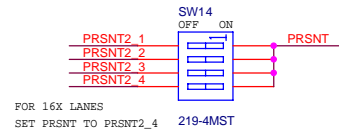
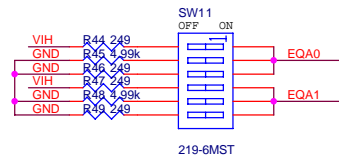
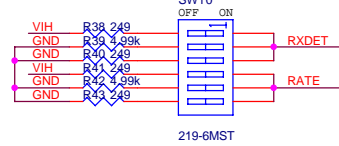
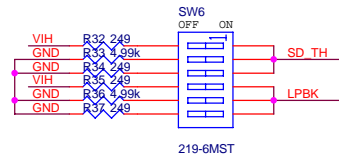
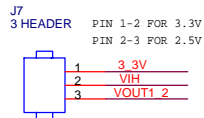
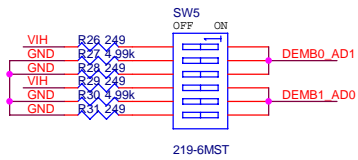
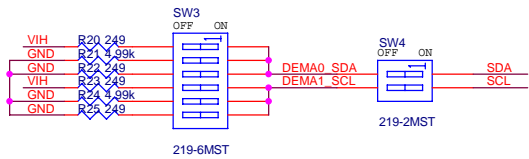
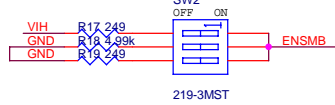
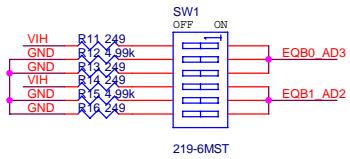


EQB0_AD3 >> EQB0_AD3
EQB1_AD2 >> EQB1_AD2
ENSMB >> ENSMB
DEMA0_SDA >> DEMA0_SDA
DEMA1_SCL >> DEMA1_SCL
PRSN1 >> PRSN1
DEMB0_AD1 >> DEMB0_AD1
DEMB1_AD0 >> DEMB1_AD0
EQA1 >> EQA1
EQA0 >> EQA0
RATE >> RATE
RXDET >> RXDET
LPBK >> LPBK
VDD_SEL1_2 >> VDD_SEL1_2
VDD_SEL3_4 >> VDD_SEL3_4
VOUT1_2 >> VOUT1_2
3.3V >> 3.3V

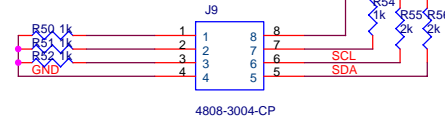
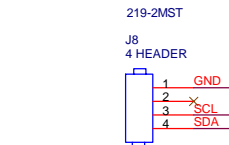
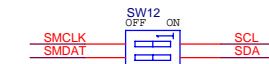
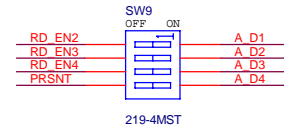
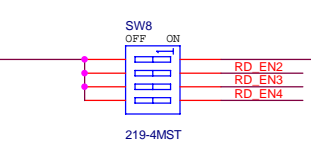
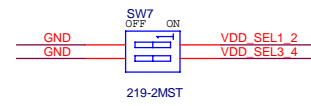
SMCLK >> SMCLK
SMDAT >> SMDAT
SD_TH >> SD_TH
RD_EN2 >> RD_EN2
RD_EN3 >> RD_EN3
RD_EN4 >> RD_EN4
A_D1 >> A_D1
A_D2 >> A_D2
A_D3 >> A_D3
A_D4 >> A_D4

PRSN2_1 >> PRSN2_1
PRSN2_2 >> PRSN2_2
PRSN2_3 >> PRSN2_3
PRSN2_4 >> PRSN2_4

PLACE ALL Rs CLOSE TO SW
SW IN THE OFF POSITION = OPEN



FOR 16X LANES
SET PRSN1 TO PRSN2_4



PIN MODE SETTINGS:
SW1 - EQB0, EQB1
SW2 - ENSMB = 1K TO GND
SW3 - DEMA0, DEMA1
SW4 - OFF POSITION
SW5 - DEMB0, DEMB1
SW6 - SD_TH, LPBK
SW7 - ON (GND) FOR 3.3V
SW8 - RD_EN2-4=ON (SD_TH)
SW9 - OFF POSITION
SW10 - RXDET, RATE
SW11 - EQA0, EQA1
SW12 - OFF POSITION
SW13 - OFF POSITION
SW14 - PRSN2_4=ON

SMBUS SLAVE MODE SETTINGS:
SW1 - AD3, AD2
SW2 - ENSMB = 1K TO VDD
SW3 - OFF POSITION
SW4 - ON POSITION (SDA, SCL)
SW5 - AD1, AD0
SW6 - SD_TH, RES
SW7 - ON (GND) FOR 3.3V
SW8 - RD_EN2-4=ON (SD_TH)
SW9 - OFF POSITION
SW10 - RXDET, RATE
SW11 - EQA0, EQA1
SW12 - OFF POSITION WHEN USING SPA BOARD
SW13 - OFF POSITION
SW14 - PRSN2_4=ON

SMBUS MASTER (READ EEPROM) MODE SETTINGS:
SW1 - AD3, AD2
SW2 - ENSMB = FLOAT
SW3 - OFF POSITION
SW4 - ON POSITION (SDA, SCL)
SW5 - AD1, AD0
SW6 - OFF POSITION
SW7 - ON (GND) FOR 3.3V
SW8 - SD_TH=ON, RD_EN2-4=OFF
SW9 - ON POSITION (ALL_DONE TO RD_EN, ALL_DONE4 TO PRSN1)
SW10 - RXDET, RATE
SW11 - EQA0, EQA1
SW12 - OFF POSITION
SW13 - GND TO START THE READ PROCESS
SW14 - OFF POSITION

Title		
PCIE 16X_402EVK		
Size B	Document Number DS80PC1402 PCIE 16X CARD PAGE 2	Rev A
Date:	Thursday, January 06, 2011	Sheet 1 of 1