

# Possible PCIe interconnections

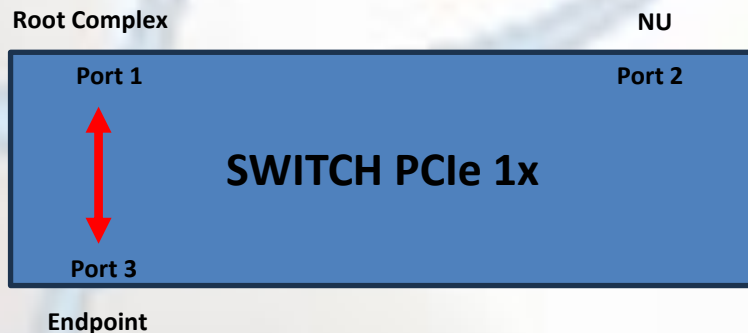
In my design, I have three PCIe entity interconnected: this PCIe ports works with different configurations.

The system sets the configuration only at the power up. At each power up, the PCIe ports can be interconnected in Config 1 or 2 (see below).

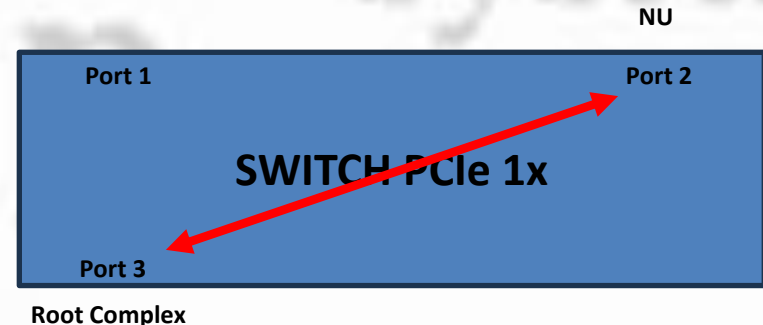
To implement this function, I'll use the TS2PCIE412. The next slides show the TS2PCIE412 interconnection in the two different Config.

Only a bank is used.

**Config. 1**



**Config. 2**

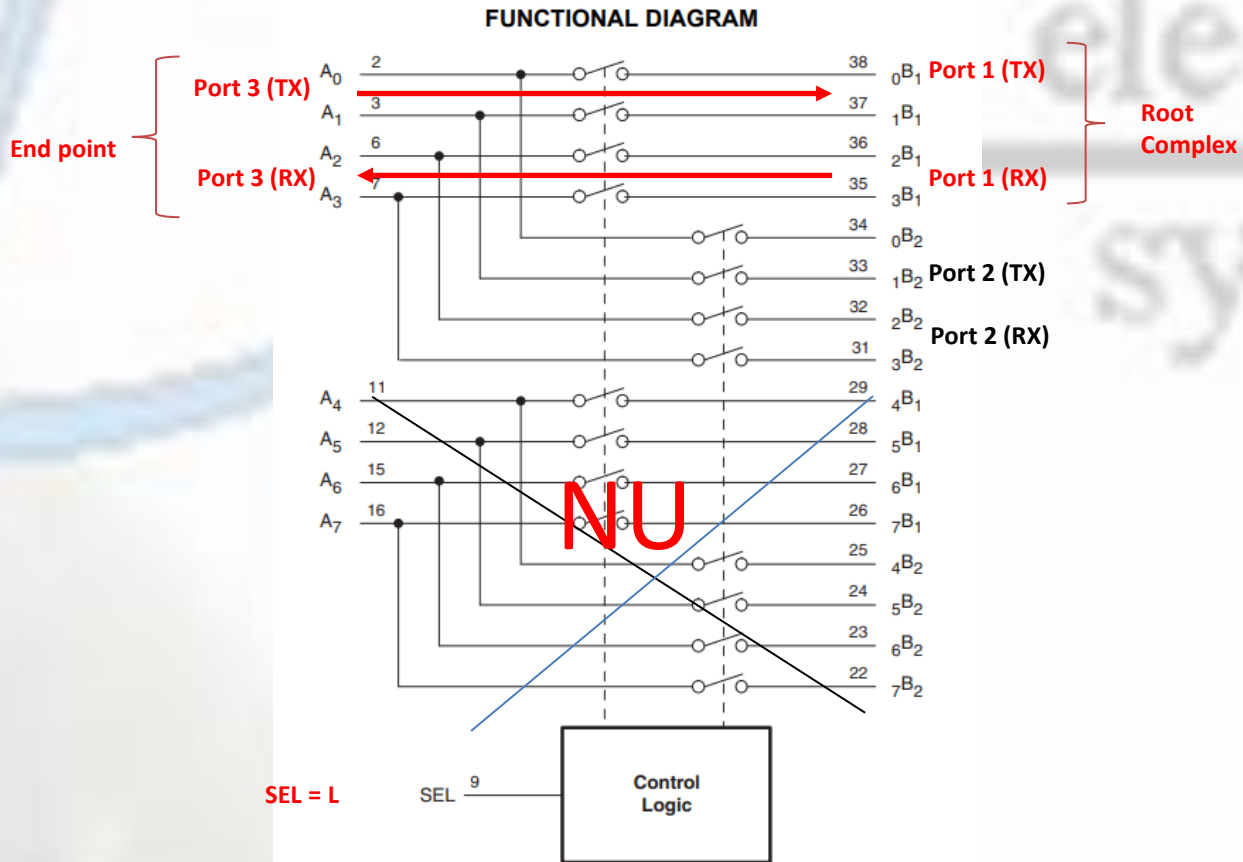


# Configuration 1 with TS2PCIE412

## Config. 1

Root Complex

NU



# Configuration 2 with TS2PCIE412

