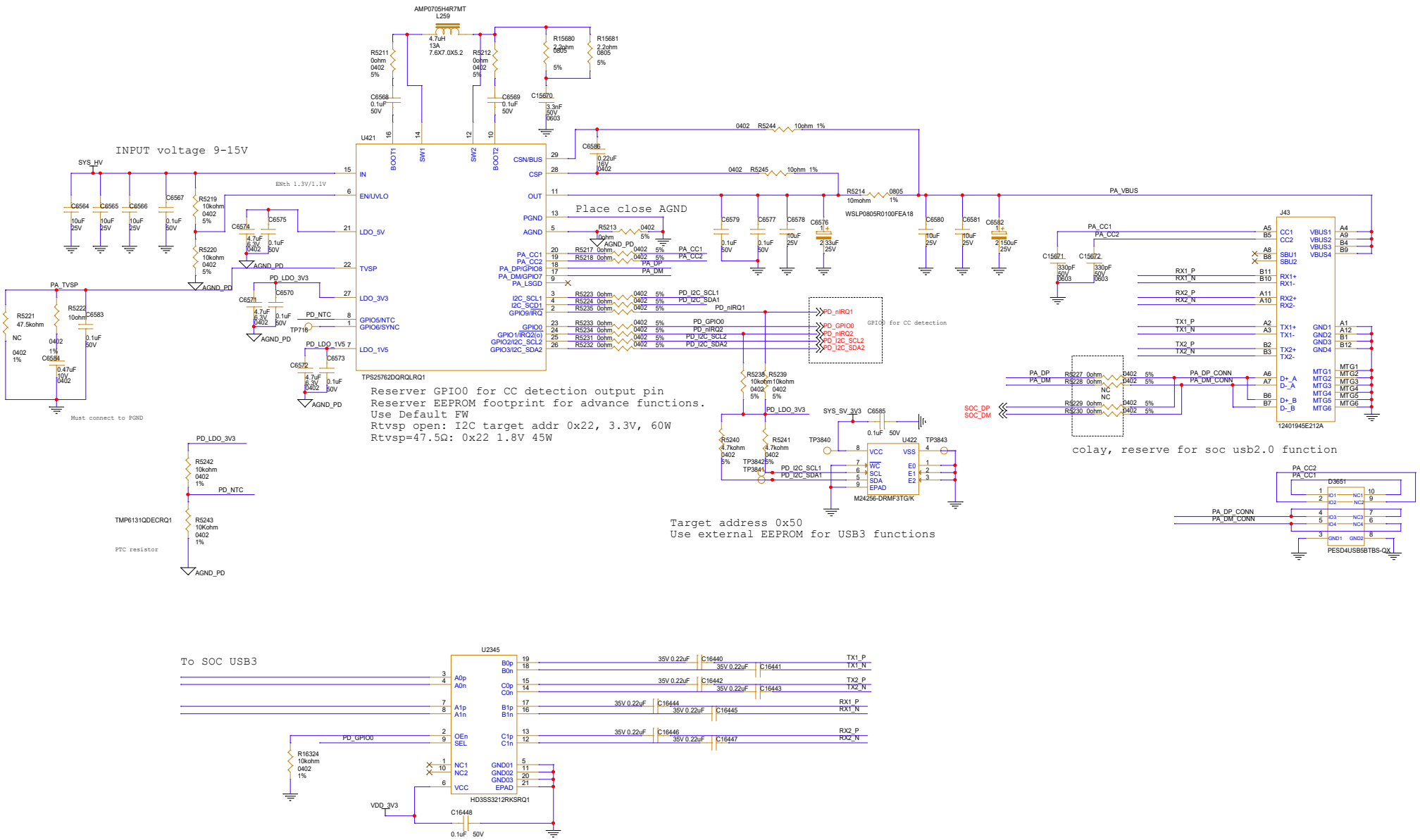
USB3 + PD


INPUT voltage 9-15V
SYS_HV
ENth 1.3V/1.1V
Must connect to PGND
U421
TPS25762DQRQLRQ1
Place close AGND
Reserver GPIO0 for CC detection output pin
Reserver EEPROM footprint for advance functions.
Use Default FW
Rtvsp open: I2C target addr 0x22, 3.3V, 60W
Rtvsp=47.5Ω: 0x22 1.8V 45W
WSLP0805R0100FEA18
PA_VBUS
J43
12401945E212A
GPIO0 for CC detection
colay, reserve for soc usb2.0 function
M24256-DRMF3TG/K
Target address 0x50
Use external EEPROM for USB3 functions
TMP6131QDECRQ1
PTC resistor
PESD4USB5BTBS-QX
To SOC USB3
HD3SS3212RKSRQ1