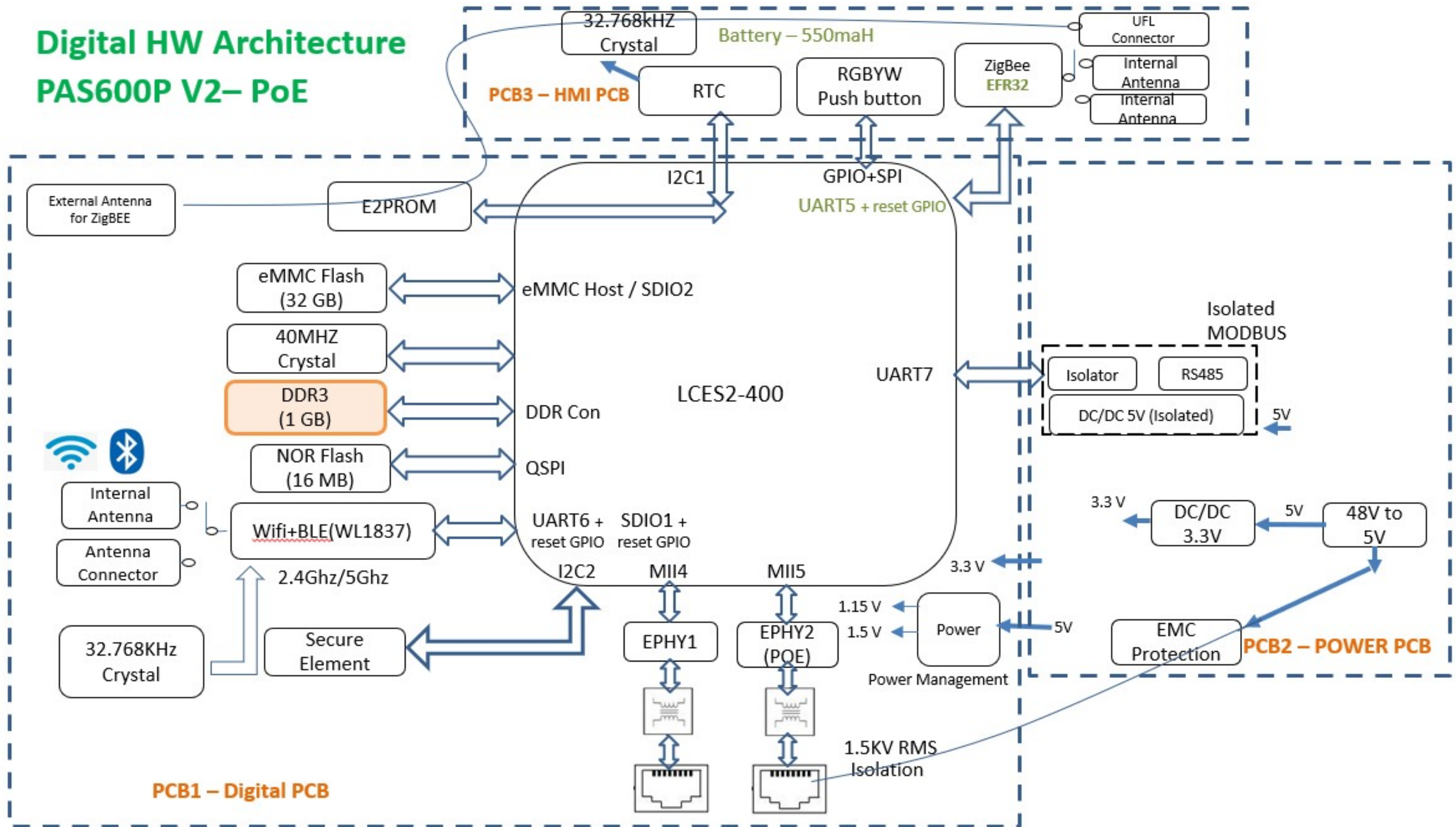


IOT BRIDGE ARCHITECTURE

Digital HW Architecture PAS600P V2- PoE



NOTA : THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

Schneider
Electric

BPAS V2-DIGITAL-72MM-POE PCBA
BPAS V2-DIGITAL-72MM-POE PCBA
A72-REAL SCHEMATIC

NUMBER :
PKR4614001

BLOCK NAME = top

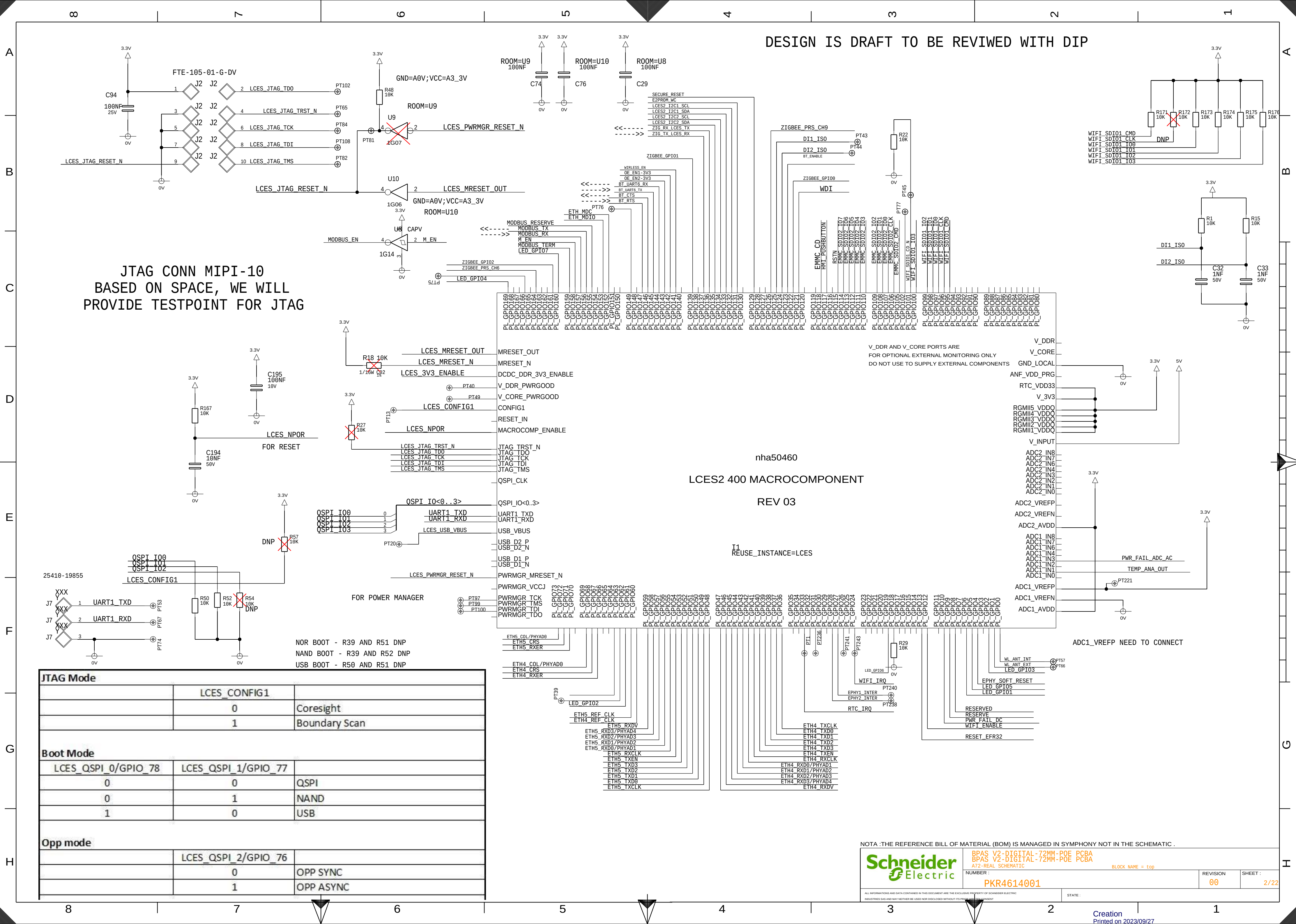
REVISION
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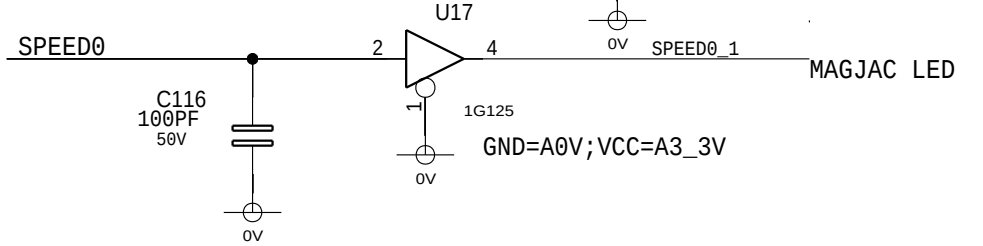
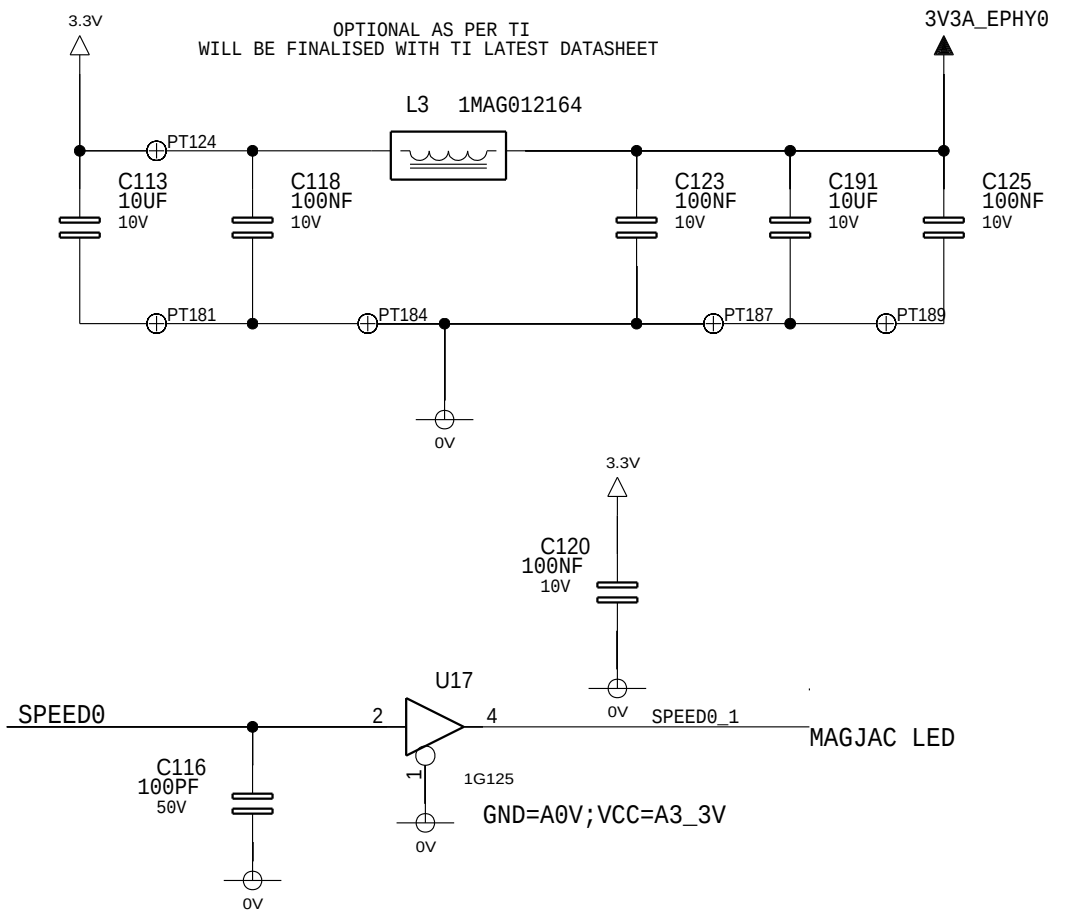
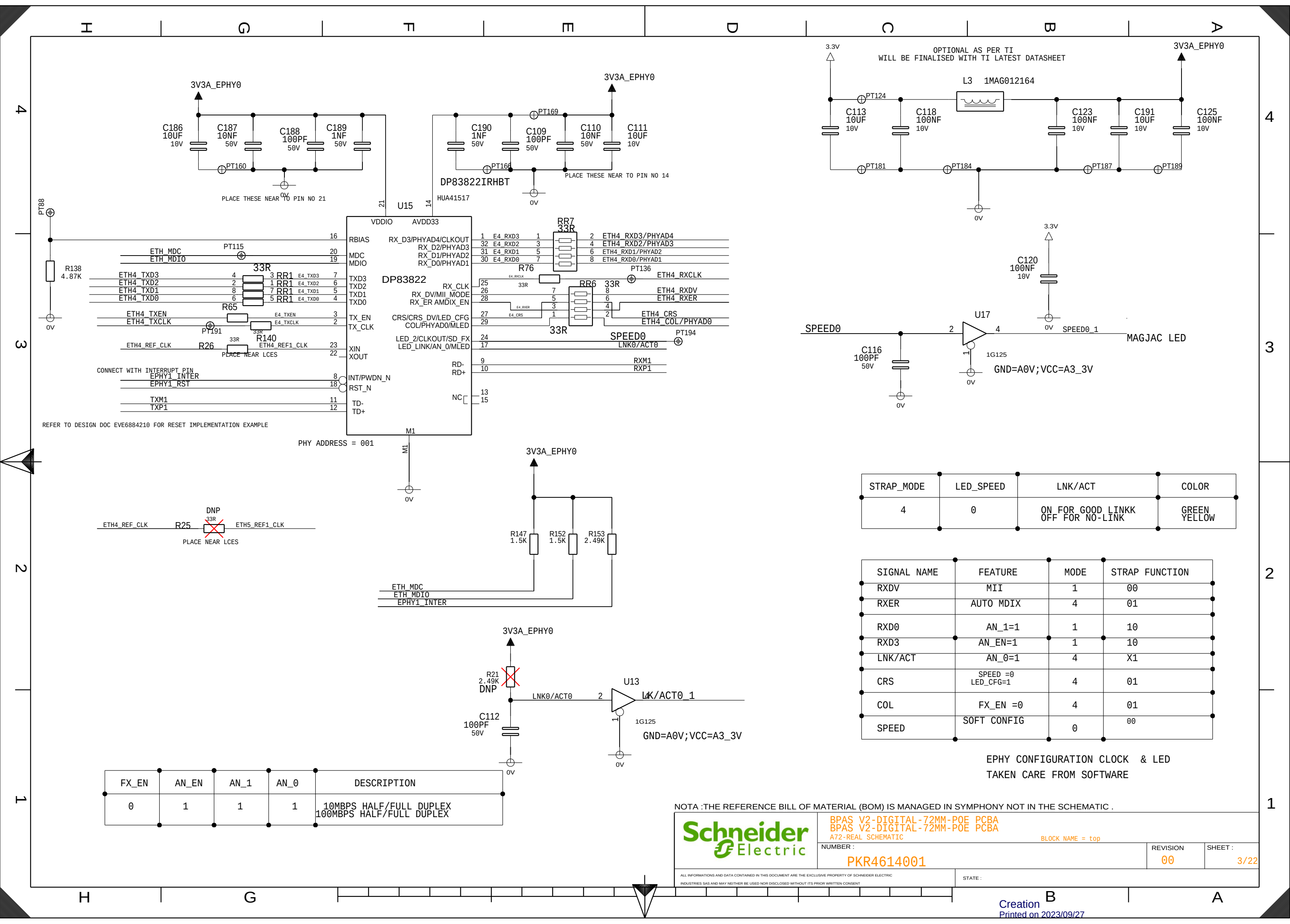
SHEET :
1/22

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STATE :

Creation
Printed on 2023/09/27





STRAP_MODE	LED_SPEED	LNK/ACT	COLOR
4	0	ON FOR GOOD LINKK OFF FOR NO-LINK	GREEN YELLOW

SIGNAL NAME	FEATURE	MODE	STRAP FUNCTION
RXDV	MII	1	00
RXER	AUTO MDIX	4	01
RXD0	AN_1=1	1	10
RXD3	AN_EN=1	1	10
LNK/ACT	AN_0=1	4	X1
CRS	SPEED =0 LED_CFG=1	4	01
COL	FX_EN =0	4	01
SPEED	SOFT CONFIG	0	00

EPHY CONFIGURATION CLOCK & LED
TAKEN CARE FROM SOFTWARE

FX_EN	AN_EN	AN_1	AN_0	DESCRIPTION
0	1	1	1	10MBPS HALF/FULL DUPLEX 100MBPS HALF/FULL DUPLEX

NOTA :THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

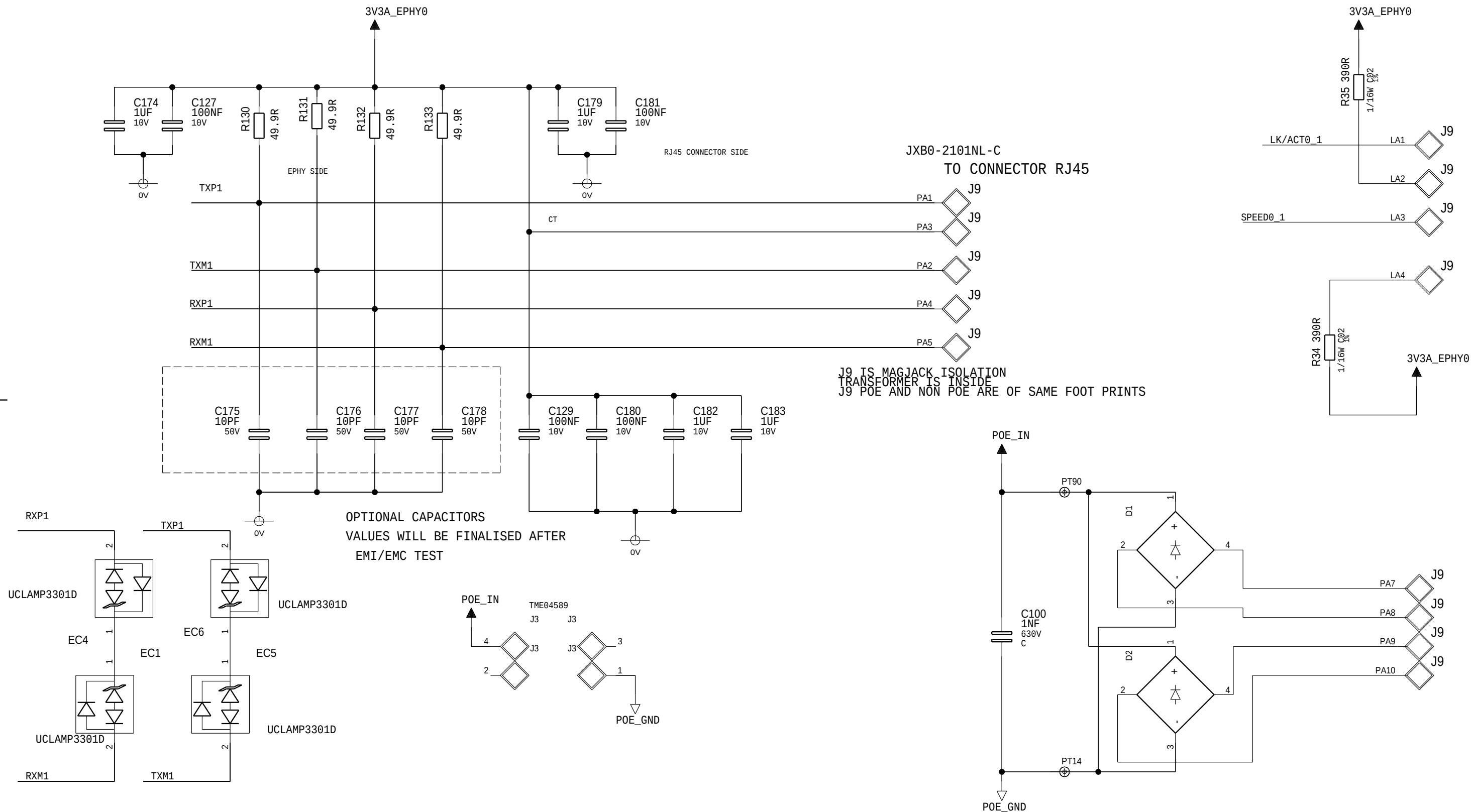
BPAS V2-DIGITAL-72MM-POE PCBA
BPAS V2-DIGITAL-72MM-POE PCBA
A72-REAL SCHEMATIC

NUMBER :
PKR4614001

BLOCK NAME = top

REVISION
00

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3/22



NOTA : THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .



BPAS V2-DIGITAL-72MM-POE PCBA
BPAS V2-DIGITAL-72MM-POE PCBA
A72-REAL SCHEMATIC

NUMBER :
PKR4614001

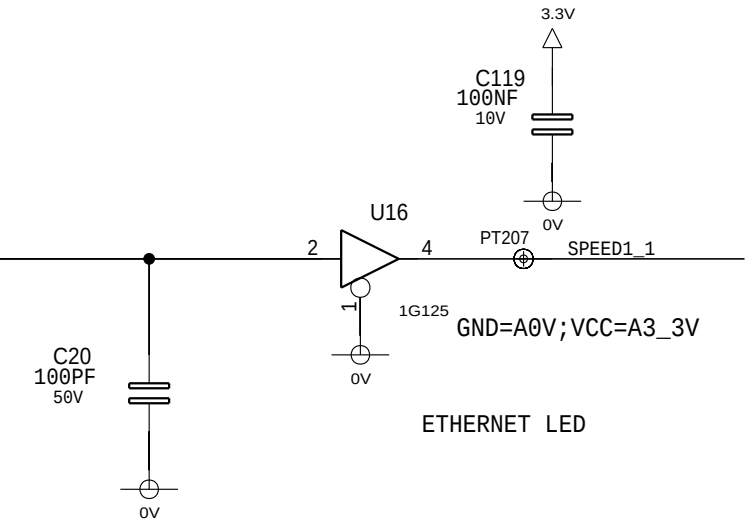
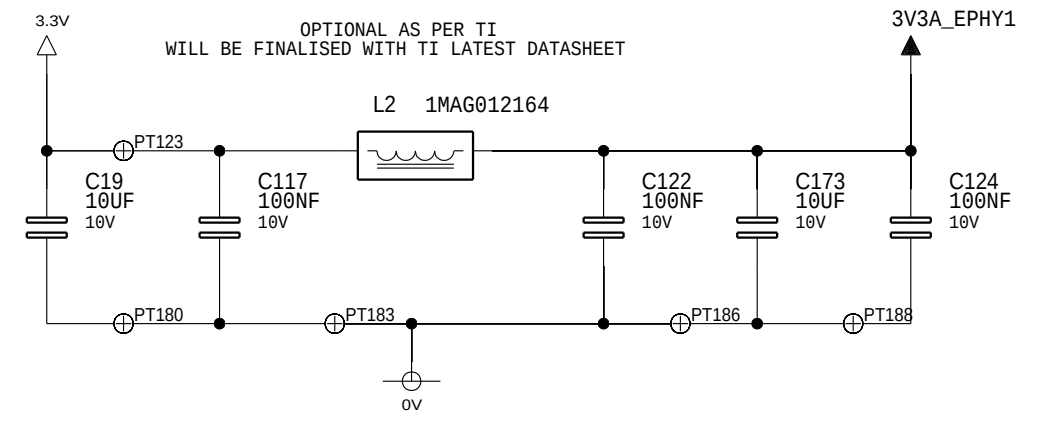
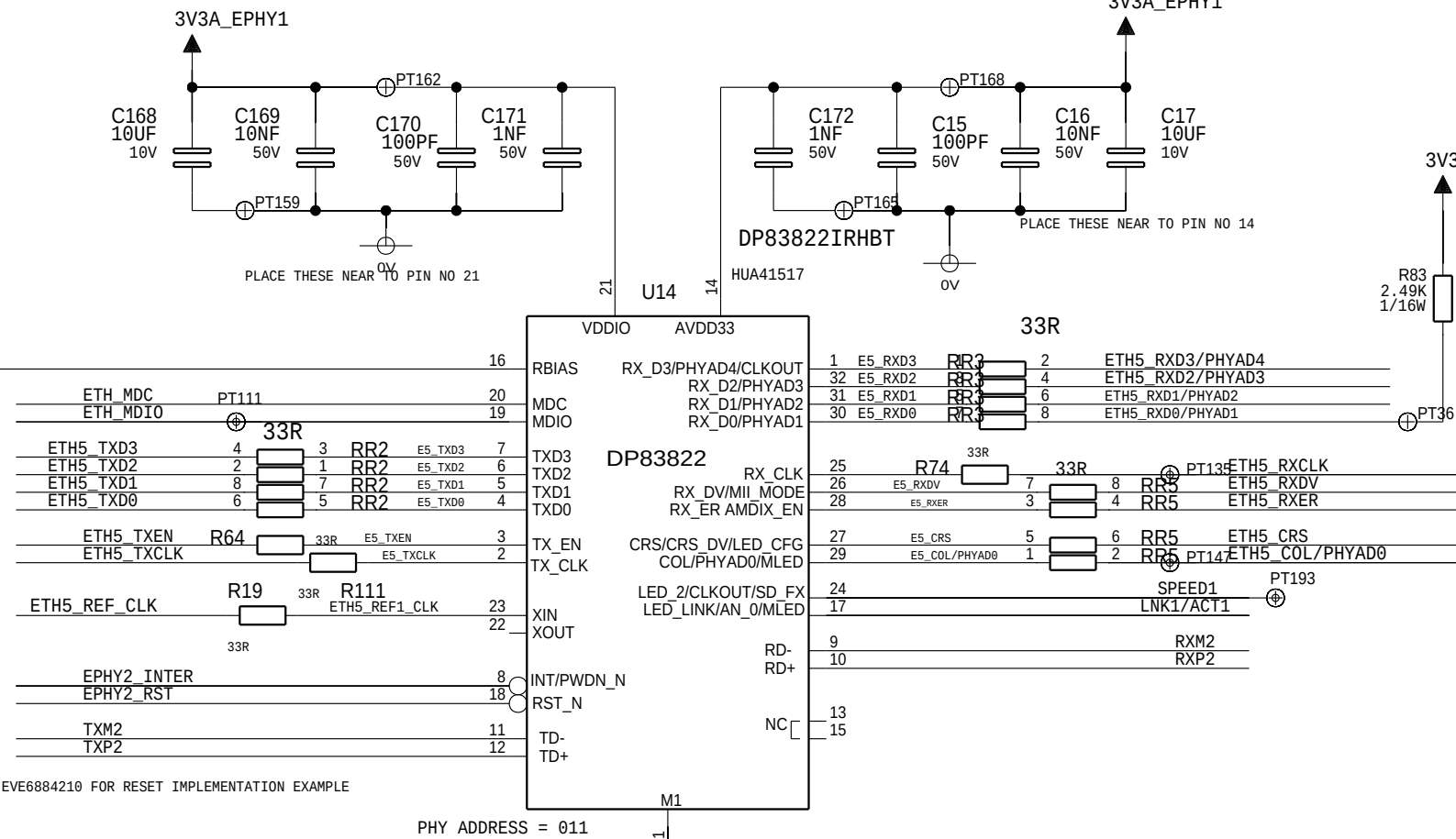
BLOCK NAME = top

REVISION
00

SHEET :
4/22

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STATE :



STRAP_MODE	LED_SPEED	LNK/ACT	COLOR
4	0	ON FOR GOOD LINK OFF FOR NO LINK	GREEN YELLOW

SIGNAL NAME	FEATURE	MODE	STRAP FUNCTION
RXDV	MII	1	00
RXER	AUTO MDIX	4	01
RXD0	AN_1=1	1	10
RXD3	AN_EN=1	1	10
LNK/ACT	AN_0=1	4	X1
CRS	SPEED =0 LED_CFG=1	4	01
COL	FX_EN =0	4	01
SPEED	SOFT CONFIG	0	00

EPHY CONFIGURATION CLOCK & LED
TAKEN CARE FROM SOFTWARE

FX_EN	AN_EN	AN_1	AN_0	DESCRIPTION
0	1	1	1	10MBPS HALF/FULL DUPLEX 100MBPS HALF/FULL DUPLEX

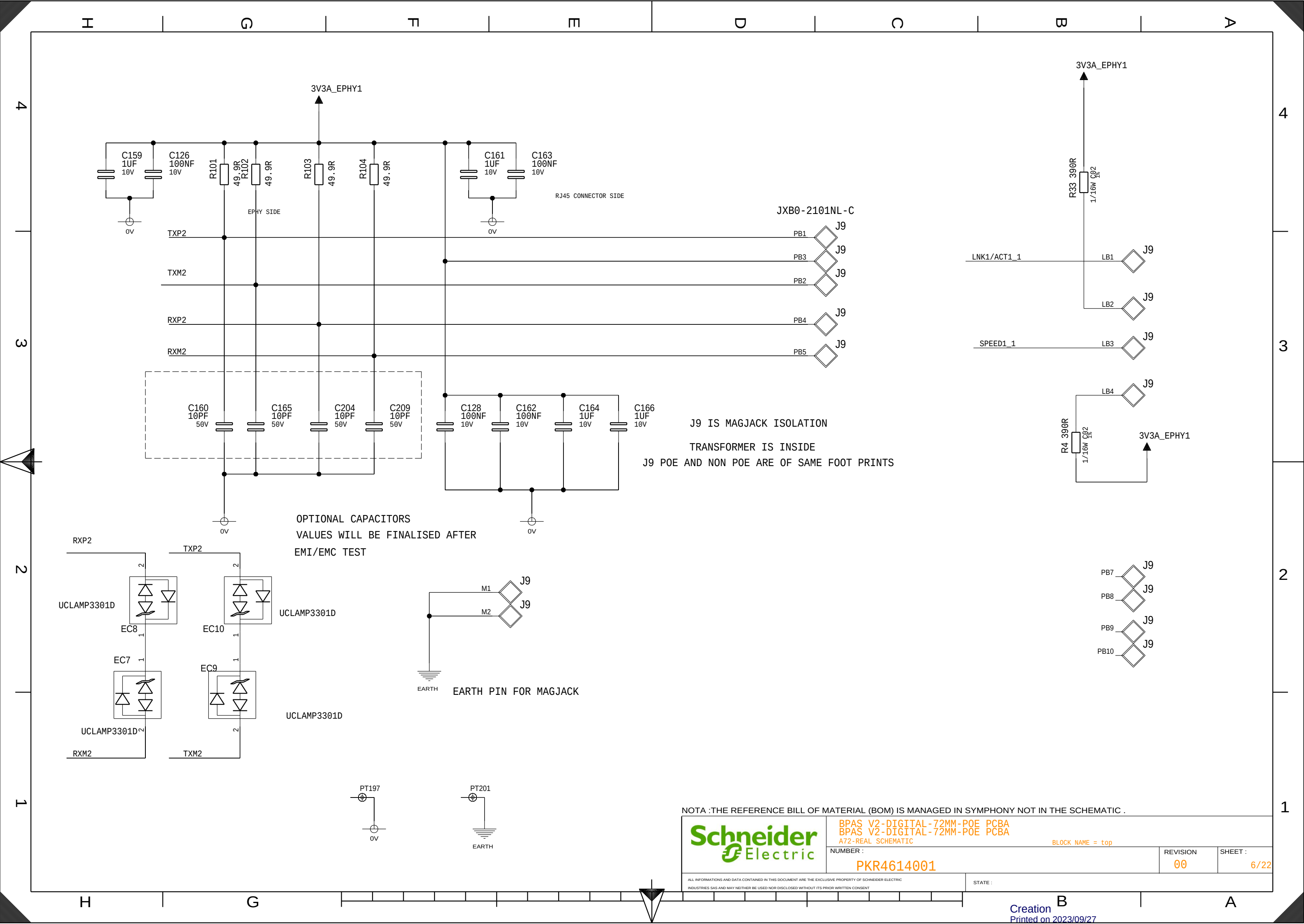
NOTA :THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .



BPAS V2-DIGITAL-72MM-POE PCBA
BPAS V2-DIGITAL-72MM-POE PCBA
A72-REAL SCHEMATIC
NUMBER :
PKR4614001

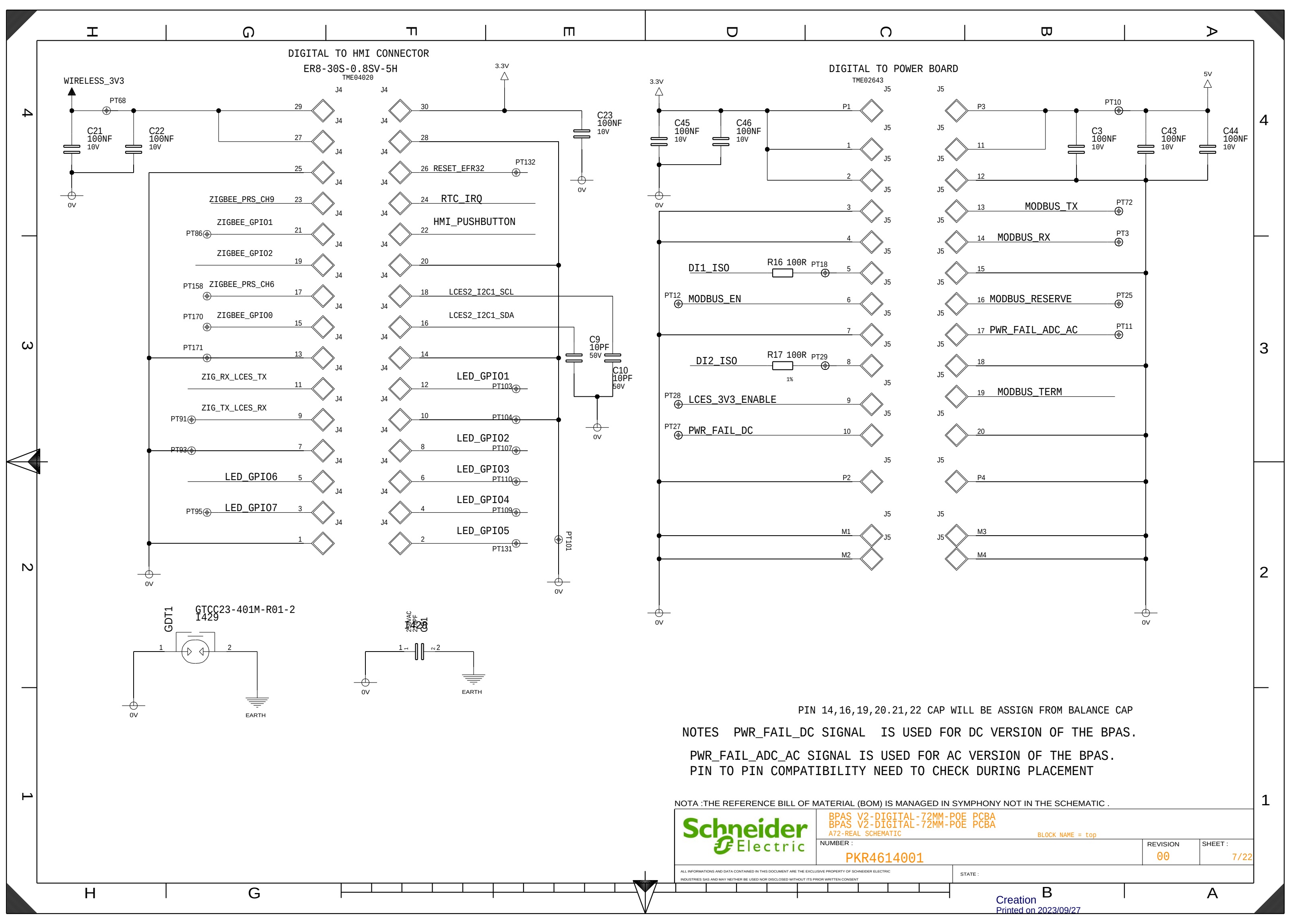
BLOCK NAME = top

REVISION
00
SHEET :
5/22



NOTA :THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

	BPAS V2-DIGITAL-72MM-POE PCBA			BLOCK NAME = top	REVISION 00	SHEET : 6/22
	BPAS V2-DIGITAL-72MM-POE PCBA					
	A72-REAL SCHEMATIC					
NUMBER : PKR4614001			STATE :			



PIN 14,16,19,20,21,22 CAP WILL BE ASSIGN FROM BALANCE CAP

NOTES PWR_FAIL_DC SIGNAL IS USED FOR DC VERSION OF THE BPAS.

PWR_FAIL_ADC_AC SIGNAL IS USED FOR AC VERSION OF THE BPAS.

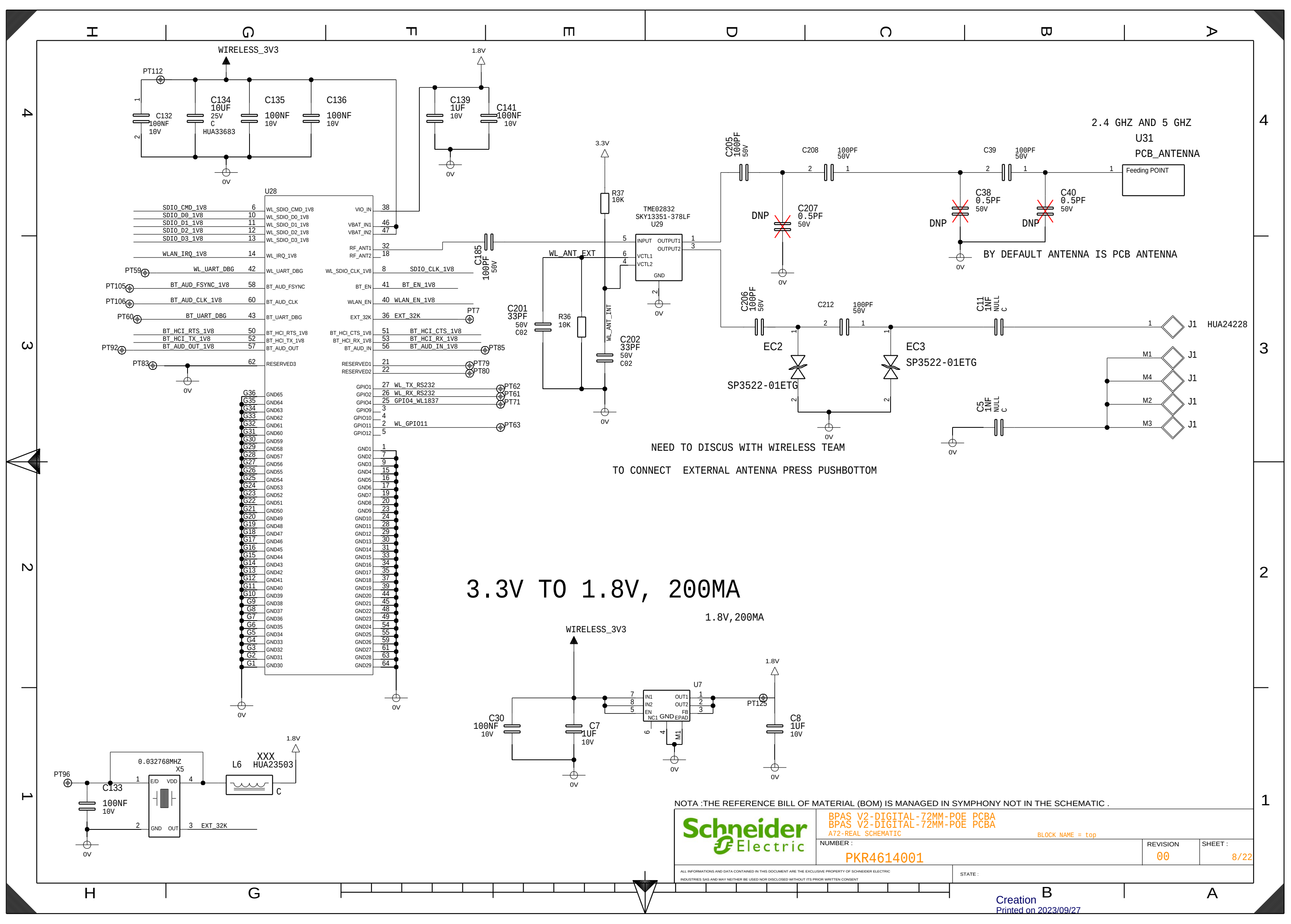
PIN TO PIN COMPATIBILITY NEED TO CHECK DURING PLACEMENT

NOTA :THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

	BPAS V2-DIGITAL-72MM-POE PCBA BPAS V2-DIGITAL-72MM-POE PCBA A72-REAL SCHEMATIC	BLOCK NAME = top	REVISION	SHEET :
	NUMBER : PKR4614001		00	7/22
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Creation B
Printed on 2023/09/27

A



SDIO_CMD_1V8	6	WL_SDIO_CMD_1V8
SDIO_D0_1V8	10	WL_SDIO_D0_1V8
SDIO_D1_1V8	11	WL_SDIO_D1_1V8
SDIO_D2_1V8	12	WL_SDIO_D2_1V8
SDIO_D3_1V8	13	WL_SDIO_D3_1V8

WL_UART_DBG	42	WL_UART_DBG
BT_AUD_FSYNC_1V8	58	BT_AUD_FSYNC
BT_AUD_CLK_1V8	60	BT_AUD_CLK
BT_UART_DBG	43	BT_UART_DBG
BT_HCI_RTS_1V8	50	BT_HCI_RTS_1V8
BT_HCI_TX_1V8	52	BT_HCI_TX_1V8
BT_AUD_OUT_1V8	57	BT_AUD_OUT

RESERVED3	62	RESERVED3
-----------	----	-----------

G36	GND65
G35	GND64
G34	GND63
G33	GND62
G32	GND61
G31	GND60
G30	GND59
G29	GND58
G28	GND57
G27	GND56
G26	GND55
G25	GND54
G24	GND53
G23	GND52
G22	GND51
G21	GND50
G20	GND49
G19	GND48
G18	GND47
G17	GND46
G16	GND45
G15	GND44
G14	GND43
G13	GND42
G12	GND41
G11	GND40
G10	GND39
G9	GND38
G8	GND37
G7	GND36
G6	GND35
G5	GND34
G4	GND33
G3	GND32
G2	GND31
G1	GND30

VIO_IN	38	
VBAT_IN1	46	
VBAT_IN2	47	
RF_ANT1	32	
RF_ANT2	18	
WL_SDIO_CLK_1V8	8	SDIO_CLK_1V8
BT_EN	41	BT_EN_1V8
WLAN_EN	40	WLAN_EN_1V8
EXT_32K	36	EXT_32K
BT_HCI_CTS_1V8	51	BT_HCI_CTS_1V8
BT_HCI_RX_1V8	53	BT_HCI_RX_1V8
BT_AUD_IN_1V8	56	BT_AUD_IN_1V8

GPIO1	27	WL_TX_RS232
GPIO2	26	WL_RX_RS232
GPIO4	25	GPIO4_WL1837
GPIO9	3	
GPIO10	4	
GPIO11	2	WL_GPIO11
GPIO12	5	
GND1	1	
GND2	7	
GND3	9	
GND4	15	
GND5	16	
GND6	17	
GND7	19	
GND8	20	
GND9	23	
GND10	24	
GND11	28	
GND12	29	
GND13	30	
GND14	31	
GND15	33	
GND16	34	
GND17	35	
GND18	37	
GND19	39	
GND20	44	
GND21	45	
GND22	48	
GND23	49	
GND24	54	
GND25	55	
GND26	59	
GND27	61	
GND28	63	
GND29	64	

Schneider

Electric

BPAS V2-DIGITAL-72MM-POE PCBA

BPAS V2-DIGITAL-72MM-POE PCBA

A72-REAL SCHEMATIC

NUMBER :

PKR4614001

BLOCK NAME = top

REVISION

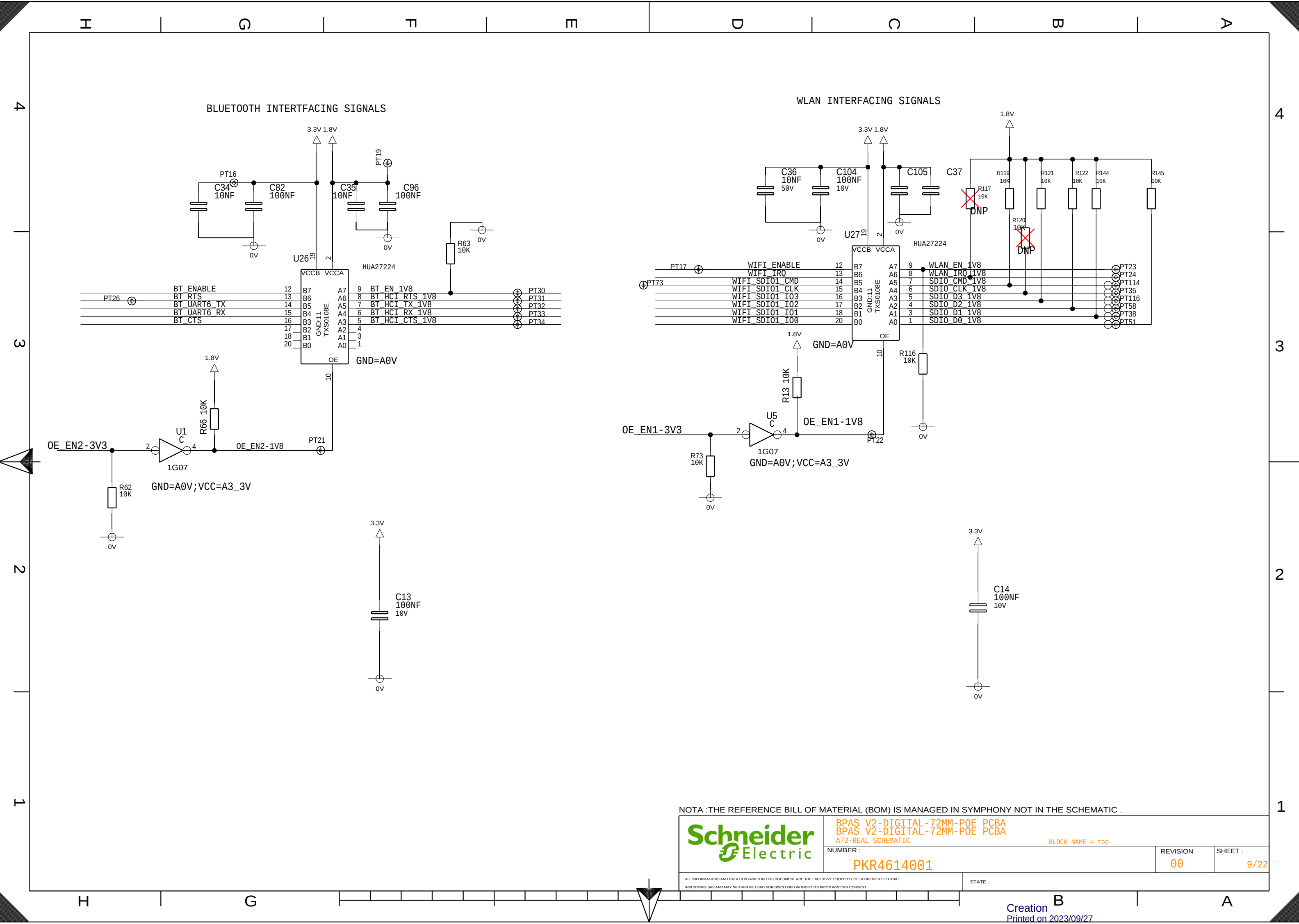
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SHEET :

8/22

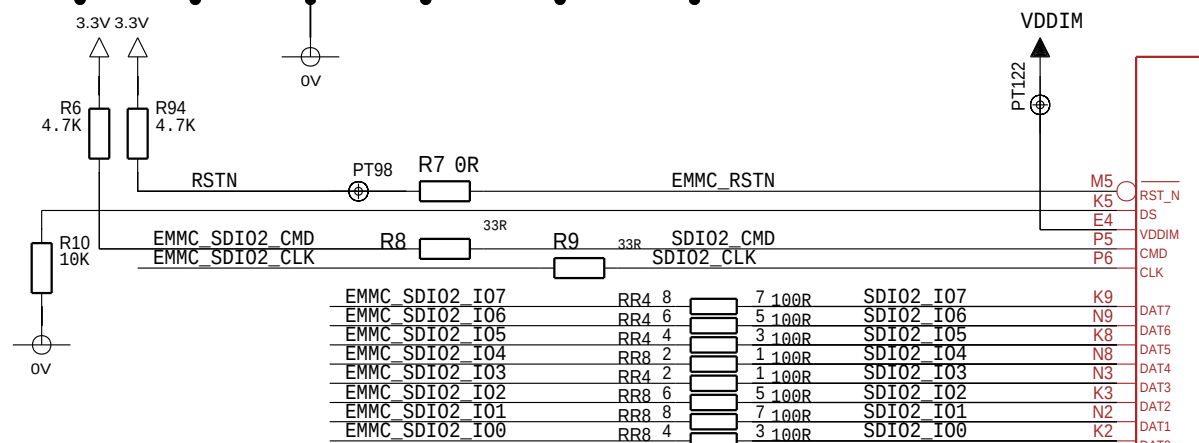
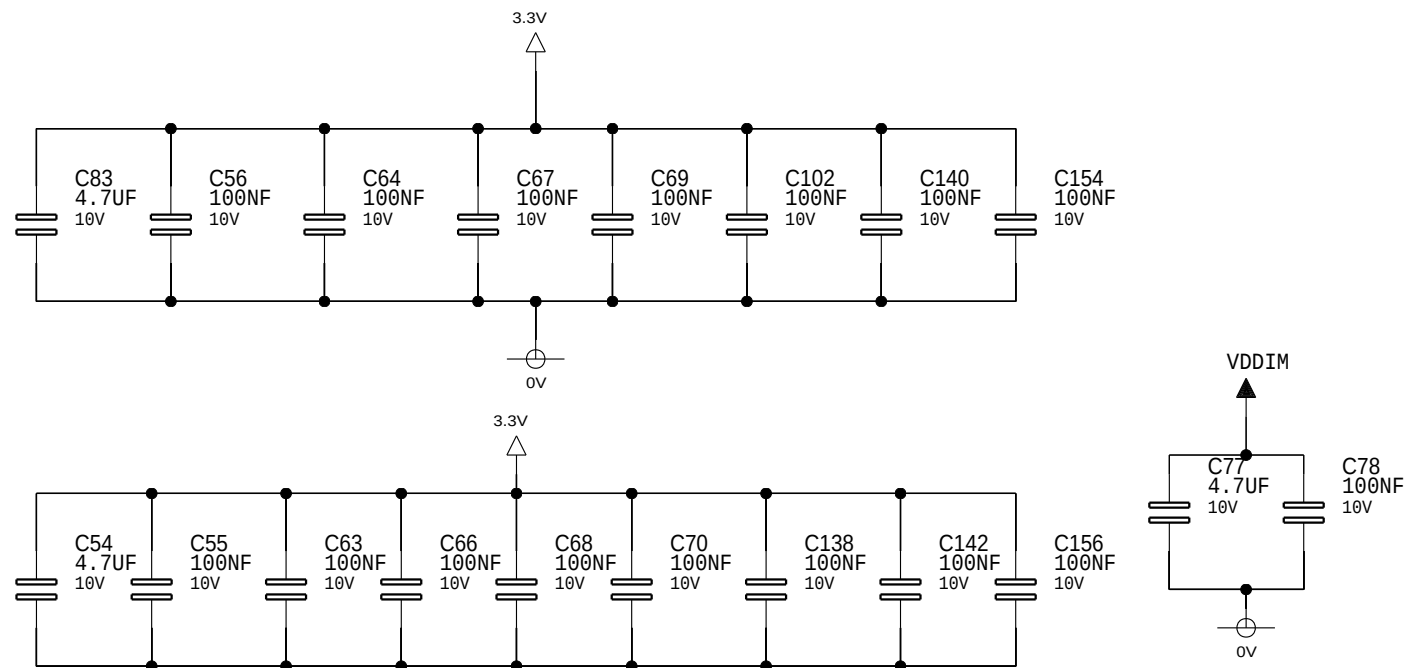
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STATE :

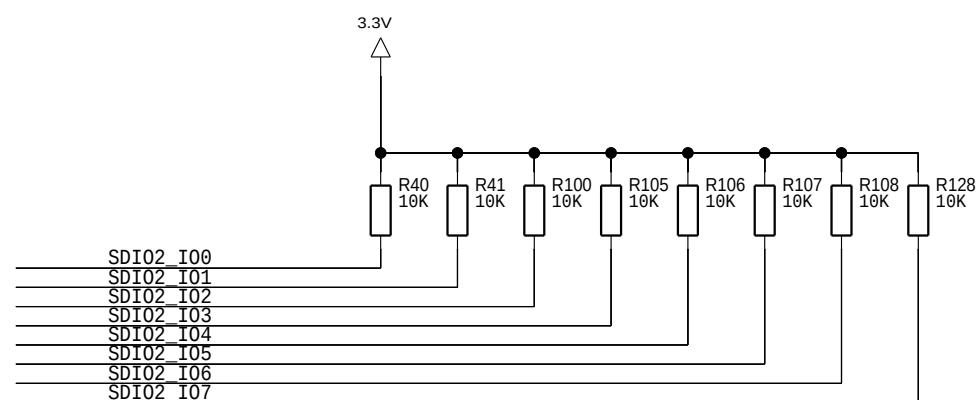


NOTA : THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

	BPAS V2-DIGITAL-72MM-POE PCBA BPAS V2-DIGITAL-72MM-POE PCBA A72-REAL SCHEMATIC			BLOCK NAME = top	REVISION 00	SHEET : 9/22
	NUMBER : PKR4614001					
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FOR CLOCK COMMAND AND DATA IMPEDANCE
MATCH FOR 50E

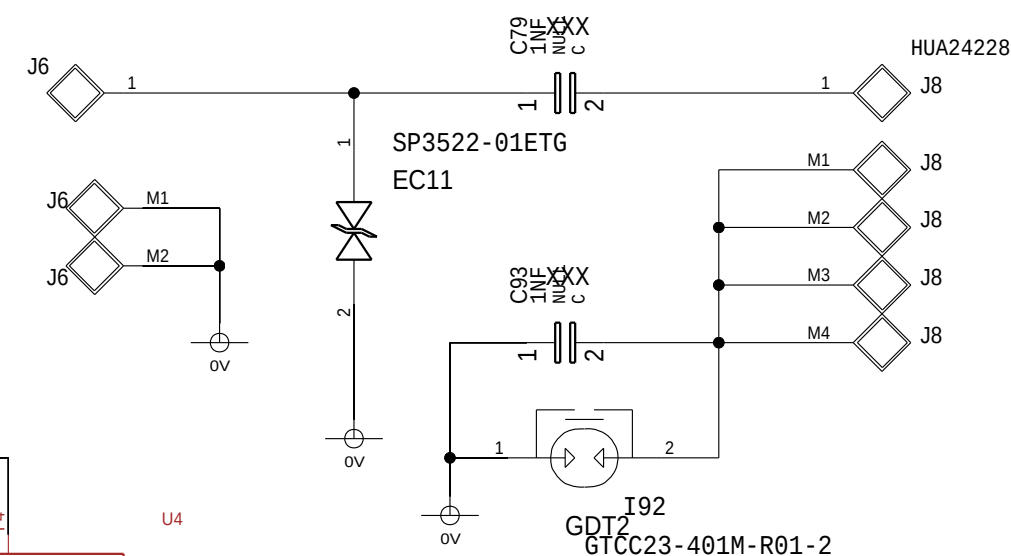


OPTIONAL PULL UP'S

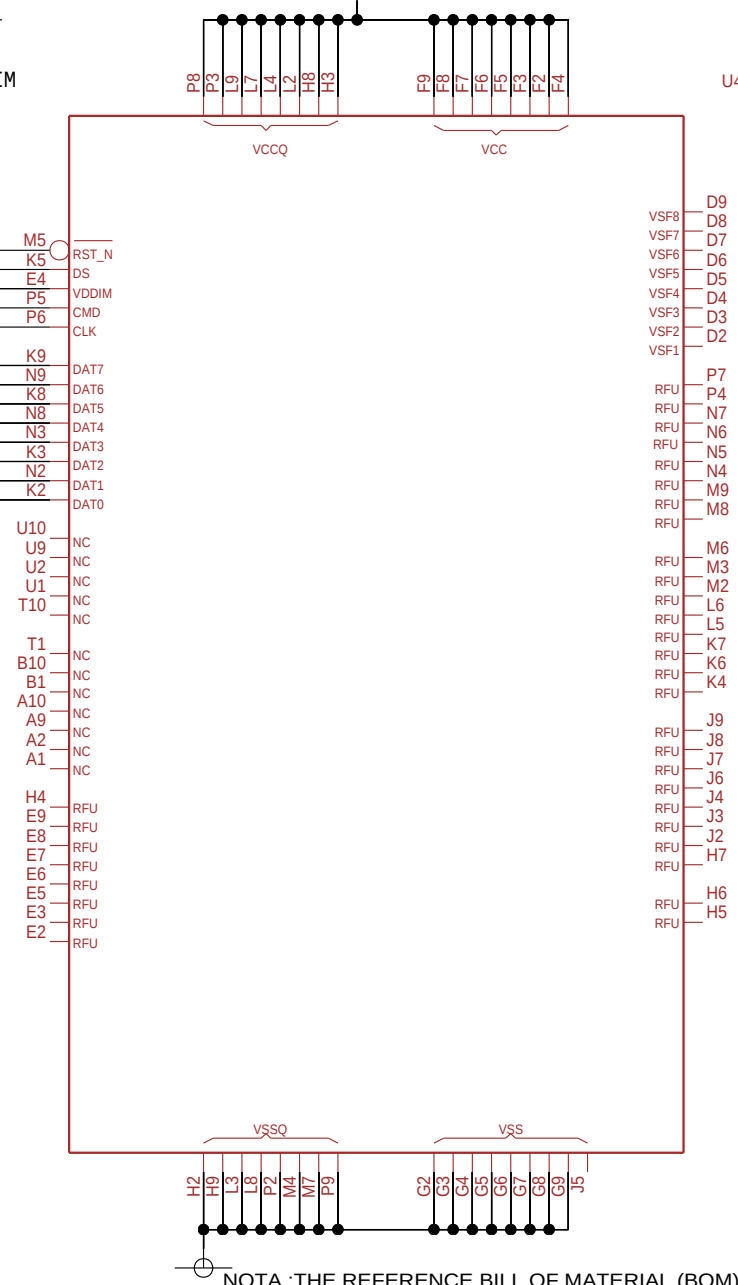
LCES DOESNOT SUPPORT HS400
ONLY FOR HS400 MODE

RF SIGNAL CONNECTED WITH COXIAL CABLE

FROM HMI BOARD



TVS DIODE WILL BE ADDED
NEAR TO CONNECTOR



NOTA :THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

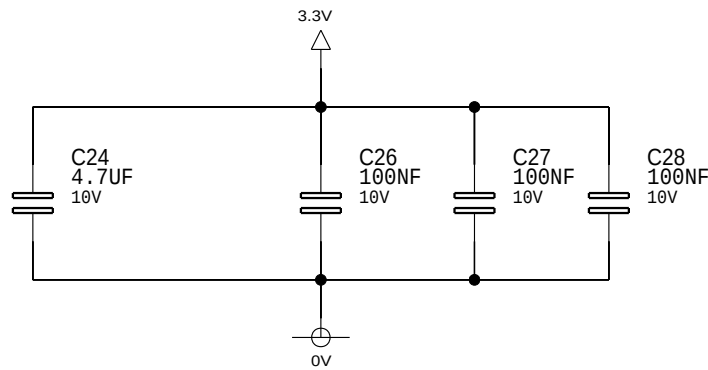


BPAS V2-DIGITAL-72MM-POE PCBA
BPAS V2-DIGITAL-72MM-POE PCBA
A72-REAL SCHEMATIC
NUMBER :
PKR4614001

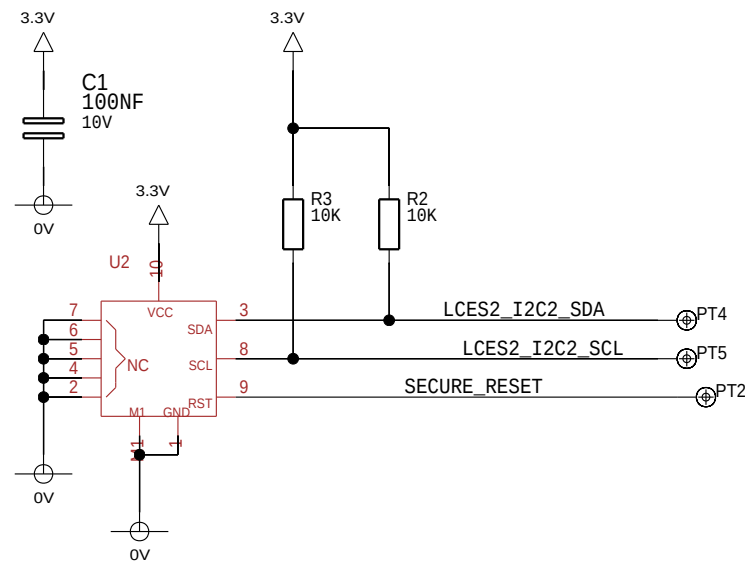
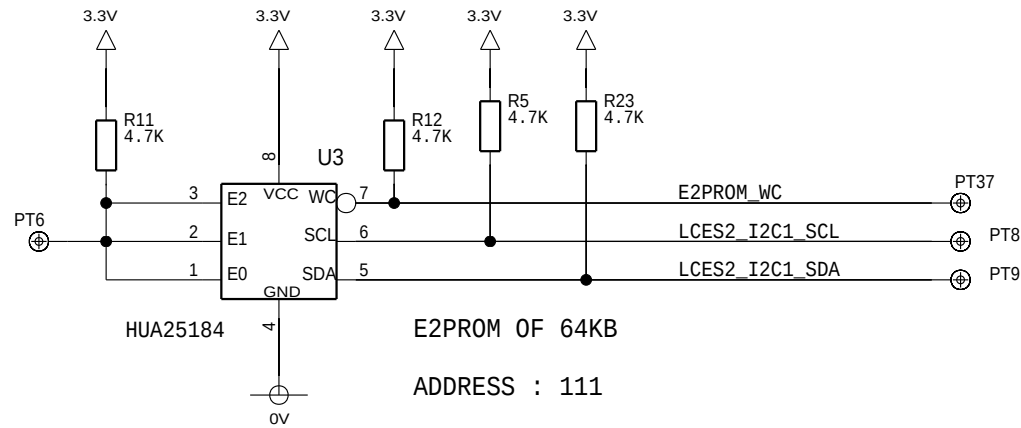
BLOCK NAME = top

REVISION
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SHEET :
10/22



BY PASS CAPACITOR FROM BALANCE CAP



NOTA : THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

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Electric

BPAS V2-DIGITAL-72MM-POE PCBA
BPAS V2-DIGITAL-72MM-POE PCBA
A72-REAL SCHEMATIC

NUMBER :
PKR4614001

BLOCK NAME = top

REVISION
00

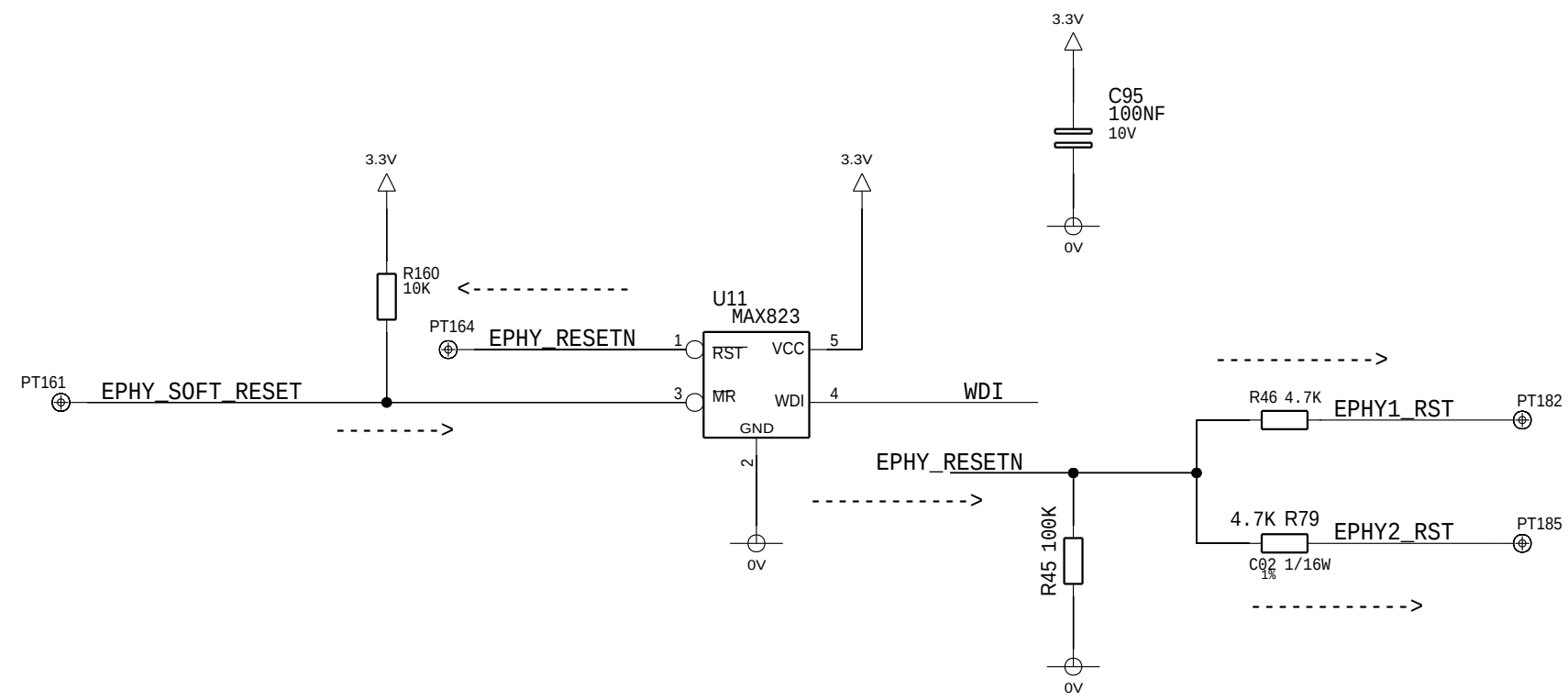
SHEET :
11/22

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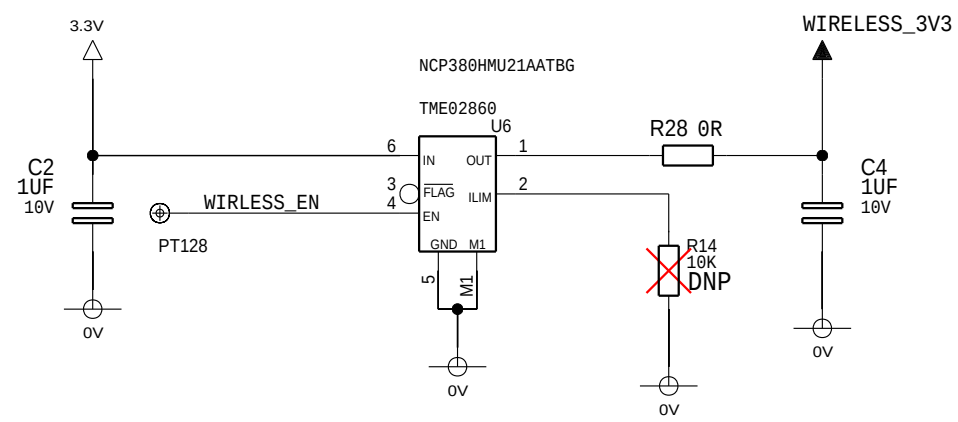
STATE :

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RESET FOR EPHY



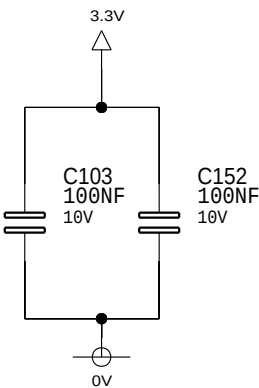
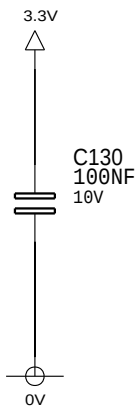
TO ENABLE AND DISABLE WIRELESS



NOTA : THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

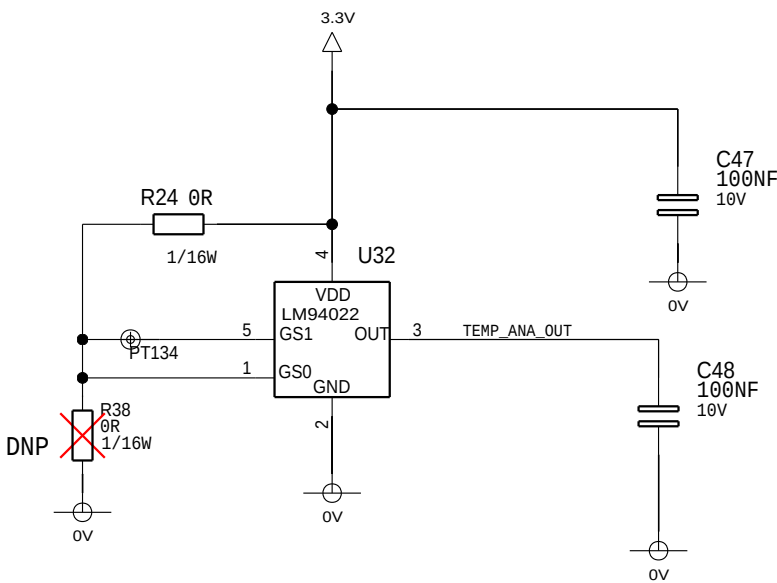
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	NUMBER : PKR4614001					
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BALANCING CAPACTIOR



TEMPERATURE SENSOR

SENSOR GAIN
GS1 =0,GS0=0 -----> -5.5MV/C
GS1 =1,GS0=1 -----> -13.6MV/C



BASED ON SENSOR GAIN SELECTION
EITHER R24 OR R38 WILL BE DNP
TEMPERATURE RANGE -50 TO 150 DEG C

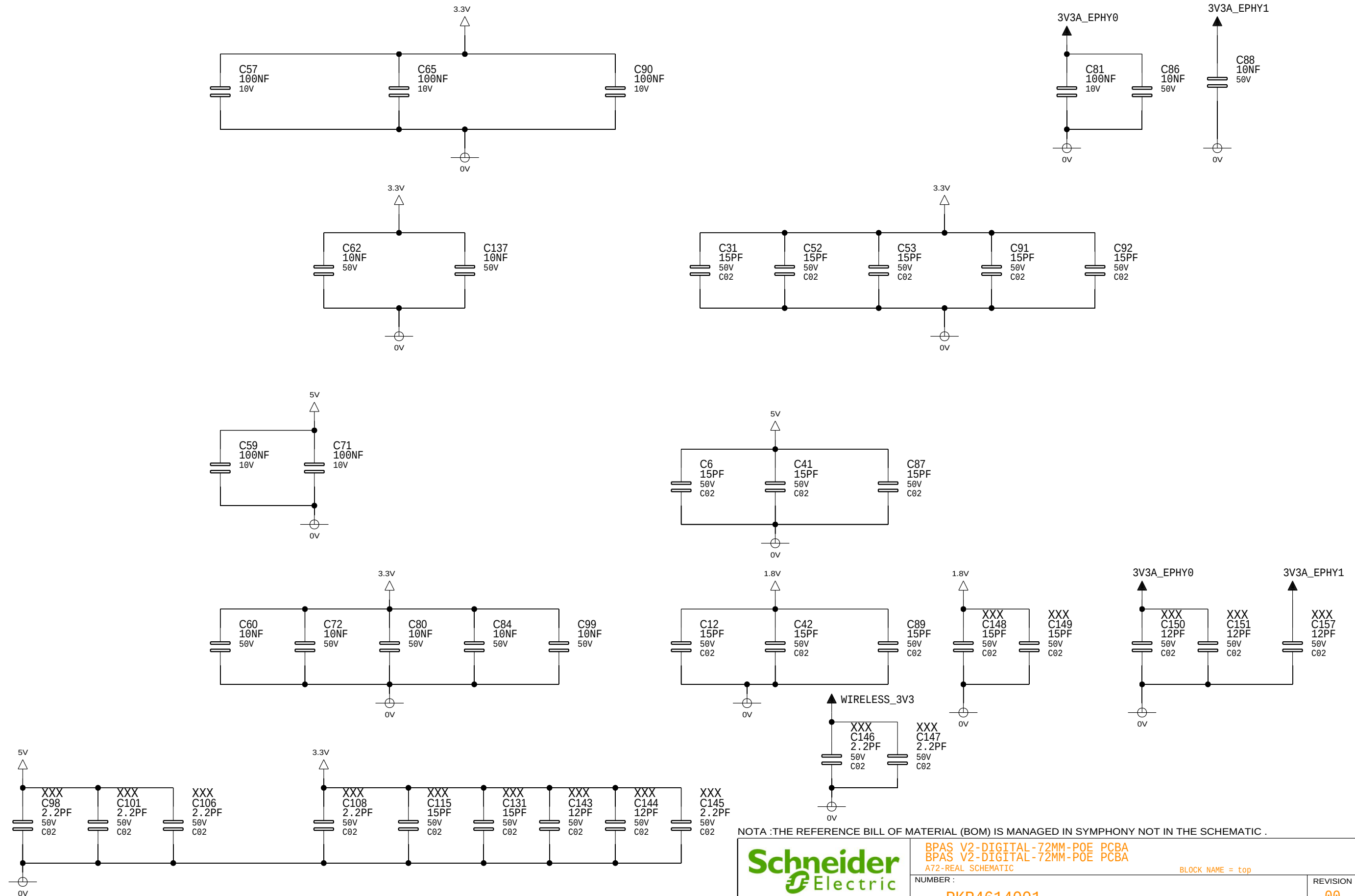
ACCURACY :±2.7 °C

NOTA :THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

	BPAS V2-DIGITAL-72MM-POE PCBA BPAS V2-DIGITAL-72MM-POE PCBA A72-REAL SCHEMATIC			BLOCK NAME = top	REVISION 00	SHEET : 13/22
	NUMBER : PKR4614001					
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BALANCE CAPACITOR

SPREAD ACCROS PLAN



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Electric

BPAS V2-DIGITAL-72MM-POE PCBA
BPAS V2-DIGITAL-72MM-POE PCBA
A72-REAL SCHEMATIC
NUMBER :
PKR4614001

BLOCK NAME = top

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14/22

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STATE :

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REV SHEET

REV #	Description	Page Number
REV0		
REV1	U8 changes to Schmitt Trigger Part	Page 3
	Push button on HMI Board Bank changed to Port A	Page3
	DI Bank changed to Bank A	Page3
	SDIO1_CD_N signal added	Page 3
	WIFI and RTC interrupt change to Bank A	Page 3
	Remove FNAND_Lock signal	Page 11 and 3
	B2B connector changed	Page 7
	R28_1 is DNP	Page 21
	Two more Buffer added to control RJ45 Led	Page 4 & 6
	Y Cap C51 and C121 value change as per EMC test	Page 7 and 8
	Y cap C12and C4 remove from RF line	Page 9
	New Part is slected for C11 and C5	Page 9
	SMA connector is replace by RPSMA	Page 9
	New part for RF switch is slected that support	page 9
	Micro push button remove	page 3
	GDT added	Page9
	EPHy Clock remove	Page6
	2 set of matching circuit is added for 2.4 & 5 Ghz	page9

NOTA : THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

Schneider
Electric

BPAS V2-DIGITAL-72MM-POE PCBA
BPAS V2-DIGITAL-72MM-POE PCBA
A72-REAL SCHEMATIC

BLOCK NAME = top

NUMBER :

PKR4614001

REVISION

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SHEET :

15/22

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STATE :

Creation
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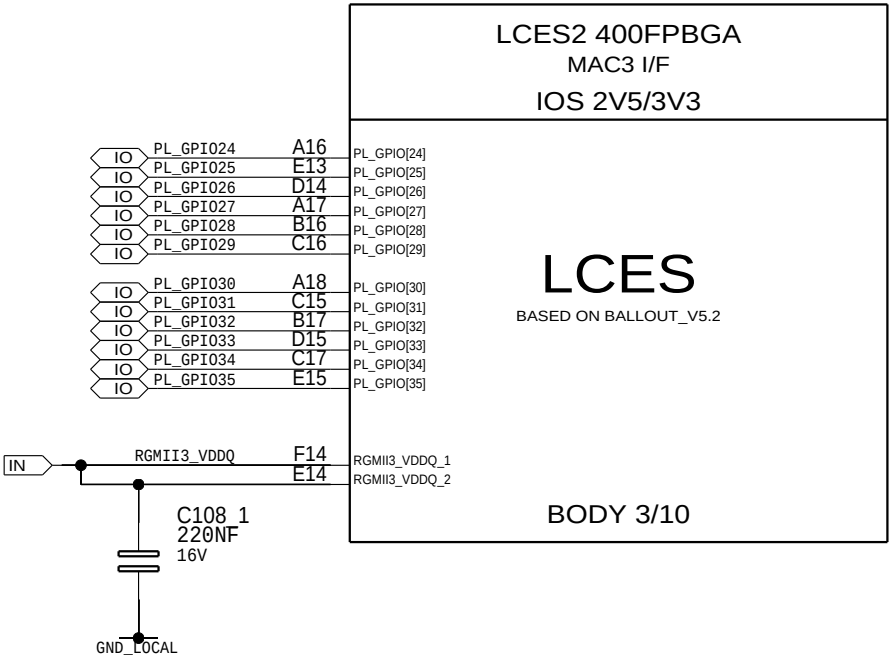
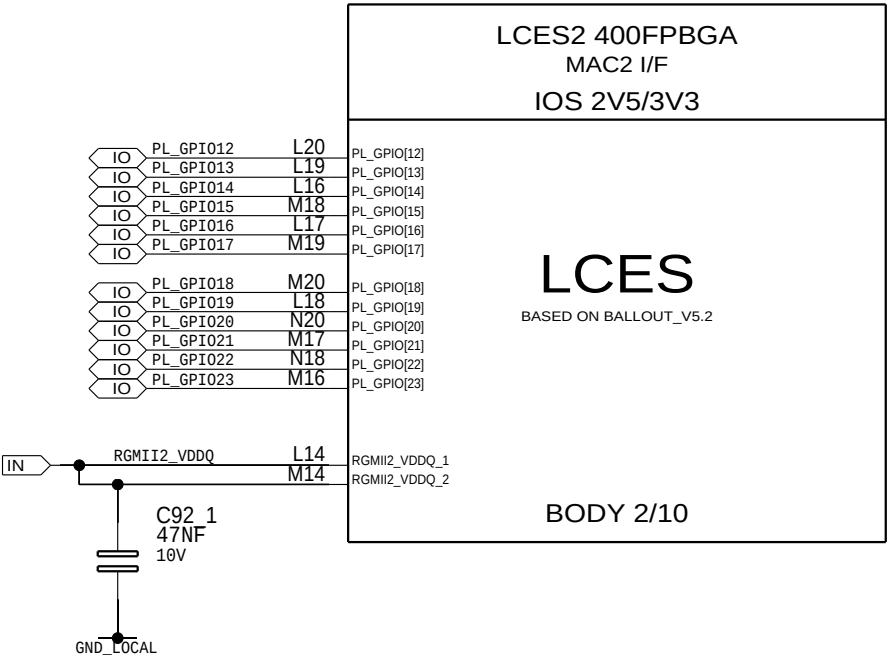
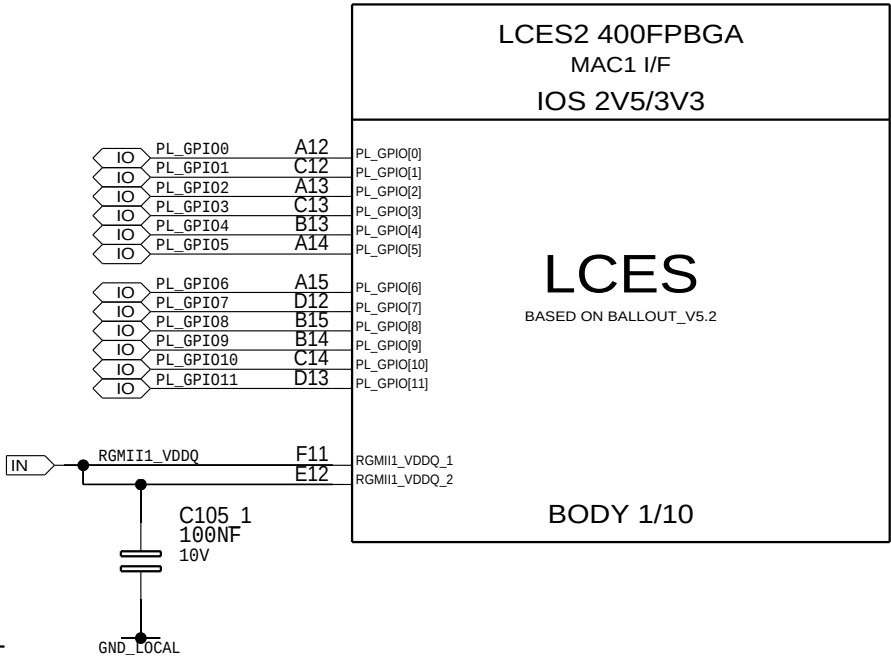
GPIOS 0 TO 59 (ETHERNET INTERFACES)

NOTE : THESE IOS ARE ORGANIZED ACCORDING TO THEIR ETHERNET RMGII FUNCTION
BUT EACH OF THEM MAY BE USED FOR OTHER FUNCTIONS
PLEASE CHECK RELEVANT DOCUMENTATION

U5_1
HUA40978

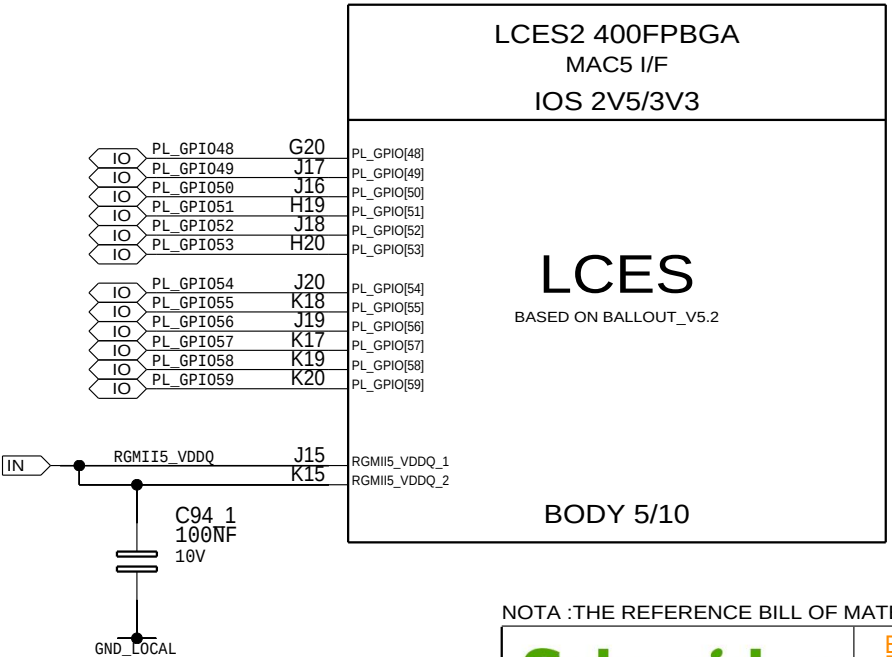
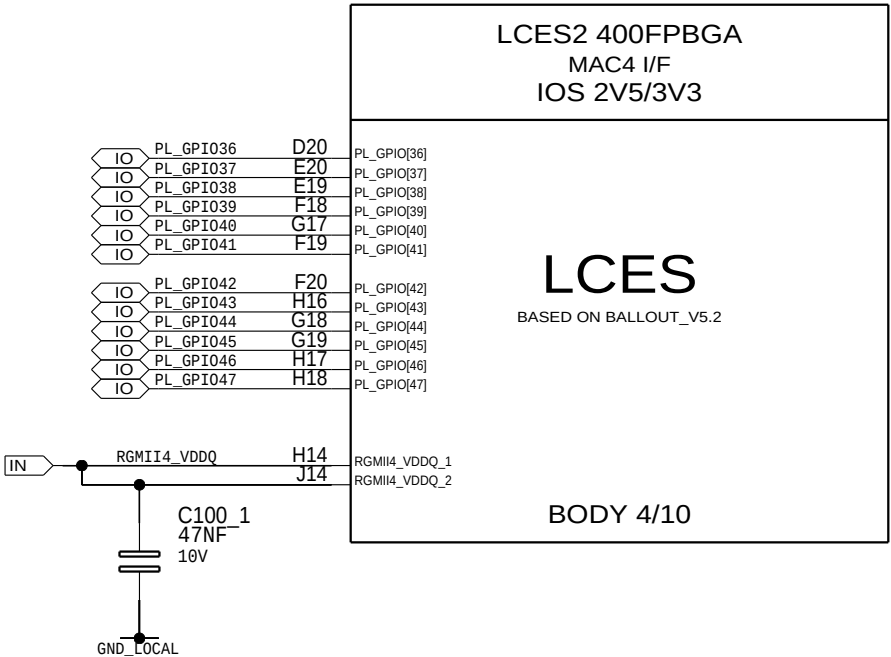
U5_1
HUA40978

U5_1
HUA40978



U5_1
HUA40978

U5_1
HUA40978



NOTA : THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

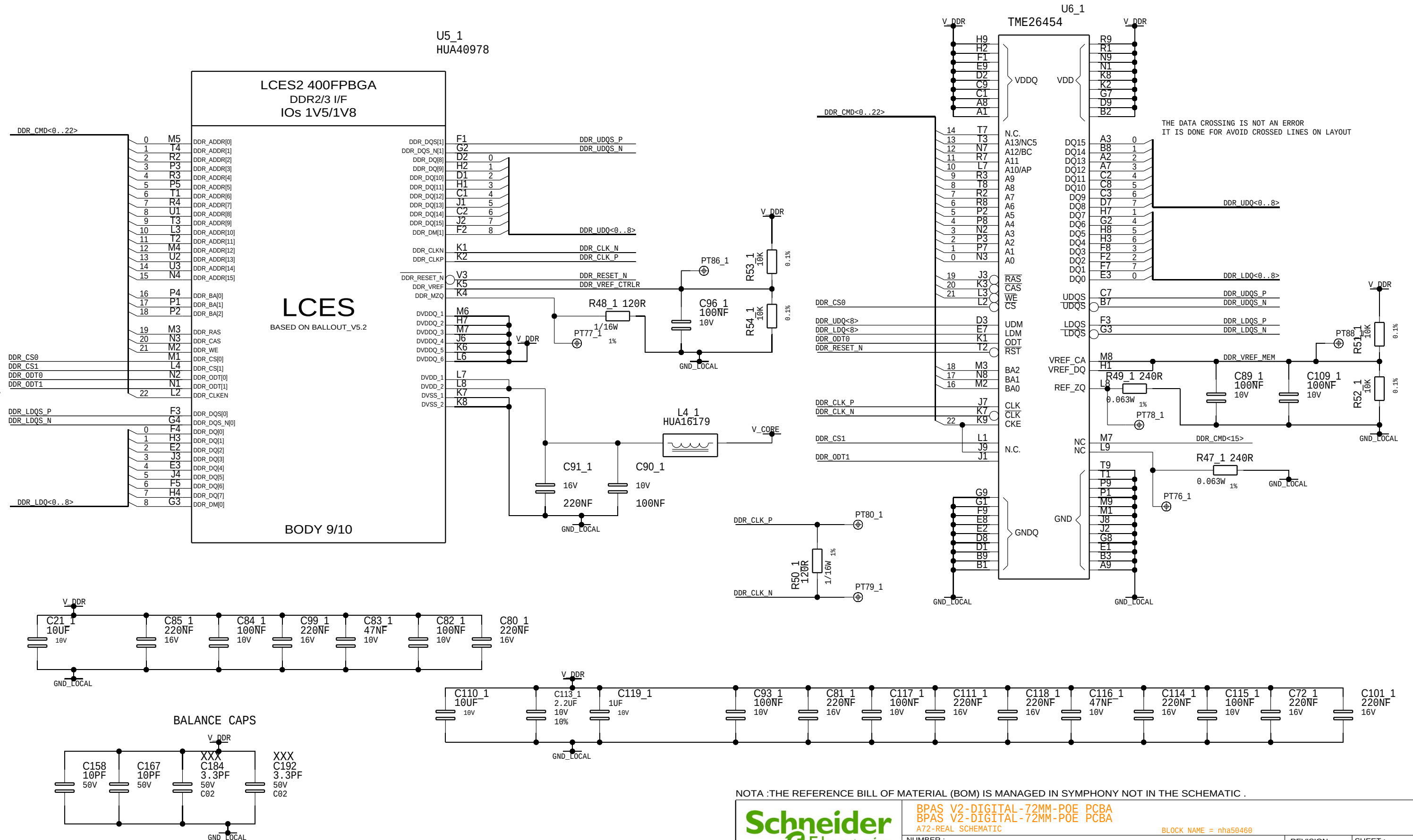
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	NUMBER : PKR4614001			00	16/22

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STATE :

DDR3 INTERFACE

NOTE : THE LCES DDR3 I/F OPERATES ONLY UNDER 1V5
THE 1V35 DDR3L MODE IS NOT SUPPORTED



NOTA : THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

Schneider Electric

BPAS V2-DIGITAL-72MM-POE PCBA
BPAS V2-DIGITAL-72MM-POE PCBA
A72-REAL SCHEMATIC

NUMBER :
PKR4614001

BLOCK NAME = nha50460

REVISION
00

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17/22

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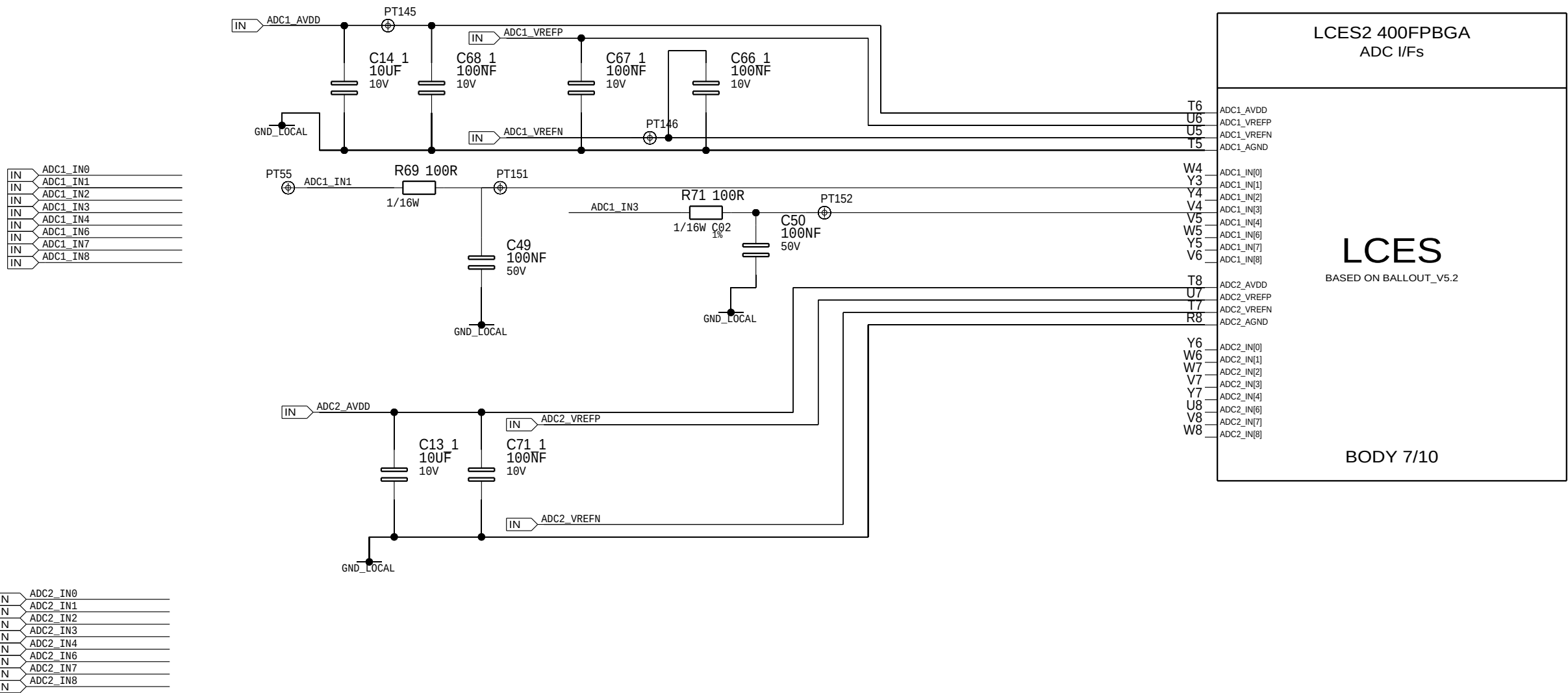
STATE :

Creation
Printed on 2023/09/27

ADC INTERFACE X 2

NOTE : THE VALUE OF RC FILTERS ON ANALOG INPUTS
MAY NEED TO BE ADJUSTED ACCORDING TO THE APPLICATION
PLEASE CHECK RELEVANT DOCUMENTATION

U5_1
HUA40978

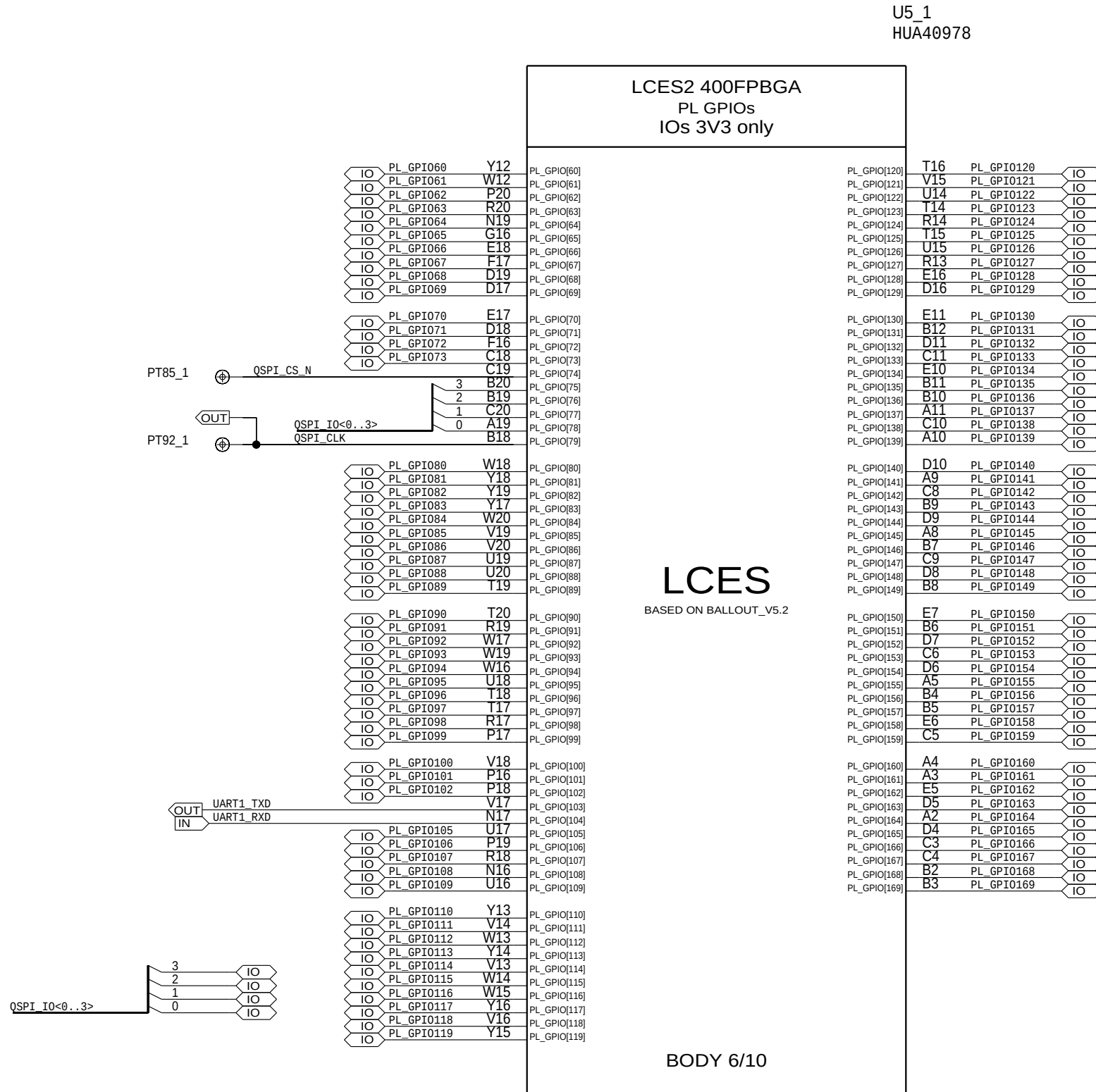


- IF AN ADC BLOCK IS NOT USED :
- VREFP AND VREFN INPUTS CAN BE LEFT UNCONNECTED
 - INPUTS CAN BE LEFT UNCONNECTED
 - AGND MUST BE CONNECTED TO GROUND
 - AVDD MUST BE CONNECTED TO 3V3

NOTA :THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

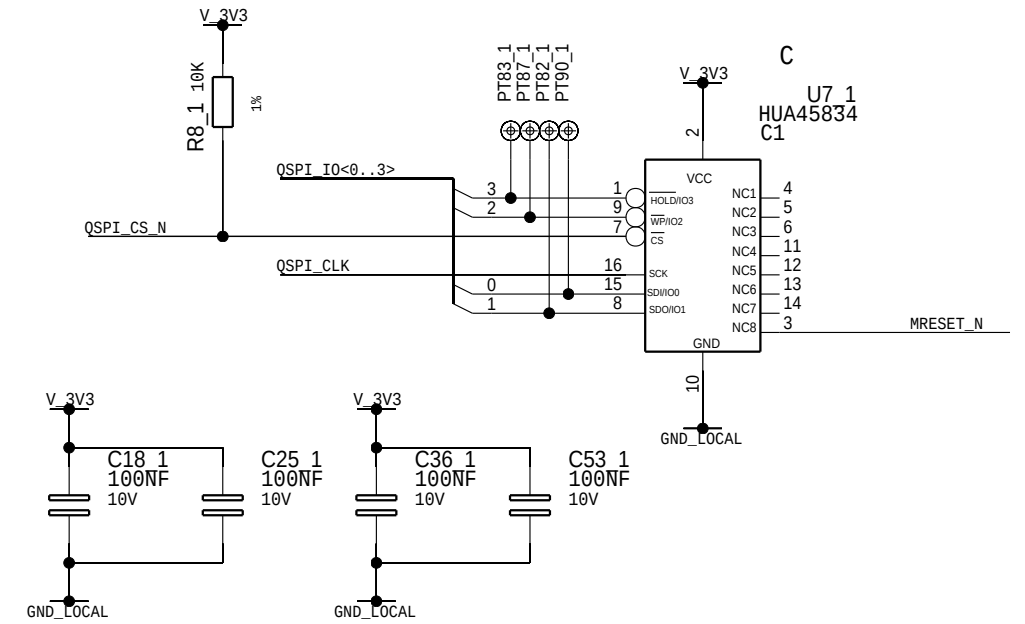
	BPAS V2-DIGITAL-72MM-POE PCBA BPAS V2-DIGITAL-72MM-POE PCBA A72-REAL SCHEMATIC			BLOCK NAME = nha50460	REVISION 00	SHEET : 18/22
	NUMBER : PKR4614001					
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OTHER GPIOS + NOR INTERFACE



QSPI NOR MEMORY - S0W16

QSPI NOR MEMORY DENSITY MAY BE ADAPTED TO SYSTEM NEED



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BPAS V2-DIGITAL-72MM-POE PCBA
BPAS V2-DIGITAL-72MM-POE PCBA
A72-REAL SCHEMATIC

BLOCK NAME = nha50460

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PKR4614001

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SHEET :

19/22

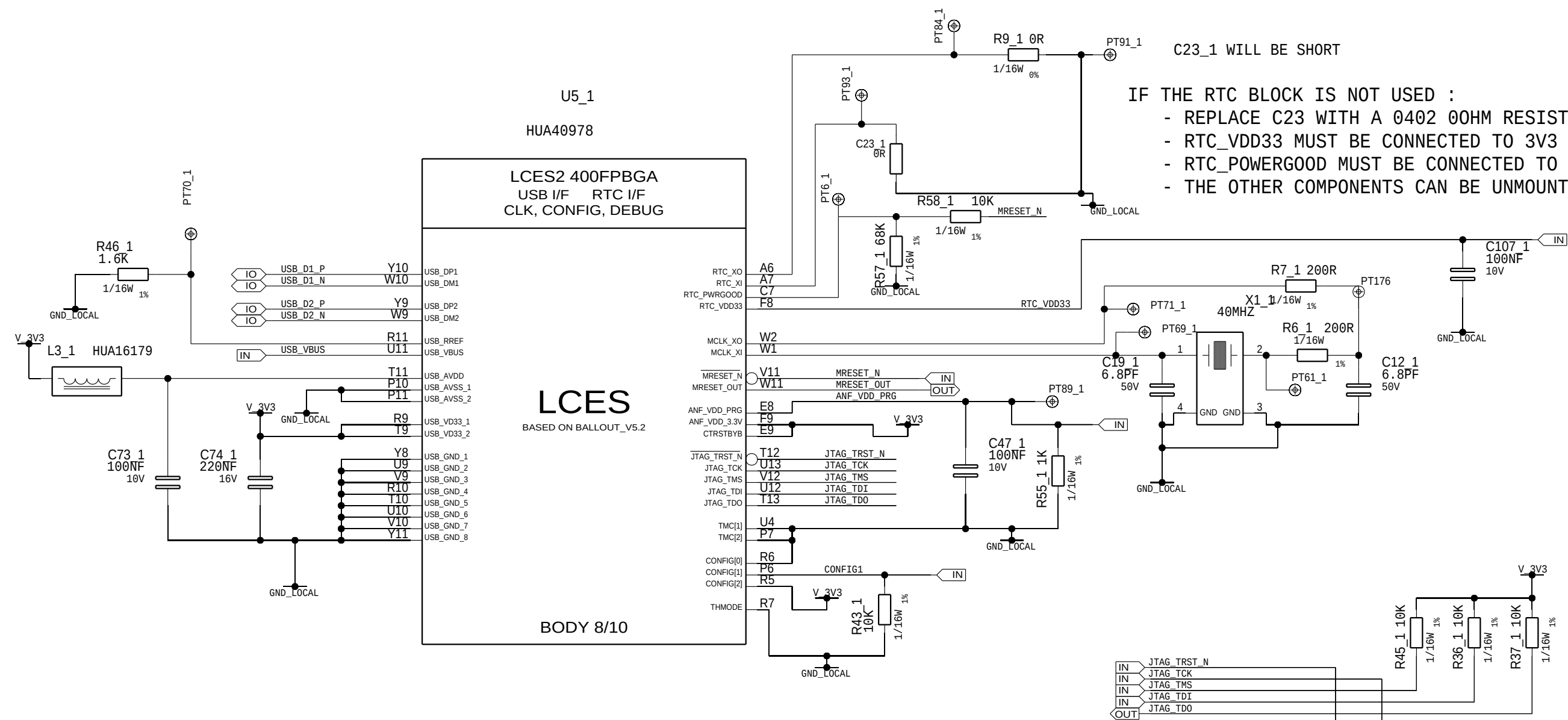
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STATE :

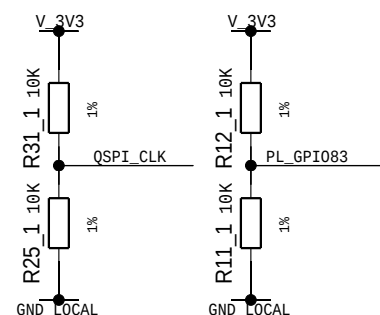
Creation
Printed on 2023/09/27

MAIN & RTC OSCILLATORS - CONFIGURATION

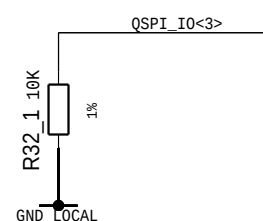
USB - JTAG - RESET



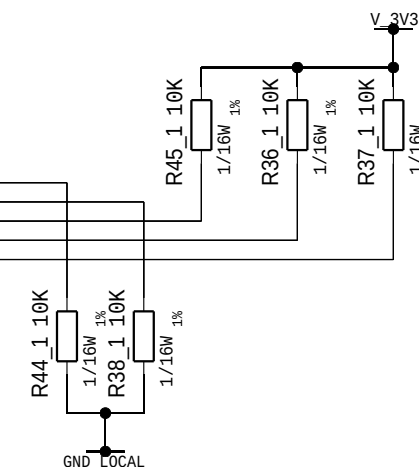
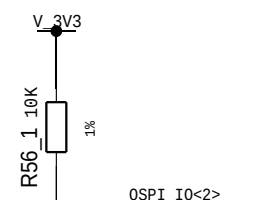
CONFIGURATION OF PULL-UP OR PULL-DOWN BUFFERS
FOR LCD INTERFACE



CONFIGURATION OF DDR3 MODE



CONFIGURATION OF OPP MODE IN ASYNC MODE



UPDATE THE BOM FOR CORRECT CONFIGURATION

OTHER CONFIGURATION SIGNALS :

- CONFIG1 (MACROCOMPONENT INPUT) FOR JTAG MODE OF OPERATION
- PL_GPIO<76> (QSPI_IO<2>) FOR OPP MODE : CONFIGURE THE CORRECT LEVEL AT RESET EXTERNALLY
- PL_GPIO<77> (QSPI_IO<1>) & PL_GPIO<78> (QSPI_IO<0>) FOR BOOT MODE : CONFIGURE THE CORRECT LEVEL AT RESET EXTERNALLY

PLEASE CHECK RELEVANT DOCUMENTATION

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Schneider
Electric

BPAS V2-DIGITAL-72MM-POE PCBA
BPAS V2-DIGITAL-72MM-POE PCBA
A72-REAL SCHEMATIC
NUMBER :
PKR4614001

BLOCK NAME = nha50460

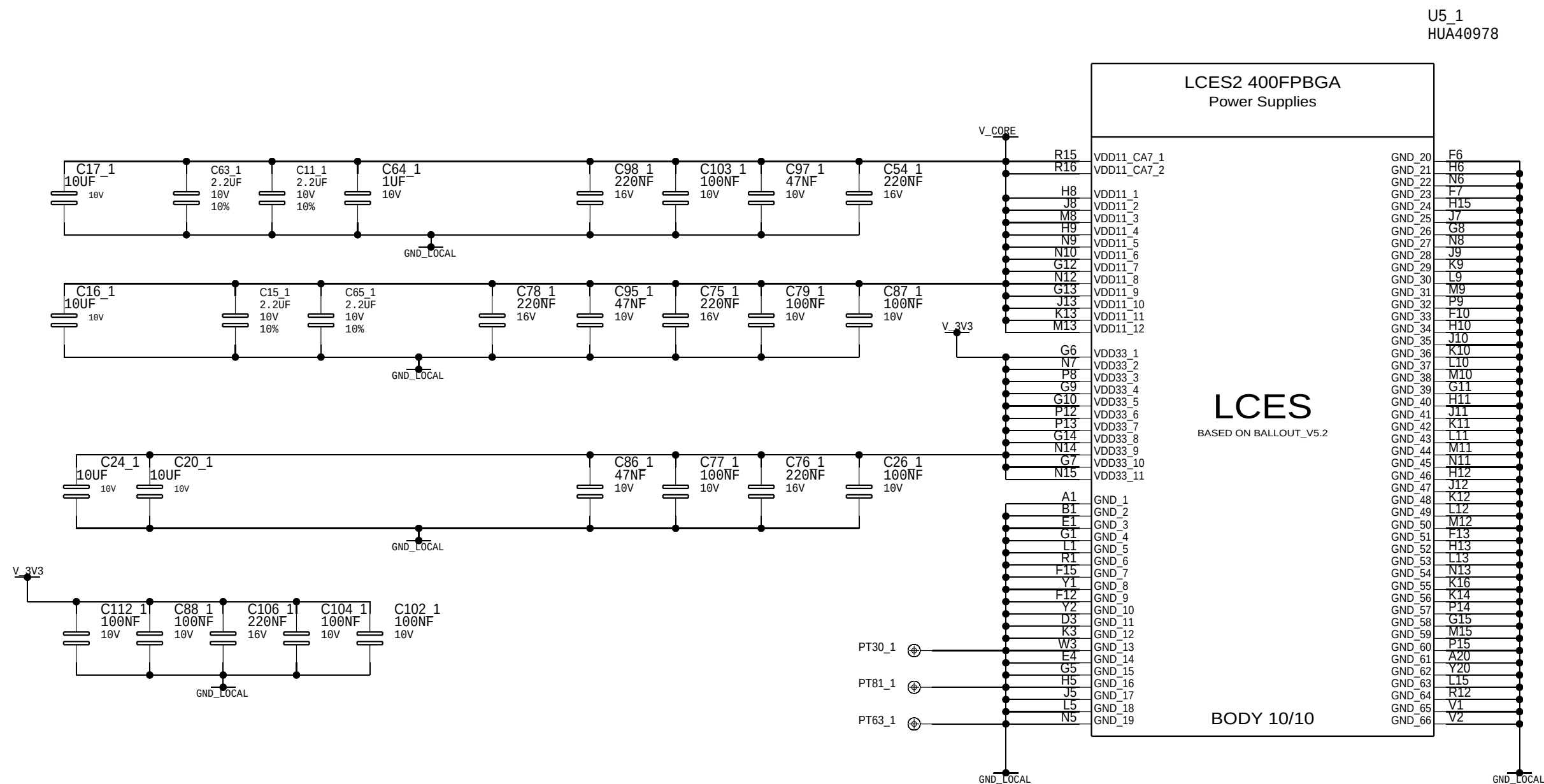
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STATE :

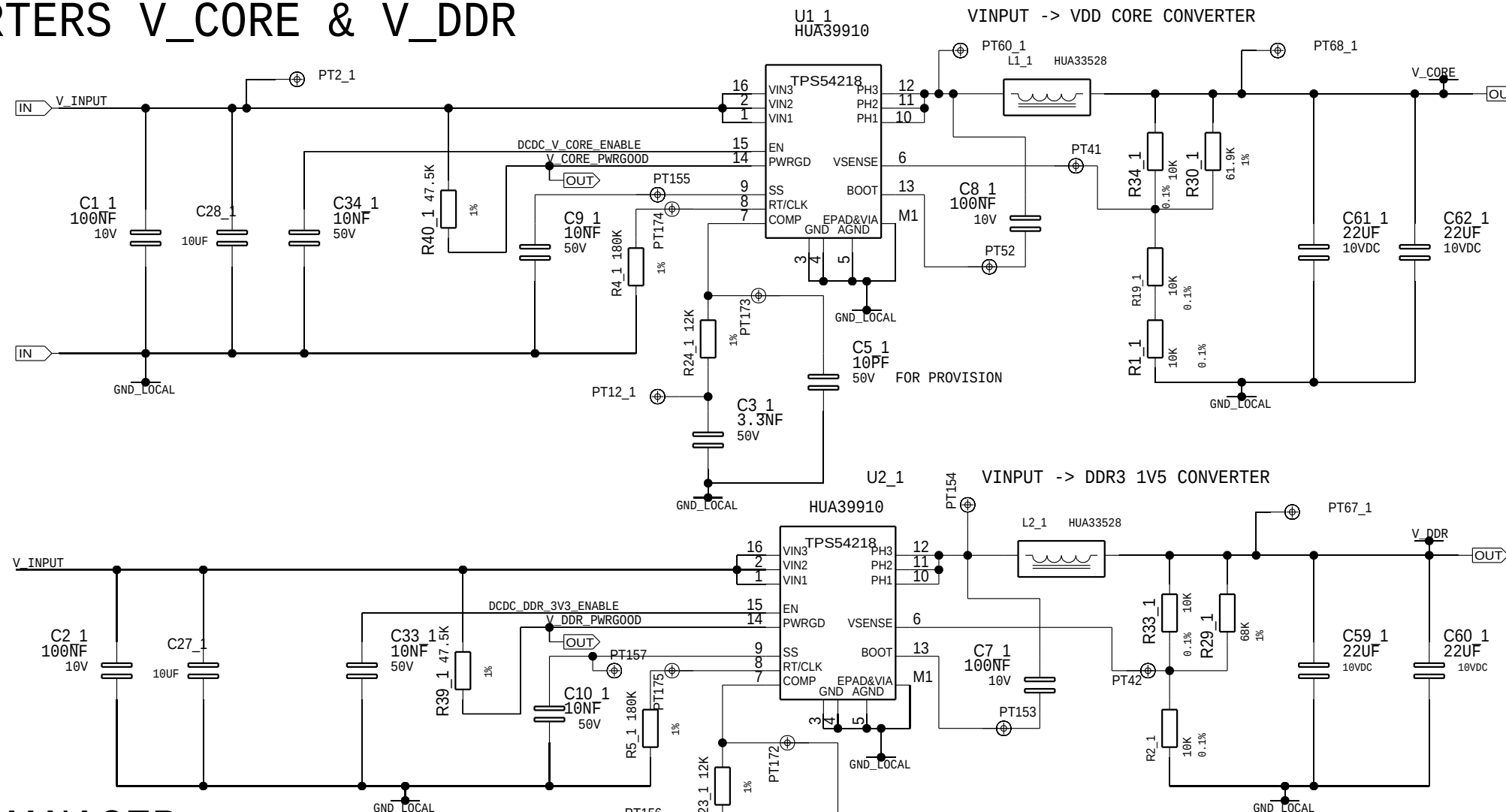
LCES POWER & GROUND INPUTS



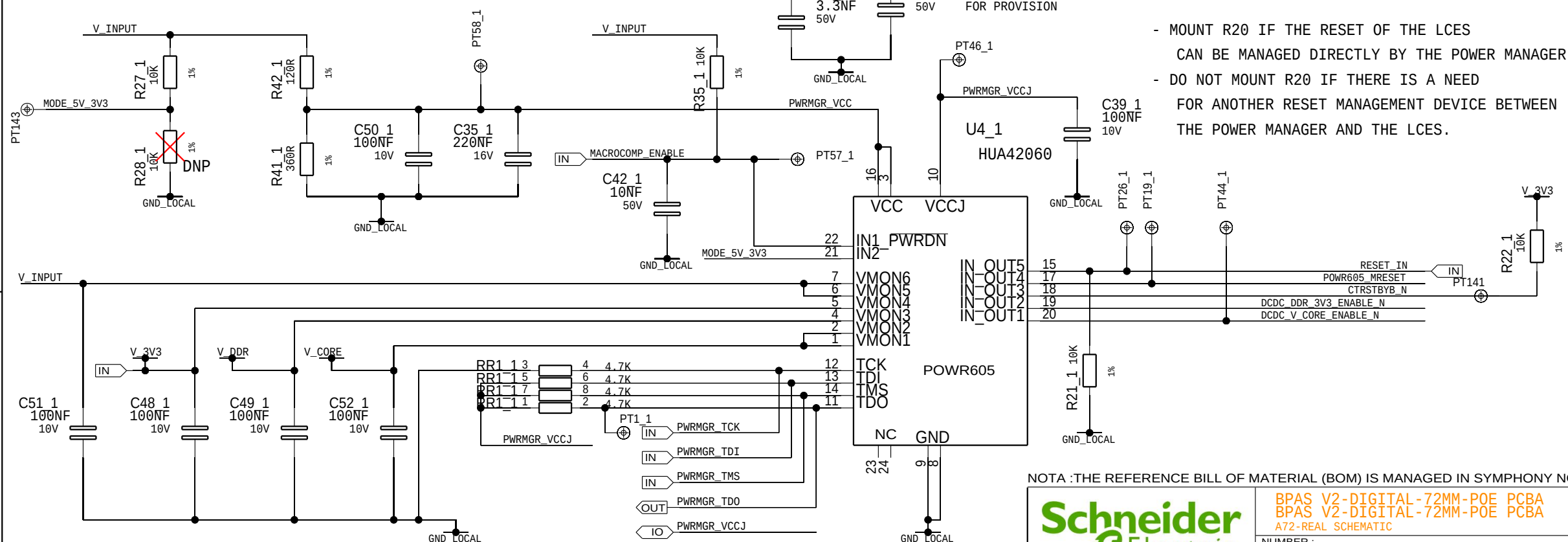
NOTA : THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

	BPAS V2-DIGITAL-72MM-POE PCBA			BLOCK NAME = nha50460	REVISION 00	SHEET : 21/22
	BPAS V2-DIGITAL-72MM-POE PCBA					
	A72-REAL SCHEMATIC					
	NUMBER : PKR4614001					
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CONVERTERS V_CORE & V_DDR



POWER MANAGER



IN 5V INPUT MODE, THE INPUT VOLTAGE TOLERANCE IS 5V+/-10%
IN 3V INPUT MODE, THE INPUT VOLTAGE TOLERANCE IS 3V3+/-5%

- MOUNT R20 IF THE RESET OF THE LCES CAN BE MANAGED DIRECTLY BY THE POWER MANAGER
- DO NOT MOUNT R20 IF THERE IS A NEED FOR ANOTHER RESET MANAGEMENT DEVICE BETWEEN THE POWER MANAGER AND THE LCES.

NOTA :THE REFERENCE BILL OF MATERIAL (BOM) IS MANAGED IN SYMPHONY NOT IN THE SCHEMATIC .

Schneider Electric

BPAS V2-DIGITAL-72MM-POE PCBA
BPAS V2-DIGITAL-72MM-POE PCBA
A72-REAL SCHEMATIC
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22/22